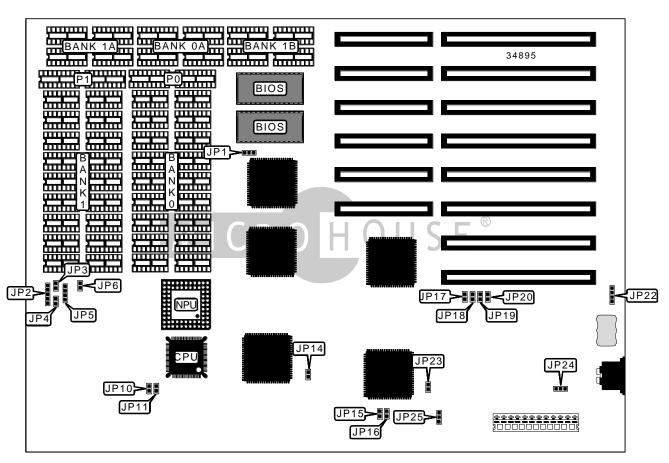
FUJIKAMA USA, INC. 80386SX VLSI

Mainboard **Device Type Processor** 80386SX **Processor Speed** 16MHz **Chip Set** VLSI **Video Chip Set** None 4MB **Maximum Onboard Memory Maximum Video Memory** None Cache None

BIOS AMI, Award, Phoenix Dimensions 330mm x 218mm

I/O OptionsNoneNPU Options80387SX



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CONNECTIONS				
Purpose Location Purpose Locat				
Power LED & keylock	JP2	Speaker	JP5	
Reset switch	JP3	Turbo switch	JP6	
Turbo LED	JP4	External battery	JP22	

USER CONFIGURABLE SETTINGS				
Function Label Position				
í BIOS type select 27256		JP1	Pins 1 & 2 closed	
BIOS type select 27128		JP1	Pins 2 & 3 closed	
í Factory configured - do not alter (sh	adow RAM)	JP14	Open	
í Power good signal detect from power	er supply	JP24	Pins 1 & 2 closed	
Power good signal detect from boar	d	JP24	Pins 2 & 3 closed	
Monitor type select CGA/EGA/VGA		JP25	Pins 1 & 2 closed	
Monitor type select monochrome		JP25	Pins 2 & 3 closed	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 0A	
512KB	None	None	(4) 44256	
640KB	None	None	(4) 44256	
1MB	None	None	(4) 44256	
2MB	(18) 41100	None	None	
4MB	(18) 41100	(18) 41100	None	

DRAM CONFIGURATION (CON'T)				
Size	Bank 1A	Bank 1B	P 0	P 1
512KB	None	None	(2) 41256	None
640KB	(4) 4464	None	(2) 41256	(2) 4164
1MB	None	(4) 44256	(2) 41256	(2) 41256
2MB	None	None	None	None
4MB	None	None	None	None

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DRAM JUMPER CONFIGURATION				
Size	JP15	JP16	JP23	
512KB	Closed	Open	Closed	
640KB	Open	Closed	Closed	
1MB	Open	Closed	Closed	
2MB	Closed	Open	Open	
4MB	Open	Closed	Open	

DRAM WAIT STATE SELECTION				
Wait state JP10 JP11				
0	Closed	Closed		
1	Open	Open		

KEYBOARD PIN CONTROLLER SELECTION				
Pin	JP17	JP18	JP19	JP20
23	Closed	Open	Open	Open
27	Open	Closed	Open	Open
30	Open	Open	Closed	Open
32	Open	Open	Open	Closed