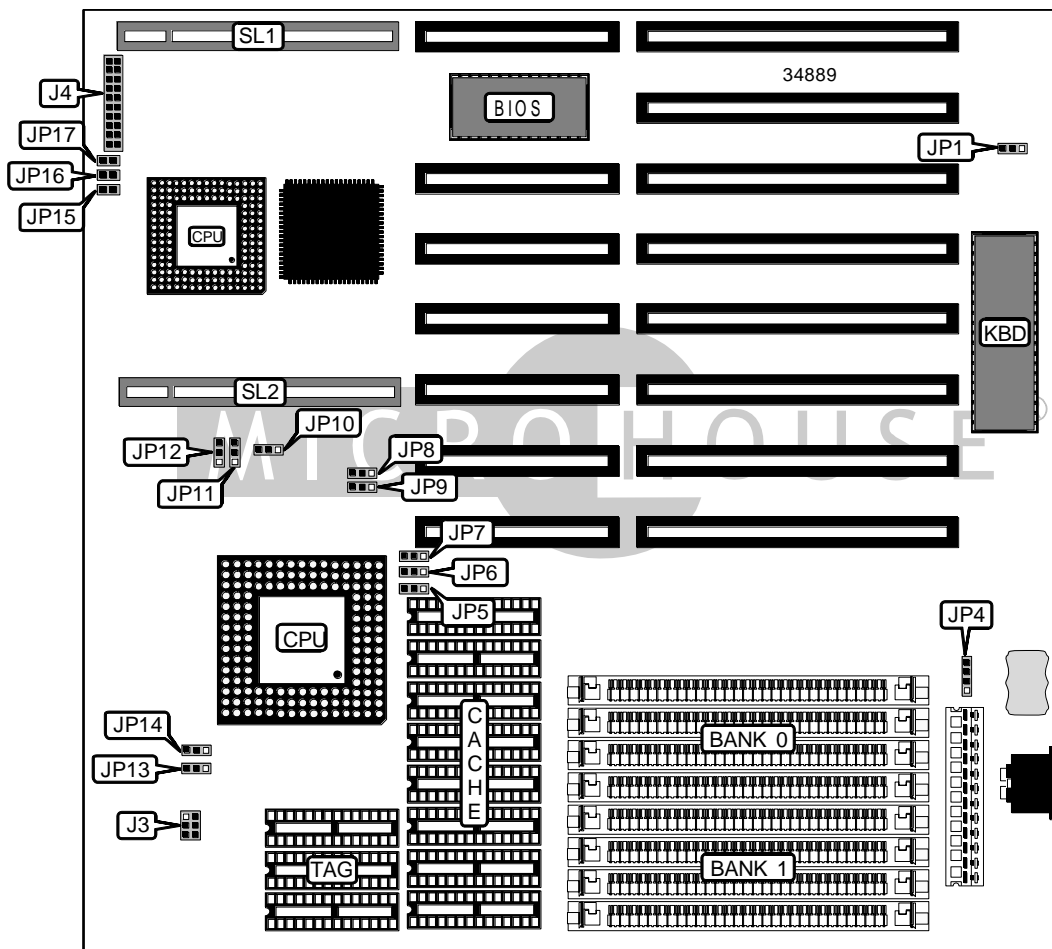


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Device Type	Mainboard
Processor	80386DX/CX486DLC/80486SX/80486DX/80486DX2
Processor Speed	33/40/50(internal)/50/66(internal)MHz
Chip Set	EFAR
Video Chip Set	None
Maximum Onboard Memory	32MB
Maximum Video Memory	None
Cache	128/256KB
BIOS	AMI
Dimensions	250mm x 220mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J4/pins 1 - 5	Reset switch	J4/pins 19 & 20
Speaker	J4/pins 7 - 10	External battery	JP4
Turbo LED	J4/pins 12 & 13	32-bit VESA local bus slots	SL1 & SL2
Turbo switch	J4/pins 15 - 17		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP1	Pins 2 & 3 closed
CMOS memory clear	JP1	Pins 1 & 2 closed
Battery type select external	JP1	Open
í Factory configured - do not alter	JP10	Unidentified
í Factory configured - do not alter	JP11	Unidentified
í Factory configured - do not alter	JP12	Unidentified

SIMM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	None	(3) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(3) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP5	JP6	JP7
128KB	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
256KB	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

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CPU SPEED SELECTION		
Speed	J3	JP14
33MHz	Pins 1 & 2 closed	Pins 1 & 2 closed
40MHz	Open	Pins 1 & 2 closed
50iMHz	Pins 3 & 4 closed	Pins 1 & 2 closed
50MHz	Pins 3 & 4 closed	Pins 2 & 3 closed
66iMHz	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU TYPE SELECTION						
Type	JP8	JP9	JP13	JP15	JP16	JP17
80386DX	2 & 3	2 & 3	1 & 2	Open	Open	Open
CX486DLC	1 & 2	1 & 2	1 & 2	Open	Open	Open
80486SX	1 & 2	1 & 2	2 & 3	Open	Closed	Closed
80486DX	1 & 2	1 & 2	1 & 2	Open	Closed	Closed
80486DX2	1 & 2	1 & 2	1 & 2	Open	Closed	Closed

Note: Pins designated should be in the closed position.