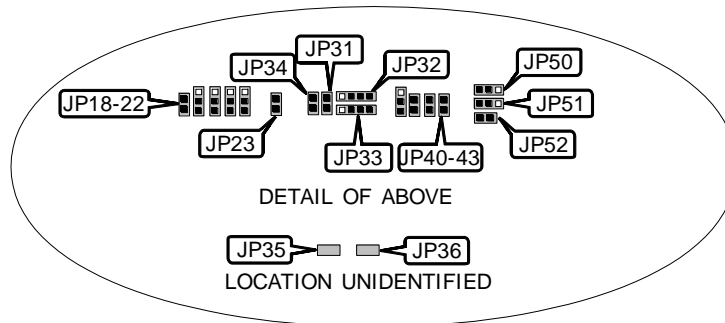
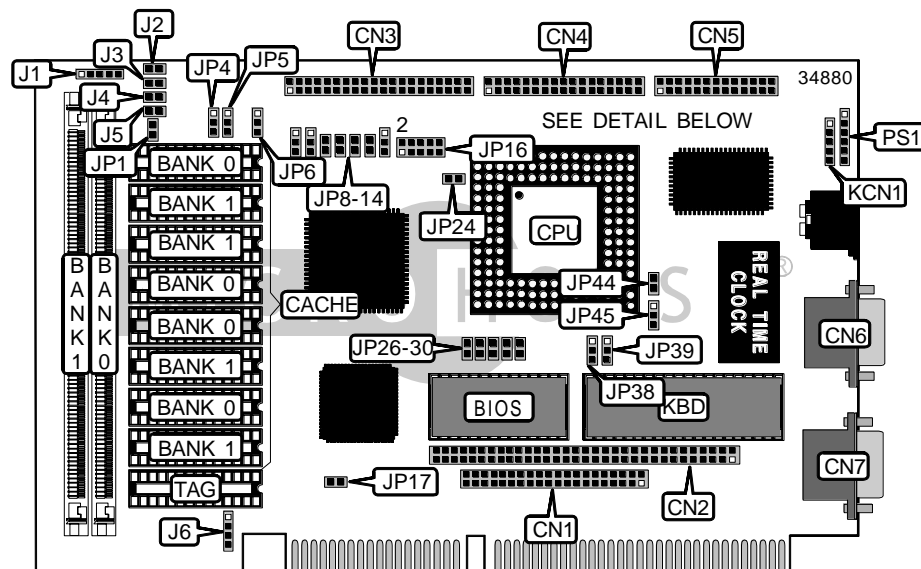


# LANNER ELECTRONICS, INC.

## AP-4100AA

<b>Device Type</b>	Mainboard
<b>Processor</b>	80486SX/ODP486/80486DX/CX486DX2/AM486DX2/AM486DE2/ 80486DX2/CX486DX4/AM486DX4/80486DX4/P24T/CX 5X86/ AM 5X86
<b>Processor Speed</b>	25/33/40/50(internal)/50/66(internal)/80(internal)/100(internal)/ 120(internal)/150(internal)/160(internal)MHz
<b>Chip Set</b>	ALI
<b>Video Chip Set</b>	None
<b>Maximum Onboard Memory</b>	128MB
<b>Maximum Video Memory</b>	None
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	185mm x 122mm
<b>I/O Options</b>	Floppy drive interface, IDE interface, parallel port, serial ports (2), PC/104 connectors (2)
<b>NPU Options</b>	None



Continued on next page . . .

LANNER ELECTRONICS, INC.  
 AP - 4100A

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
PC/104 connector (8-bit)	CN1	IDE interface LED	J2
PC/104 connector (16-bit)	CN2	Turbo LED	J3
IDE interface	CN3	Turbo switch	J4
Floppy drive interface	CN4	Reset switch	J5
Parallel port	CN5	Speaker	J6
Serial port 1	CN6	Auxiliary keyboard connector	KCN1
Serial port 2	CN7	Power connector	PS1
Power LED & keylock	J1		

USER CONFIGURABLE SETTINGS			
Function	Label	Position	
í Serial port 1 COM select COM1	JP8	Pins 1 & 2 closed	
Serial port 1 COM select COM3	JP8	Pins 2 & 3 closed	
í Factory configured - do not alter	JP10	Open	
í Serial port 2 COM select COM2	JP14	Pins 1 & 2 closed	
Serial port 2 COM select COM4	JP14	Pins 2 & 3 closed	
IDE interface enabled	JP18	Open	
IDE interface disabled	JP18	Closed	
í Factory configured - do not alter	JP20	Pins 1 & 2 closed	
í Factory configured - do not alter	JP31	Closed	
í Factory configured - do not alter	JP34	Closed	
í Factory configured - do not alter	JP35	Open	
í Power good from board	JP36	Pins 1 & 2 closed	
Power good from power supply	JP36	Pins 2 & 3 closed	
í Factory configured - do not alter	JP41	Closed	
Floppy drive interface enabled	JP42	Open	
Floppy drive interface disabled	JP42	Closed	
Serial port 1 enabled	JP43	Open	
Serial port 1 disabled	JP43	Closed	
Serial port 2 enabled	JP52	Open	
Serial port 2 disabled	JP52	Closed	

SIMM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(1) 256K x 36	None
2MB	(1) 512K x 36	None
2MB	(1) 256K x 36	(1) 256K x 36
4MB	(1) 1M x 36	None

Continued on next page...

LANNER ELECTRONICS, INC.  
 AP-4100AA

... continued from previous page

SIMM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
4MB	(1) 512K x 36	(1) 512K x 36
5MB	(1) 1M x 36	(1) 256K x 36
6MB	(1) 1M x 36	(1) 512K x 36
8MB	(1) 2M x 36	None
8MB	(1) 1M x 36	(1) 1M x 36
9MB	(1) 2M x 36	(1) 256K x 36
10MB	(1) 2M x 36	(1) 512K x 36
12MB	(1) 2M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None
16MB	(1) 2M x 36	(1) 2M x 36
17MB	(1) 4M x 36	(1) 256K x 36
18MB	(1) 4M x 36	(1) 512K x 36
20MB	(1) 4M x 36	(1) 1M x 36
24MB	(1) 4M x 36	(1) 2M x 36
32MB	(1) 8M x 36	None
32MB	(1) 4M x 36	(1) 4M x 36
33MB	(1) 8M x 36	(1) 256K x 36
34MB	(1) 8M x 36	(1) 512K x 36
36MB	(1) 8M x 36	(1) 1M x 36
40MB	(1) 8M x 36	(1) 2M x 36
48MB	(1) 8M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36
64MB	(1) 16M x 36	None
65MB	(1) 16M x 36	(1) 256K x 36
66MB	(1) 16M x 36	(1) 512K x 36
68MB	(1) 16M x 36	(1) 1M x 36
72MB	(1) 16M x 36	(1) 2M x 36
80MB	(1) 16M x 36	(1) 4M x 36
96MB	(1) 16M x 36	(1) 8M x 36
128MB	(1) 16M x 36	(1) 16M x 36

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	Unidentified
128KB	(4) 32K x 8	None	Unidentified
256KB	(4) 32K x 8	(4) 32K x 8	Unidentified

Continued on next page...

LANNER ELECTRONICS, INC.  
 AP-4100A

... continued from previous page

CACHE JUMPER CONFIGURATION				
Size	JP1	JP4	JP5	JP17
64KB	Open	Pins 2 & 3 closed	Pins 2 & 3 closed	Open
128KB	Closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Open
256KB	Closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Closed

CPU SPEED SELECTION					
Speed	JP6	JP9	JP11	JP12	JP13
25MHz	1 & 2	1 & 2	Open	Open	Closed
33MHz	1 & 2	1 & 2	Closed	Open	Closed
40MHz	2 & 3	2 & 3	Closed	Closed	Open
50iMHz	1 & 2	1 & 2	Open	Open	Closed
50MHz	2 & 3	2 & 3	Open	Open	Closed
66iMHz	1 & 2	1 & 2	Closed	Open	Closed
80iMHz	2 & 3	2 & 3	Closed	Closed	Open
100iMHz	1 & 2	1 & 2	Closed	Open	Closed
120iMHz	2 & 3	2 & 3	Closed	Closed	Open
133iMHz	1 & 2	1 & 2	Closed	Open	Closed
150iMHz	2 & 3	2 & 3	Open	Open	Closed
160iMHz	2 & 3	2 & 3	Closed	Closed	Open

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION						
Type	JP19	JP21	JP22	JP23	JP24	JP30
80486SX	Open	2 & 3	Open	Open	Open	Open
ODP486	Open	2 & 3	2 & 3	Open	Open	Open
80486DX	Open	2 & 3	Open	Open	Open	Open
CX 486DX2	Open	2 & 3	1 & 2	Closed	Closed	Open
AM486DX2	1 & 2	2 & 3	Open	Open	Open	Open
AM486DE2	1 & 2	2 & 3	Open	Open	Open	Open
AM486DX2 SV8B	2 & 3	2 & 3	2 & 3	Open	Open	Open
80486DX2	Open	2 & 3	Open	Open	Open	Open
CX 486DX4	Open	2 & 3	1 & 2	Closed	Closed	Open
AM486DX4 NV8T	Open	2 & 3	Open	Open	Open	Open
AM486DX4 SV8B	2 & 3	2 & 3	2 & 3	Open	Open	Open
80486DX4	2 & 3	2 & 3	2 & 3	Open	Open	Open
P24T	Open	1 & 2	Open	Open	Open	Closed
CX 5X86	Open	2 & 3	2 & 3	Open	Closed	Open
AM 5X86	2 & 3	2 & 3	2 & 3	Open	Open	Open

Note: Pins designated should be in the closed position.

Continued on next page. . .

LANNER ELECTRONICS, INC.  
 AP-4100AA

... continued from previous page

CPU TYPE SELECTION (CON'T)					
Type	JP32	JP33	JP40	JP44	JP45
80486SX	Open	2 & 3	Open	Open	Open
ODP486	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2	Closed	Open
80486DX	Open	1 & 2, 3 & 4	2 & 3	Open	Open
CX 486DX2	2 & 3	1 & 2, 3 & 4	2 & 3	Open	1 & 2
AM486DX2	Open	1 & 2, 3 & 4	2 & 3	Open	Open
AM486DE2	Open	1 & 2, 3 & 4	2 & 3	Open	Open
AM486DX2 SV8B	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	Closed	2 & 3
80486DX2	Open	1 & 2, 3 & 4	2 & 3	Open	Open
CX 486DX4	2 & 3	1 & 2, 3 & 4	2 & 3	Open	1 & 2
AM486DX4 NV8T	Open	1 & 2, 3 & 4	2 & 3	Open	Open
AM486DX4 SV8B	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	Closed	2 & 3
80486DX4	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	Closed	Open
P24T	1 & 2	1 & 2, 3 & 4	1 & 2	Closed	Open
CX 5X86	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	Closed	Open
AM 5X86	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	Closed	2 & 3

Note: Pins designated should be in the closed position.

PARALLEL PORT SELECTION		
Setting	JP50	JP51
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed
í SPP	Pins 2 & 3 closed	Pins 1 & 2 closed
EPP	Pins 1 & 2 closed	Pins 2 & 3 closed
ECP	Pins 1 & 2 closed	Pins 1 & 2 closed

SERIAL PORT 2 INTERRUPT SELECTION	
IRQ	JP16
3	Pins 3 & 4 closed
10	Pins 5 & 6 closed
11	Pins 6 & 8 closed
12	Pins 9 & 10 closed

SERIAL PORT 1 INTERRUPT SELECTION	
IRQ	JP16
4	Pins 1 & 2 closed
10	Pins 5 & 7 closed
11	Pins 7 & 8 closed
12	Pins 7 & 9 closed

Continued on next page. . .

LANNER ELECTRONICS, INC.  
 AP-4100AA

... continued from previous page

WATCHDOG TIMER SELECTION				
Time out	JP26	JP27	JP28	JP29
.5 sec	Closed	Open	Open	Open
1 sec	Open	Closed	Open	Open
2 sec	Open	Open	Closed	Open
4 sec	Open	Open	Open	Closed

WATCHDOG TIMER FUNCTION SELECTION	
Setting	JP38
í Active by reset	Pins 1 & 2 closed
Active by NMI	Pins 2 & 3 closed
Disabled	Open