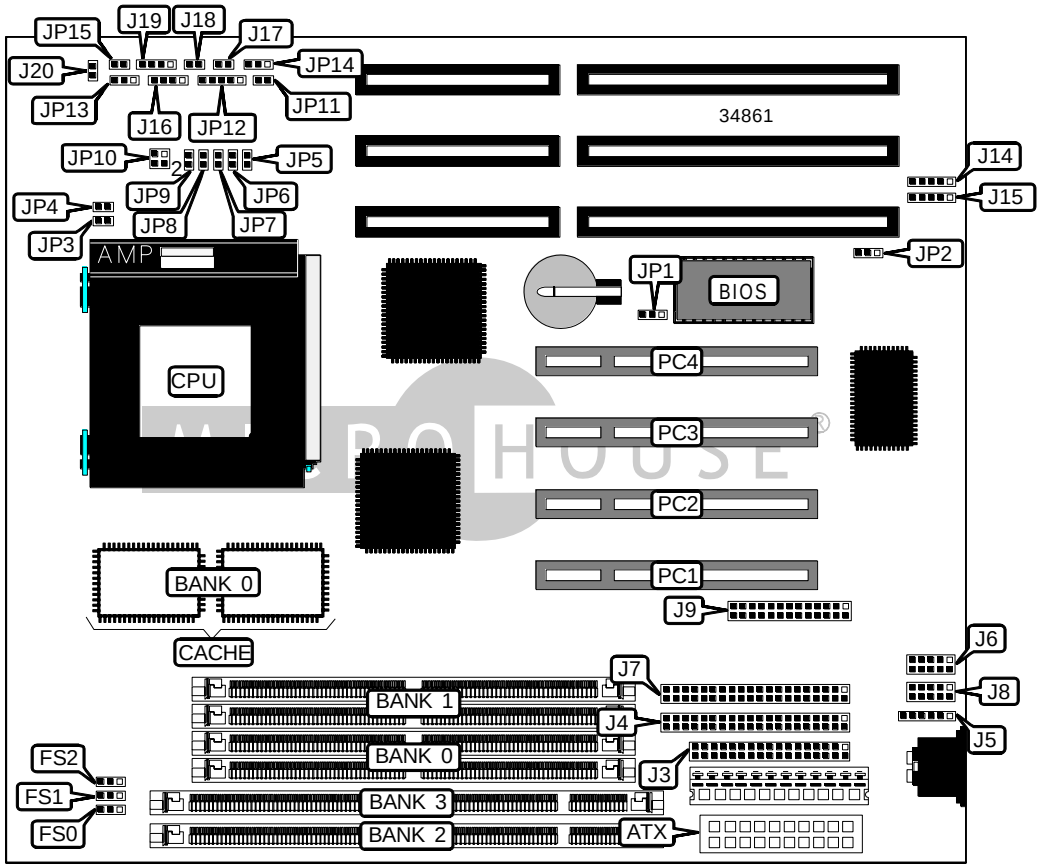
SKYWELL TECHNOLOGY CORPORATION, LTD.
V T 5 8 6 T X

Device Type	Mainboard
Processor	CX 6X86/CX 6X86L/AM K5/AM K6/Pentium
Processor Speed	75/90/100/120/133/150/166/200/233MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	256MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB (located on Pentium II CPU)
BIOS	Award
Dimensions	241mm x 221mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, USB connectors (2), ATX power connector
NPU Options	None



Continued on next page. . .

SKYWELL TECHNOLOGY CORPORATION, LTD.
V T 5 8 6 T X

. . . continued from previous page

CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
ATX power connector	ATX	Reset switch	J17
Floppy drive interface	J3	IDE interface LED	J18
IDE interface 2	J4	IR connector	J19
PS/2 mouse interface	J5	Chassis fan power	J20
Serial port 2	J6	Turbo LED	JP11
IDE interface 1	J7	Power LED & keylock	JP12
Serial port 1	J8	Green PC connector	JP13
Parallel port	J9	Turbo switch	JP14
USB connector	J14	Soft off power supply	JP15
USB connector	J15	32-bit PCI slots	PC1 - PC4
Speaker	J16		

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í CMOS memory normal operation	JP1	Pins 1 & 2 closed
CMOS memory clear	JP1	Pins 2 & 3 closed
Flash BIOS voltage select 12v	JP2	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP2	Pins 1 & 2 closed

SIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.		

Continued on next page. . .

. . . continued from previous page

DIMM CONFIGURATION		
Size	**Bank 2**	**Bank 3**
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64

CACHE CONFIGURATION	
Size	**Bank 0**
256KB	(2) 32K x 32
512KB	(2) 64K x 32

CPU SPEED SELECTION (CX 6X86)							
CPU speed	**Clock speed**	**Multiplier**	**FS0**	**FS1**	**FS2**	**JP3**	**JP4**
120MHz	50MHz	2x	1 & 2	1 & 2	1 & 2	Open	Closed
133MHz	55MHz	2x	2 & 3	1 & 2	1 & 2	Open	Closed
150MHz	60MHz	2x	1 & 2	1 & 2	2 & 3	Open	Closed
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (CX 6X86L)							
CPU speed	**Clock speed**	**Multiplier**	**FS0**	**FS1**	**FS2**	**JP3**	**JP4**
166MHz	66MHz	2x	1 & 2	2 & 3	1 & 2	Open	Closed
200MHz	75MHz	2x	2 & 3	1 & 2	2 & 3	Open	Closed
Note: Pins designated should be in the closed position.							

Continued on next page. . .

. . . continued from previous page

CPU SPEED SELECTION (AM K5)							
CPU speed	**Clock speed**	**Multiplier**	**FS0**	**FS1**	**FS2**	**JP3**	**JP4**
75MHz	50MHz	1.5x	1 & 2	1 & 2	1 & 2	Open	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	2 & 3	Open	Open
100MHz	66MHz	1.5x	1 & 2	2 & 3	1 & 2	Open	Open
133MHz	66MHz	2x	1 & 2	2 & 3	1 & 2	Open	Closed
166MHz	66MHz	2.5x	1 & 2	2 & 3	1 & 2	Closed	Closed
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AM K6)							
CPU speed	**Clock speed**	**Multiplier**	**FS0**	**FS1**	**FS2**	**JP3**	**JP4**
166MHz	66MHz	2.5x	1 & 2	2 & 3	1 & 2	Closed	Closed
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	Closed	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	**Clock speed**	**Multiplier**	**FS0**	**FS1**	**FS2**	**JP3**	**JP4**
75MHz	50MHz	1.5x	1 & 2	1 & 2	1 & 2	Open	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	2 & 3	Open	Open
100MHz	66MHz	1.5x	1 & 2	2 & 3	1 & 2	Open	Open
120MHz	60MHz	2x	1 & 2	1 & 2	2 & 3	Open	Closed
133MHz	66MHz	2x	1 & 2	2 & 3	1 & 2	Open	Closed
150MHz	60MHz	2.5x	1 & 2	1 & 2	2 & 3	Closed	Closed
166MHz	66MHz	2.5x	1 & 2	2 & 3	1 & 2	Closed	Closed
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	Closed	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL MMX)							
CPU speed	**Clock speed**	**Multiplier**	**FS0**	**FS1**	**FS2**	**JP3**	**JP4**
166MHz	66MHz	2.5x	1 & 2	2 & 3	1 & 2	Closed	Closed
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	Closed	Open
233MHz	66MHz	3.5x	1 & 2	2 & 3	1 & 2	Open	Open
Note: Pins designated should be in the closed position.							

CPU VOLTAGE SELECTION						
Voltage	**JP5**	**JP6**	**JP7**	**JP8**	**JP9**	**JP10**
2.8v	Open	Open	Open	Open	Closed	3 & 4
2.9v	Open	Open	Open	Closed	Open	3 & 4
3.2v	Open	Open	Closed	Open	Open	3 & 4
3.4v	Open	Closed	Open	Open	Open	1 & 2
3.5v	Closed	Open	Open	Open	Open	1 & 2
Note: Pins designated should be in the closed position.						