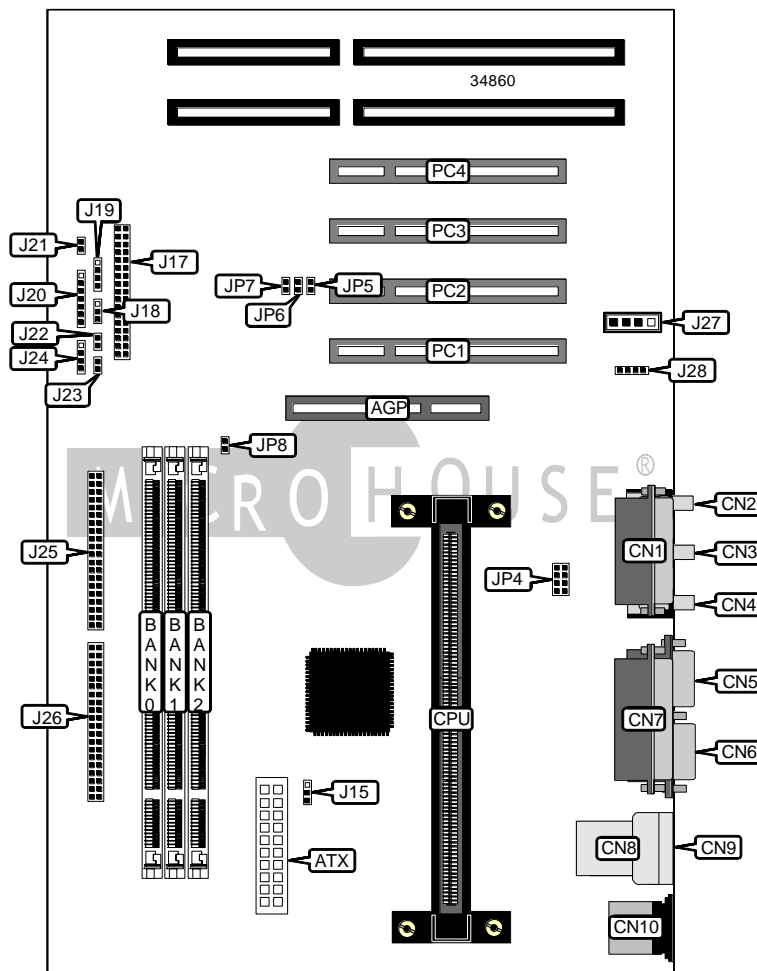


BCM ADVANCED RESEARCH, INC.

K R 6 3 0

Device Type	Mainboard
Processor	Pentium II
Processor Speed	233/266/333/366MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	384MB (EDO & SDRAM supported)
Maximum Video Memory	None
Cache	256/512KB (located on Pentium II CPU)
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), green PC connector, floppy drive interface, game/MIDI interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connectors (2), line in, line out, microphone in, ATX power connector, AGP slot
NPU Options	None



Continued on next page . . .

BCM ADVANCED RESEARCH, INC.
KR630

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
AGP slot	AGP	Floppy drive interface	J17
ATX power connector	ATX	Power LED	J18
Game/MIDI port	CN1	IDE interface LED	J19
Line in	CN2	IR connector	J20
Microphone in	CN3	Green PC connector	J21
Line out	CN4	Soft off power supply	J22
Serial port 2	CN5	Reset switch	J23
Serial port 1	CN6	Speaker	J24
Parallel port	CN7	IDE interface 1	J25
USB connector 1	CN8	IDE interface 2	J26
USB connector 2	CN9	Audio in - CD-ROM	J27
PS/2 mouse port	CN10	Modem connector	J28
Chassis fan power	J15	32-bit PCI slots	PC1 - PC4

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	JP5	Unidentified
í CMOS memory normal operation	JP6	Open
CMOS memory clear	JP6	Closed
í Factory configured - do not alter	JP7	Unidentified

DIMM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64	None
40MB	(1) 2M x 64	(1) 2M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
56MB	(1) 4M x 64	(1) 2M x 64	(1) 1M x 64
64MB	(1) 8M x 64	None	None

Continued on next page...

BCM ADVANCED RESEARCH, INC.
K R 6 3 0

... continued from previous page

DIMM CONFIGURATION (CON'T)			
Size	Bank 0	Bank 1	Bank 2
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64	None
72MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64
88MB	(1) 8M x 64	(1) 2M x 64	(1) 1M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
104MB	(1) 8M x 64	(1) 4M x 64	(1) 1M x 64
112MB	(1) 8M x 64	(1) 4M x 64	(1) 2M x 64
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64	None
136MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64
152MB	(1) 16M x 64	(1) 2M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64	None
160MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64
168MB	(1) 16M x 64	(1) 4M x 64	(1) 1M x 64
176MB	(1) 16M x 64	(1) 4M x 64	(1) 2M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
200MB	(1) 16M x 64	(1) 8M x 64	(1) 1M x 64
208MB	(1) 16M x 64	(1) 8M x 64	(1) 2M x 64
224MB	(1) 16M x 64	(1) 8M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

Note: Board accepts SDRAM memory.

CACHE CONFIGURATION
Note: 256KB/512KB cache is located on the Pentium II CPU.

Continued on next page...

BCM ADVANCED RESEARCH, INC.
K R 6 3 0

... continued from previous page

CPU SPEED SELECTION				
CPU speed	Clock speed	Multiplier	JP4	JP8
233MHz	66MHz	3.5x	1 & 2, 5 & 6	Open
266MHz	66MHz	4x	3 & 4, 5 & 6, 7 & 8	Open
300MHz	66MHz	4.5x	3 & 4, 5 & 6	Open
333MHz	66MHz	5x	5 & 6, 7 & 8	Open
366MHz	66MHz	5.5x	5 & 6	Open

Note: Pins designated should be in the closed position.