KADATCO CO., LTD.
AM-436TX
Device Type
Processor

Processor Speed
Chip Set
Video Chip Set
Maximum Onboard Memory
Maximum Video Memory
Cache
BIOS
Dimensions
I/O Options

NPU Options

Mainboard
CX 6X86/IBM 6X86/CX 6X86L/IBM 6X86L/CX 686MX/IBM 6X86MX/ AM K5/AM K6/Pentium
90/100/120/133/150/166/180/200/233MHz
Intel
None
256MB (EDO \& SDRAM supported)
None
256/512KB
Award
$254 \mathrm{~mm} \times 218 \mathrm{~mm}$
32-bit PCI slots (3), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), USB connector, ATX power connector None


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## AM-436TX

| Cocation |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose |  | ATX | IDE interface 1 |
| ATX power connector | P1 | IDE interface 2 | P8 |
| Power LED \& keylock | P2 | USB connector | P9 |
| Speaker | P3 | Serial port | P10 |
| IDE interface LED | P4 | Serial port | P14 |
| Soft off power supply | P5 | Parallel port | P15 |
| Reset switch | P6 | PS/2 mouse interface | P16 |
| CPU fan power | P7 | 32-bit PCI slots | P17 |
| Floppy drive interface | PC1 - PC3 |  |  |


| USER CONFIGURABLE SETTINGS |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Function |  |  |  | Label | Position |
| í Modem ring select COM2 | JP12 | Pins 2 \& 3 closed |  |  |  |
| Modem ring select COM1 | JP12 | Pins 1 \& 2 closed |  |  |  |
| í CMOS memory normal operation | JP17 | Pins 1 \& 2 closed |  |  |  |
| CMOS memory clear | JP17 | Pins 2 \& 3 closed |  |  |  |


| SIMM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 |
| 8MB | (2) $1 \mathrm{M} \times 36$ | None |
| 16 MB | (2) $2 \mathrm{M} \times 36$ | None |
| 16MB | (2) $1 \mathrm{M} \times 36$ | (2) $1 \mathrm{M} \times 36$ |
| 24MB | (2) $2 \mathrm{M} \times 36$ | (2) $1 \mathrm{M} \times 36$ |
| 32 MB | (2) $4 \mathrm{M} \times 36$ | None |
| 32 MB | (2) $2 \mathrm{M} \times 36$ | (2) $2 \mathrm{M} \times 36$ |
| 40MB | (2) $4 \mathrm{M} \times 36$ | (2) $1 \mathrm{M} \times 36$ |
| 48 MB | (2) $4 \mathrm{M} \times 36$ | (2) $2 \mathrm{M} \times 36$ |
| 64 MB | (2) $8 \mathrm{M} \times 36$ | None |
| 64 MB | (2) $4 \mathrm{M} \times 36$ | (2) $4 \mathrm{M} \times 36$ |
| 72 MB | (2) $8 \mathrm{M} \times 36$ | (2) $1 \mathrm{M} \times 36$ |
| 80MB | (2) $8 \mathrm{M} \times 36$ | (2) $2 \mathrm{M} \times 36$ |
| 96MB | (2) $8 \mathrm{M} \times 36$ | (2) $4 \mathrm{M} \times 36$ |
| 128 MB | (2) $8 \mathrm{M} \times 36$ | (2) $8 \mathrm{M} \times 36$ |
| 128MB | (2) $16 \mathrm{M} \times 36$ | None |
| 136MB | (2) $16 \mathrm{M} \times 36$ | (2) $1 \mathrm{M} \times 36$ |
| 144MB | (2) $16 \mathrm{M} \times 36$ | (2) $2 \mathrm{M} \times 36$ |
| 160MB | (2) $16 \mathrm{M} \times 36$ | (2) $4 \mathrm{M} \times 36$ |
| 192MB | (2) $16 \mathrm{M} \times 36$ | (2) $8 \mathrm{M} \times 36$ |
| 256MB | (2) $16 \mathrm{M} \times 36$ | (2) $16 \mathrm{M} \times 36$ |
| Note: Board accepts |  |  |

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| DIMM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 2 | Bank 3 |
| 8MB | (1) $1 \mathrm{M} \times 64$ | None |
| 16MB | (1) $2 \mathrm{M} \times 64$ | None |
| 16MB | (1) $1 \mathrm{M} \times 64$ | (1) $1 \mathrm{M} \times 64$ |
| 24 MB | (1) $2 \mathrm{M} \times 64$ | (1) $1 \mathrm{M} \times 64$ |
| 32 MB | (1) $4 \mathrm{M} \times 64$ | None |
| 32 MB | (1) $2 \mathrm{M} \times 64$ | (1) $2 \mathrm{M} \times 64$ |
| 40MB | (1) $4 \mathrm{M} \times 64$ | (1) $1 \mathrm{M} \times 64$ |
| 48 MB | (1) $4 \mathrm{M} \times 64$ | (1) $2 \mathrm{M} \times 64$ |
| 64 MB | (1) $8 \mathrm{M} \times 64$ | None |
| 64 MB | (1) $4 \mathrm{M} \times 64$ | (1) $4 \mathrm{M} \times 64$ |
| 72 MB | (1) $8 \mathrm{M} \times 64$ | (1) $1 \mathrm{M} \times 64$ |
| 80MB | (1) $8 \mathrm{M} \times 64$ | (1) $2 \mathrm{M} \times 64$ |
| 96 MB | (1) $8 \mathrm{M} \times 64$ | (1) $4 \mathrm{M} \times 64$ |
| 128 MB | (1) $16 \mathrm{M} \times 64$ | None |
| 128 MB | (1) $8 \mathrm{M} \times 64$ | (1) $8 \mathrm{M} \times 64$ |
| 136 MB | (1) $16 \mathrm{M} \times 64$ | (1) $1 \mathrm{M} \times 64$ |
| 144MB | (1) $16 \mathrm{M} \times 64$ | (1) $2 \mathrm{M} \times 64$ |
| 160MB | (1) $16 \mathrm{M} \times 64$ | (1) $4 \mathrm{M} \times 64$ |
| 192MB | (1) $16 \mathrm{M} \times 64$ | (1) $8 \mathrm{M} \times 64$ |
| 256MB | (1) $16 \mathrm{M} \times 64$ | (1) $16 \mathrm{M} \times 64$ |
| Note: Board accepts SDRAM memory. |  |  |


| DIMM VOLTAGE CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Voltage | JP8 | JP19 |
| $3.3 v$ | Pins $1 \& 2$ closed | Pins $1 \& 2$ closed |
| $5 v$ | Pins 2 \& 3 closed | Pins 2 \& 3 closed |

## CACHE CONFIGURATION

Note: The location of the $256 \mathrm{~KB} / 512 \mathrm{~KB}$ is unidentified.

| CPU SPEED SELECTION (CX 6X86) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| 133 MHz | 55 MHz | $2 x$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $1 \& 2$ | $1 \& 2$ | Open |
| 150 MHz | 60 MHz | $2 x$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | Open |
| 166 MHz | 66 MHz | $2 x$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |
| 200 MHz | 75 MHz | $3 x$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |

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|  | CPU SPEED SELECTION (IBM 6X86) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| 200 MHz | 75 MHz | $3 x$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |


| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 166 MHz | 66 MHz | 2.5 x | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |


| CPU SPEED SELECTION (IBM 6X86L) |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |  |
| 166 MHz | 66 MHz | $2.5 x$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |  |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |  |

CPU SPEED SELECTION (CX 6X86MX)

| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 166 MHz | 60 MHz | $2 x$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | Open |
| 200 MHz | 66 MHz | $3 x$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |
| 233 MHz | 75 MHz | 3.5 x | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |


| CPU SPEED SELECTION (IBM 6X86MX) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| 166 MHz | 60 MHz | 2x | 1 \& 2 | 2 \& 3 | 2 \& 3 | 1 \& 2 | 2 \& 3 | Open |
| 200 MHz | 66 MHz | 3 x | 2 \& 3 | 2 \& 3 | $2 \& 3$ | 2 \& 3 | 2 \& 3 | Open |
| 233 MHz | 75 MHz | $3.5 x$ | 2 \& 3 | 2 \& 3 | 2 \& 3 | 2 \& 3 | 1 \& 2 | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |


| CPU SPEED SELECTION (AM K5) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| 90 MHz | 60 MHz | $1.5 x$ | $1 \& 2$ | $1 \& 2$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | Open |
| 100 MHz | 66 MHz | $1.5 x$ | $2 \& 3$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |
| 120 MHz | 60 MHz | $2 x$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | Open |
| 133 MHz | 66 MHz | $2 x$ | $2 \& 3$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |
| 150 MHz | 60 MHz | $2.5 x$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | Open |
| 166 MHz | 66 MHz | $2.5 x$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |

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CPU SPEED SELECTION (AM K6)

| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 166 MHz | 66 MHz | $2.5 x$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |
| 200 MHz | 66 MHz | $3 x$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |
| 233 MHz | 66 MHz | 3.5 x | $2 \& 3$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |

CPU SPEED SELECTION (INTEL)

| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 90 MHz | 60 MHz | 1.5 x | $1 \& 2$ | $1 \& 2$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | Open |
| 100 MHz | 66 MHz | 1.5 x | $2 \& 3$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |
| 120 MHz | 60 MHz | $2 x$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | Open |
| 133 MHz | 66 MHz | $2 x$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |
| 150 MHz | 60 MHz | 2.5 x | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | Open |
| 166 MHz | 66 MHz | 2.5 x | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | $2 \& 3$ | Open |
| 180 MHz | 60 MHz | $3 x$ | $1 \& 2$ | $2 \& 3$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | Open |
| 200 MHz | 66 MHz | $3 x$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (INTEL MMX) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP3 | JP10 | JP11 | JP15 |
| 200 MHz | 66 MHz | $3 x$ | $2 \& 3$ | $2 \& 3$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |
| 233 MHz | 66 MHz | $3.5 x$ | $2 \& 3$ | $1 \& 2$ | $1 \& 2$ | $2 \& 3$ | $2 \& 3$ | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |  |  |  |

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| CPU VOLTAGE SELECTION |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Voltage | JP4 | JP5 | JP6 | JP7 |
| 2.0 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.1 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.2 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.3 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.4 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.5 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.6 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.7 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.8 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 2.9 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 3.0 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 3.1 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 3.2 v | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |
| 3.3 v | Pins 2 \& 3 closed | Pins 2 \& 3 closed | Pins 2 \& 3 closed | Pins 2 \& 3 closed |
| 3.4 v | Pins 2 \& 3 closed | Pins 2 \& 3 closed | Pins 2 \& 3 closed | Pins 2 \& 3 closed |
| 3.5 v | Pins 2 \& 3 closed | Pins 2 \& 3 closed | Pins 2 \& 3 closed | Pins 2 \& 3 closed |


| CPU VOLTAGE SELECTION (CON'T) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Voltage | JP9/pins 1 \& 2 | JP9/pins 3 \& 4 | JP9/pins 5 \& 6 | JP9/pins 7 \& 8 |
| 2.0 v | Open | Open | Open | Open |
| 2.1 v | Open | Open | Open | Closed |
| 2.2 v | Open | Open | Closed | Open |
| 2.3 v | Open | Open | Closed | Closed |
| 2.4 v | Open | Closed | Open | Open |
| 2.5 v | Open | Closed | Open | Closed |
| 2.6 v | Open | Closed | Closed | Open |
| 2.7 v | Open | Closed | Closed | Closed |
| 2.8 v | Closed | Open | Open | Open |
| 2.9 v | Closed | Open | Open | Closed |
| 3.0 v | Closed | Open | Closed | Open |
| 3.1 v | Closed | Open | Closed | Closed |
| 3.2 v | Closed | Closed | Open | Open |
| 3.3 v | Closed | Closed | Open | Closed |
| 3.4 v | Closed | Closed | Closed | Open |
| 3.5 v | Closed | Closed | Closed | Closed |

