ASUS COMPUTER INTERNATIONAL
P2L97-DS (REV. 1.03)

Device Type	Mainboard
Processor	Pentium II
Processor Speed	233/266/300MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO & SDRAM supported)
Maximum Video Memory	None
Cache	256/512KB (located on Pentium II CPU)
BIOS	Unidentified
Dimensions	305mm x 244mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), Narrow SCSI interface, Wide SCSI interface, parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connectors (2), ATX power connector, AGP slot
NPU Options	None

ASUS COMPUTER INTERNATIONAL
P 2 L 9 7 - D S (R E V. 1.0 3)

CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
AGP slot	AGP	Serial port 1	CN3
ATX power connector	ATX	USB connector 1	CN4
Alarm connector	C1	USB connector 2	CN5
IDE interface LED	C2	PS/2 mouse port	CN6
Speaker	C3/pins 1 – 4	Wide SCSI interface	CN7
Power LED & keylock	C3/pins 6 - 10	Narrow SCSI interface	CN8
Reset switch	C3/pins 11 & 12	Floppy drive interface	CN9
Soft off power supply	C3/pins 14 & 15	IDE interface 1	CN10
Green PC connector	C3/pins 16 & 17	IDE interface 2	CN11
Turbo LED	C3/pins 18 & 19	IR connector	IR1
Wake on LAN connector	C4	32-bit PCI slots	PC1 – PC4
CPU fan power	CFAN	Power supply fan	PFAN
Parallel port	CN1	Chassis fan power	SFAN
Serial port 2	CN2		

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í CMOS memory normal operation	RTC	Open
CMOS memory clear	RTC	Closed
í Factory configured - do not alter	SMB	Unidentified

DIMM CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
8MB	(1) 1M x 64	None	None	None
16MB	(1) 2M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
24MB	(1) 2M x 64	(1) 1M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None
32MB	(1) 4M x 64	None	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
40MB	(1) 4M x 64	(1) 1M x 64	None	None
48MB	(1) 4M x 64	(1) 2M x 64	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None	None	None

ASUS COMPUTER INTERNATIONAL
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DIMM CONFIGURATION (CON'T)				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
64MB	(1) 4M x 64	(1) 4M x 64	None	None
72MB	(1) 8M x 64	(1) 1M x 64	None	None
80MB	(1) 8M x 64	(1) 2M x 64	None	None
96MB	(1) 8M x 64	(1) 4M x 64	None	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
128MB	(1) 16M x 64	None	None	None
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
136MB	(1) 16M x 64	(1) 1M x 64	None	None
144MB	(1) 16M x 64	(1) 2M x 64	None	None
152MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 4M x 64	None	None
176MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 16M x 64	(1) 8M x 64	None	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
224MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 32M x 64	None	None	None
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
280MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
304MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
320MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
352MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
448MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
Note: Board accepts EDO & SDRAM memory.				

CACHE CONFIGURATION
Note: 256KB/512KB cache is located on the Pentium II CPU.

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CPU SPEED SELECTION						
CPU speed	**Clock speed**	**Multiplier**	**CF1**	**CF2**	**CF3**	**CF4**
233MHz	66MHz	3.5x	Open	Open	Closed	Closed
266MHz	66MHz	4x	Closed	Closed	Open	Closed
300MHz	66MHz	4.5x	Open	Closed	Open	Closed
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (CON'T)					
CPU speed	**Clock speed**	**Multiplier**	**FS0**	**FS1**	**FS2**
233MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2
266MHz	66MHz	4x	1 & 2	1 & 2	1 & 2
300MHz	66MHz	4.5x	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					

MISCELLANEOUS TECHINCAL NOTE
If only one CPU is installed, it must be in CPU1.