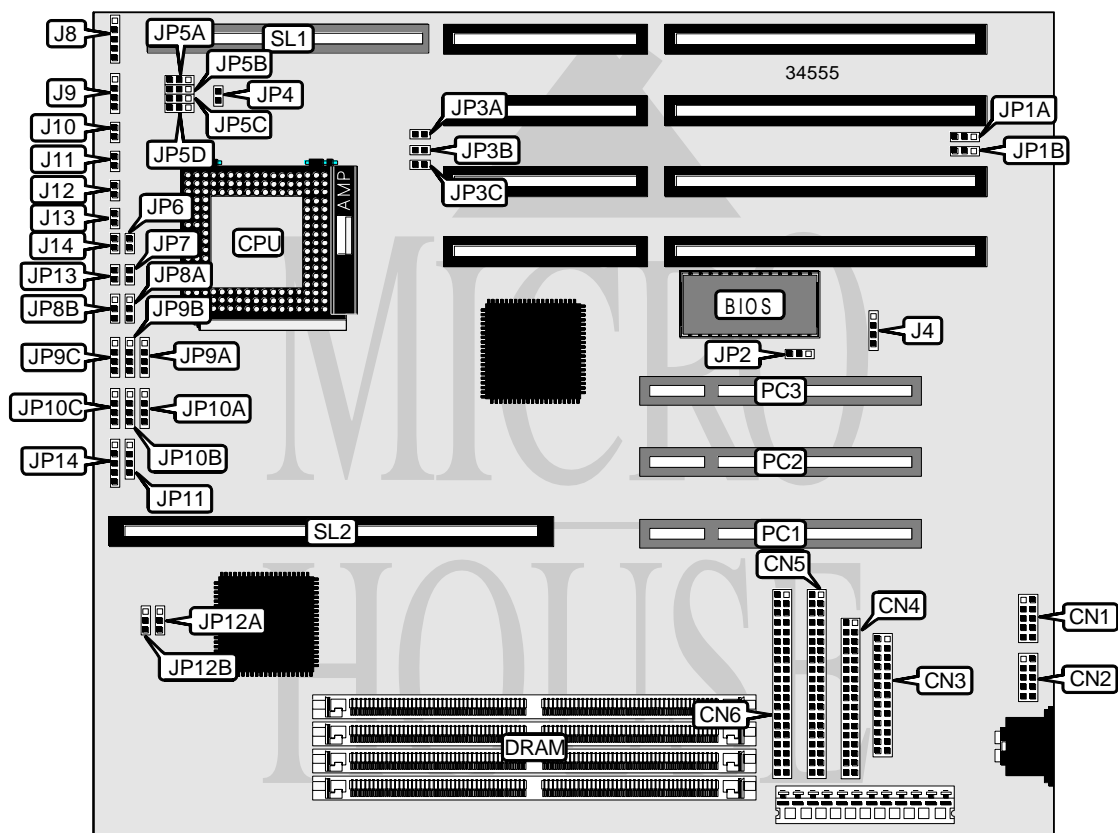


# AMPTRON INTERNATIONAL, INC.

## DX-9700

<b>Processor</b>	CX486DX/IBM486DX/TI486DX/SGS486DX/80486DX/SL80486DX/ CX486DX2/IBM486DX2/TI486DX2/SGS486DX2/AM486DX2/ SL80486DX2/80486DX2/CX486DX4/IBM486DX4/TI486DX4/ SGS486DX4/AM486DX4/SL80486DX4/SL80486DX4+/CX5X86/ IBM5X86/TI5X86/SGS5X86/AM5X86
<b>Processor Speed</b>	25/33/40/50(internal)/50/66(internal)/75(internal)/80(internal)/ 100(internal)/120(internal)/133(internal)MHz
<b>Chip Set</b>	UMC
<b>Video Chip Set</b>	None
<b>Maximum Onboard Memory</b>	256MB
<b>Maximum Video Memory</b>	None
<b>Cache</b>	256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	250mm x 220mm
<b>I/O Options</b>	32-bit VESA local bus slot, 32-bit PCI slots (3), green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2),
<b>NPU Options</b>	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Turbo LED	J10
Serial port 2	CN2	Reset switch	J11
Parallel port	CN3	Turbo switch	J12
Floppy drive interface	CN4	IDE interface LED	J13
IDE interface 1	CN5	Green PC connector	J14
IDE interface 2	CN6	32-bit PCI slots	PC1 - PC3
External battery	J4	32-bit VESA local bus slot	SL1
Power LED & keylock	J8	Cache slot	SL2
Speaker	J9		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Battery type select internal	J4	Closed
Battery type select external	J4	Pins 2 & 3 closed
CMOS memory clear	J4	Pins 3 & 4 closed
Flash BIOS voltage select 12v	JP2	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP2	Pins 2 & 3 closed

SIMM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	None	None	None
8MB	(1) 2M x 36	None	None	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
12MB	(1) 2M x 36	(1) 1M x 36	None	None
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
16MB	(1) 4M x 36	None	None	None
16MB	(1) 2M x 36	(1) 1M x 36	(1) 1M x 36	None
16MB	(1) 2M x 36	(1) 2M x 36	None	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
20MB	(1) 4M x 36	(1) 1M x 36	None	None
20MB	(1) 2M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
24MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	None
24MB	(1) 4M x 36	(1) 2M x 36	None	None
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	None
28MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
32MB	(1) 8M x 36	None	None	None
32MB	(1) 4M x 36	(1) 2M x 36	(1) 2M x 36	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
36MB	(1) 8M x 36	(1) 1M x 36	None	None
40MB	(1) 8M x 36	(1) 1M x 36	(1) 1M x 36	None

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SIMM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
40MB	(1) 8M x 36	(1) 2M x 36	None	None
40MB	(1) 4M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
44MB	(1) 8M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
48MB	(1) 8M x 36	(1) 2M x 36	(1) 2M x 36	None
48MB	(1) 8M x 36	(1) 4M x 36	None	None
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
64MB	(1) 8M x 36	(1) 8M x 36	None	None
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	None	None	None
76MB	(1) 16M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	None
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 16M x 36	(1) 16M x 36	None	None
192MB	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36	None
256MB	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36

CACHE CONFIGURATION	
Size	SL1
256KB	256KB module installed

CPU SPEED SELECTION			
Speed	JP3A	JP3B	JP3C
25MHz	Open	Open	Closed
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed
75iMHz	Open	Open	Closed
80iMHz	Open	Closed	Closed
100iMHz	Closed	Closed	Closed
120iMHz	Open	Closed	Closed
133iMHz	Closed	Closed	Closed

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CPU TYPE SELECTION					
Type	JP6	JP7	JP8A	JP8B	JP9A
CX486DX	Open	Open	Open	1 & 2	1 & 2
IBM486DX	Open	Open	Open	1 & 2	1 & 2
TI486DX	Open	Open	Open	1 & 2	1 & 2
SGS486DX	Open	Open	Open	1 & 2	1 & 2
80486DX	Open	Closed	2 & 3	Open	2 & 3
SL80486DX	Open	Open	2 & 3	Open	1 & 2
CX486DX2	Open	Open	Open	1 & 2	1 & 2
IBM486DX2	Open	Open	Open	1 & 2	1 & 2
TI486DX2	Open	Open	Open	1 & 2	1 & 2
SGS486DX2	Open	Open	Open	1 & 2	1 & 2
CX486DX2-80V	Open	Open	Open	1 & 2	1 & 2
IBM486DX2-80V	Open	Open	Open	1 & 2	1 & 2
TI486DX2-80V	Open	Open	Open	1 & 2	1 & 2
SGS486DX2-80V	Open	Open	Open	1 & 2	1 & 2
AM486DX2	Open	Closed	1 & 2	Open	2 & 3
SL80486DX2	Open	Open	2 & 3	Open	1 & 2
80486DX2	Open	Closed	2 & 3	Open	2 & 3
CX486DX4-GC	Open	Open	Open	1 & 2	1 & 2
IBM486DX4-GC	Open	Open	Open	1 & 2	1 & 2
TI486DX4-GC	Open	Open	Open	1 & 2	1 & 2
SGS486DX4-GC	Open	Open	Open	1 & 2	1 & 2
CX486DX4-GP	Open	Open	Open	1 & 2	1 & 2
IBM486DX4-GP	Open	Open	Open	1 & 2	1 & 2
TI486DX4-GP	Open	Open	Open	1 & 2	1 & 2
SGS486DX4-GP	Open	Open	Open	1 & 2	1 & 2
CX486DX4-GIC	Open	Open	1 & 2	Open	1 & 2
IBM486DX4-GIC	Open	Open	1 & 2	Open	1 & 2
TI486DX4-GIC	Open	Open	1 & 2	Open	1 & 2
SGS486DX4-GIC	Open	Open	1 & 2	Open	1 & 2
CX486DX4-GP4	Open	Open	1 & 2	Open	1 & 2
IBM486DX4-GP4	Open	Open	1 & 2	Open	1 & 2
TI486DX4-GP4	Open	Open	1 & 2	Open	1 & 2
SGS486DX4-GP4	Open	Open	1 & 2	Open	1 & 2
AM486DX4	Open	Closed	1 & 2	Open	2 & 3
SL80486DX4	Open	Open	2 & 3	Open	1 & 2
SL80486DX4+	Open	Open	1 & 2	Open	1 & 2
CX 5X86 (P24D)	Open	Open	1 & 2	Open	1 & 2
IBM 5X86 (P24D)	Open	Open	1 & 2	Open	1 & 2
TI 5X86 (P24D)	Open	Open	1 & 2	Open	1 & 2
SGS 5X86 (P24D)	Open	Open	1 & 2	Open	1 & 2
CX 5X86-133	Closed	Open	1 & 2	Open	1 & 2
IBM 5X86 -133	Closed	Open	1 & 2	Open	1 & 2
TI 5X86 -133	Closed	Open	1 & 2	Open	1 & 2
SGS 5X86 -133	Closed	Open	1 & 2	Open	1 & 2
AM 5X86	Closed	Open	1 & 2	Open	1 & 2

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CPU TYPE SELECTION (CON'T)					
Type	JP9B	JP9C	JP10A	JP10B	JP10C
CX486DX	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
IBM486DX	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
TI486DX	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
SGS486DX	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
80486DX	Open	Open	3 & 4	Open	1 & 2, 3 & 4
SL80486DX	1 & 2	Open	3 & 4	1 & 2	1 & 2, 3 & 4
CX486DX2	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
IBM486DX2	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
TI486DX2	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
SGS486DX2	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
CX486DX2-80V	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
IBM486DX2-80V	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
TI486DX2-80V	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
SGS486DX2-80V	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
AM486DX2	Open	Open	3 & 4	Open	1 & 2, 3 & 4
SL80486DX2	1 & 2	Open	3 & 4	1 & 2	1 & 2, 3 & 4
80486DX2	Open	Open	3 & 4	Open	1 & 2, 3 & 4
CX486DX4-GC	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
IBM486DX4-GC	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
TI486DX4-GC	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
SGS486DX4-GC	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
CX486DX4-GP	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
IBM486DX4-GP	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
TI486DX4-GP	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
SGS486DX4-GP	1 & 2	2 & 3	3 & 4	2 & 3	1 & 2, 3 & 4
CX486DX4-GIC	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
IBM486DX4-GIC	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
TI486DX4-GIC	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
SGS486DX4-GIC	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
CX486DX4-GP4	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
IBM486DX4-GP4	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
TI486DX4-GP4	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
SGS486DX4-GP4	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
AM486DX4	Open	Open	3 & 4	Open	1 & 2, 3 & 4
SL80486DX4	1 & 2	Open	3 & 4	1 & 2	1 & 2, 3 & 4
SL80486DX4+	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
CX 5X86 (P24D)	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
IBM 5X86 (P24D)	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
TI 5X86 (P24D)	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
SGS 5X86 (P24D)	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
CX 5X86-133	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
IBM 5X86 -133	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
TI 5X86 -133	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
SGS 5X86 -133	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4
AM 5X86	1 & 2, 3 & 4	3 & 4	3 & 4	1 & 2, 3 & 4	1 & 2, 3 & 4

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CPU TYPE SELECTION (CON'T)					
Type	JP11	JP12A	JP12B	JP13	JP14
CX486DX	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
IBM486DX	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
TI486DX	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
SGS486DX	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
80486DX	Open	1 & 2	1 & 2	Open	Open
SL80486DX	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
CX486DX2	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
IBM486DX2	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
TI486DX2	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
SGS486DX2	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
CX486DX2-80V	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
IBM486DX2-80V	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
TI486DX2-80V	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
SGS486DX2-80V	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
AM486DX2	Open	1 & 2	2 & 3	Open	Open
SL80486DX2	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
80486DX2	Open	1 & 2	1 & 2	Open	Open
CX486DX4-GC	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
IBM486DX4-GC	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
TI486DX4-GC	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
SGS486DX4-GC	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
CX486DX4-GP	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
IBM486DX4-GP	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
TI486DX4-GP	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
SGS486DX4-GP	2 & 3	2 & 3	1 & 2	Open	1 & 2, 3 & 4
CX486DX4-GIC	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
IBM486DX4-GIC	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
TI486DX4-GIC	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
SGS486DX4-GIC	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
CX486DX4-GP4	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
IBM486DX4-GP4	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
TI486DX4-GP4	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
SGS486DX4-GP4	Open	2 & 3	1 & 2	Closed	2 & 3, 4 & 5
AM486DX4	Open	1 & 2	2 & 3	Open	Open
SL80486DX4	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
SL80486DX4+	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
CX 5X86 (P24D)	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
IBM 5X86 (P24D)	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
TI 5X86 (P24D)	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
SGS 5X86 (P24D)	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
CX 5X86-133	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
IBM 5X86 -133	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
TI 5X86 -133	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
SGS 5X86 -133	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5
AM 5X86	Open	1 & 2	1 & 2	Open	2 & 3, 4 & 5



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CPU VOLTAGE SELECTION					
Voltage	JP4	JP5A	JP5B	JP5C	JP5D
3.3v	Closed	1 & 2	1 & 2	1 & 2	1 & 2
4v	Open	1 & 2	1 & 2	1 & 2	1 & 2
5v	Closed	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.					

DMA CHANNEL SELECTION		
Channel	JP1A	JP1B
1	Pins 1 & 2 closed	Pins 1 & 2 closed
2	Pins 2 & 3 closed	Pins 2 & 3 closed