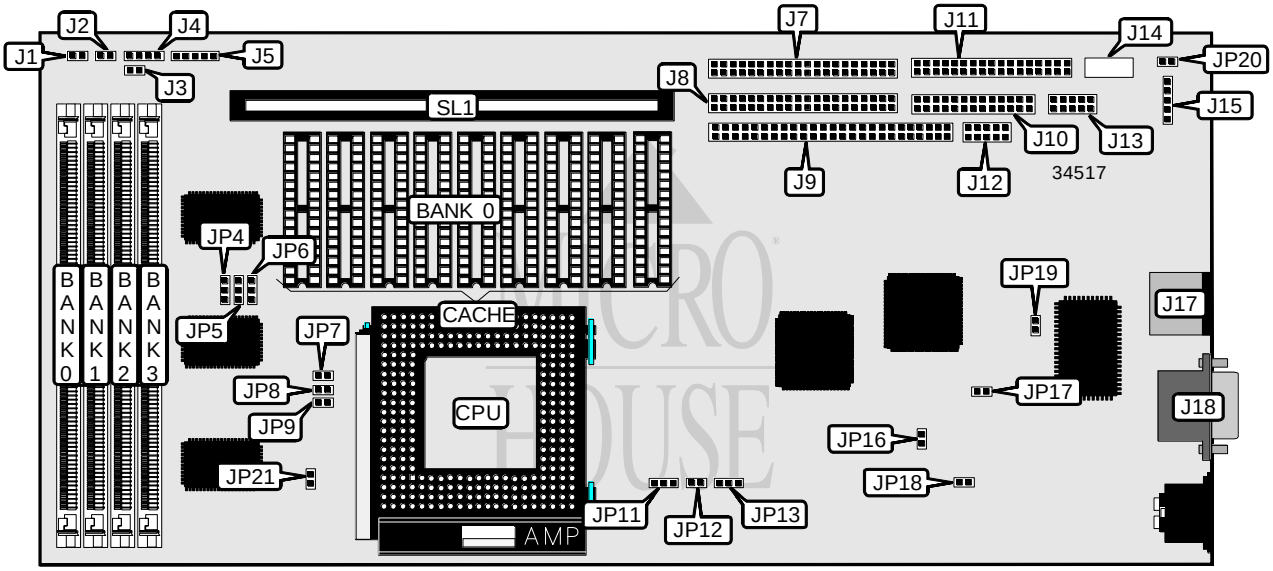
TECHNOLAND, INC.
P E A K 5 2 1 S

Processor	Pentium
Processor Speed	75/90/100/120/133/150/166MHz
Chip Set	SIS
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	256/512/1024KB
BIOS	Award
Dimensions	338mm x 122mm
I/O Options	Floppy drive interface, IDE interfaces (2), SCSI interface, parallel port, PS/2 mouse port, serial ports (2), cache slot
NPU Options	None



CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Turbo LED	J1	Floppy drive interface	J11
Reset switch	J2	SCSI adapter connector	J12
IDE interface LED	J3	Serial port	J13
Speaker	J4	Auxiliary keyboard connector	J15
Power LED & keylock	J5	PS/2 mouse port	J17
IDE interface 1	J7	Serial port	J18
IDE interface 2	J8	SCSI interface LED	JP20
SCSI interface	J9	Cache slot	SL1
Parallel port	J10		

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USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	J14	Unidentified
í IDE interface enabled	JP12	Closed
IDE interface disabled	JP12	Open
í Cache type select write back	JP13	Pins 1 & 2 closed
Cache type select write through	JP13	Pins 2 & 3 closed
í Watchdog timer port select Port F2	JP16	Closed
Watchdog timer port select Port F6	JP16	Open
í CMOS memory normal operation	JP17	Open
CMOS memory clear	JP17	Closed
í Monitor type select color	JP18	Closed
Monitor type select monochrome	JP18	Open
í On board I/O enabled	JP19	Open
On board I/O disabled	JP19	Closed

SIMM CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
4MB	(1) 1M x 32	None	None	None
8MB	None	None	None	(1) 2M x 32
8MB	(1) 1M x 32	(1) 1M x 32	None	None
8MB	(1) 2M x 32	None	None	None
16MB	(1) 1M x 32	(1) 1M x 32	(1) 1M x 32	(1) 1M x 32
16MB	(1) 4M x 32	None	None	None
16MB	(1) 2M x 32	(1) 2M x 32	None	None
16MB	None	None	None	(1) 4M x 32
24MB	(1) 1M x 32	(1) 1M x 32	(1) 2M x 32	(1) 2M x 32
32MB	(1) 8M x 32	None	None	None
32MB	(1) 2M x 32	(1) 2M x 32	(1) 2M x 32	(1) 2M x 32
32MB	(1) 4M x 32	(1) 4M x 32	None	None
32MB	None	None	None	(1) 8M x 32
48MB	(1) 2M x 32	(1) 2M x 32	(1) 4M x 32	(1) 4M x 32
64MB	(1) 4M x 32	(1) 4M x 32	(1) 4M x 32	(1) 4M x 32
64MB	(1) 8M x 32	(1) 8M x 32	None	None
96MB	(1) 4M x 32	(1) 4M x 32	(1) 8M x 32	(1) 8M x 32
128MB	(1) 8M x 32	(1) 8M x 32	(1) 8M x 32	(1) 8M x 32

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CACHE CONFIGURATION			
Size	**Bank 0**	**SL1**	**TAG**
256KB (A)	(8) 32K x 8	Not installed	Unidentified
256KB (B)	None	256KB module installed	Unidentified
512KB (A)	None	512KB module installed	Unidentified
512KB (B)	(8) 64K x 8	Not installed	Unidentified
1MB	(8) 128 x 8	Not installed	Unidentified

CACHE JUMPER CONFIGURATION		
Setting	**JP4**	**JP5**
256KB (A)	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB (B)	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB (A)	Pins 1 & 2 closed	Pins 2 & 3 closed
512KB (B)	Pins 1 & 2 closed	Pins 2 & 3 closed
1MB	Pins 2 & 3 closed	Pins 2 & 3 closed

CACHE TYPE CONFIGURATION	
Type	**JP6**
í Asynchronous	Pins 1 & 2 closed
Burst	Pins 2 & 3 closed

CPU SPEED SELECTION							
CPU speed	**Clock speed**	**Multiplier**	**JP7**	**JP8**	**JP9**	**JP11**	**JP21**
75MHz	50MHz	1.5x	Open	Closed	Open	1 & 2	Open
90MHz	60MHz	1.5x	Open	Closed	Closed	1 & 2	Open
100MHz	66MHz	1.5x	Closed	Closed	Closed	1 & 2	Open
120MHz	60MHz	2x	Open	Closed	Closed	2 & 3	Open
133MHz	66MHz	2x	Closed	Closed	Closed	2 & 3	Open
150MHz	60MHz	2.5x	Open	Closed	Closed	2 & 3	Closed
166MHz	66MHz	2.5x	Closed	Closed	Closed	1 & 2	Closed
Note: Pins designated should be in the closed position.							