TMC RESEARCH CORPORATION AI5TH (VER. 1.0E)

Processor
Processor Speed
Chip Set
Video Chip Set
Maximum Onboard Memory
Maximum Video Memory
Cache
BIOS
Dimensions
I/O Options
-

CX M1/AM K5/Pentium 75/90/100/120/133/150/166/200MHz Intel None 256MB None 256/512KB Unidentified 254mm x 218mm 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), cache slot, IR connector, USB connector None

NPU Options



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CONNECTIONS					
Purpose	Location	Purpose	Location		
USB connector 1	J6	Speaker	J20/pins 1 & 4		
Serial port 1	J7	Green PC connector	J20/pins 6 & 16		
USB connector 2	J8	Turbo LED	J20/pins 8 & 18		
Serial port 2	19	Reset switch	J20/pins 9 & 19		
Floppy drive interface	J12	IDE interface LED	J20/pins 10 & 20		
Parallel port	J13	Power LED & keylock	J20/pins 11 & 15		
IDE interface 1	J15	32-bit PCI slots	PC1 – PC4		
IDE interface 2	J16	Cache slot	SL1		

	USER CONFIGURABLE SETTINGS						
	Function	Label	Position				
í	Flash BIOS voltage select 5v	JP7	Pins 2 & 3 closed				
	Flash BIOS voltage select 12v	JP7	Pins 1 & 2 closed				
í	PCI CLK select /4	JP13	Closed				
	PCI CLK select /3	JP13	Open				
í	CMOS memory normal operation	JP17	Open				
	CMOS memory clear	JP17	Closed				

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
8MB	(2) 1Mx 32	None
16MB	(2) 2Mx 32	None
16MB	(2) 1Mx 32	(2) 1Mx 32
24MB	(2) 2Mx 32	(2) 1Mx 32
32MB	(2) 4Mx 32	None
32MB	(2) 2Mx 32	(2) 2Mx 32
40MB	(2) 4Mx 32	(2) 1Mx 32
48MB	(2) 4Mx 32	(2) 2Mx 32
64MB	(2) 8Mx 32	None
64MB	(2) 4Mx 32	(2) 4Mx 32
72MB	(2) 8Mx 32	(2) 1Mx 32
80MB	(2) 8Mx 32	(2) 2Mx 32
96MB	(2) 8Mx 32	(2) 4Mx 32
128MB	(2) 8Mx 32	(2) 8Mx 32
128MB	(2) 16Mx 32	None
136MB	(2) 16Mx 32	(2) 1Mx 32
144MB	(2) 16Mx 32	(2) 2Mx 32
192MB	(2) 16Mx 32	(2) 8Mx 32
256MB	(2) 16Mx 32	(2) 16Mx 32
Note: Board accepts EDO memory.		

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Note [.]	The cache	configuration	is unider	ntified
NOLC.	The cache	configuration	13 uniuer	iuneu.

CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JP8	JP9	JP10	JP11	JP12
120MHz	50MHz	2x	Closed	Closed	Closed	Closed	Open
133MHz	55MHz	2x	Closed	Open	Open	Closed	Open
150MHz	60MHz	2x	Closed	Open	Closed	Closed	Open
166MHz	66MHz	2x	Open	Closed	Open	Closed	Open

CACHE CONFIGURATION

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP8	JP9	JP10	JP11	JP12
75MHz	50MHz	1.5x	Closed	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	Closed	Open	Closed	Open	Open
100MHz	66MHz	1.5x	Open	Closed	Open	Open	Open
120MHz	60MHz	1.5x	Closed	Open	Closed	Open	Open
133MHz	66MHz	1.5x	Open	Closed	Open	Open	Open
150MHz	60MHz	2.5x	Closed	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	Open	Closed	Open	Closed	Closed

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP8	JP9	JP10	JP11	JP12
75MHz	50MHz	1.5x	Closed	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	Closed	Open	Closed	Open	Open
100MHz	66MHz	1.5x	Open	Closed	Open	Open	Open
120MHz	60MHz	2x	Closed	Open	Closed	Closed	Open
133MHz	66MHz	2x	Open	Closed	Open	Closed	Open
150MHz	60MHz	2.5x	Closed	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	Open	Closed	Open	Closed	Closed
200MHz	66MHz	Зx	Open	Closed	Open	Open	Closed

CPU VOLTAGE SELECTION					
Voltage	JP14	JP19			
Single	Closed	Open			
Dual	Open	Closed			

DMA CHANNEL SELECTION					
Channel	JP3	JP4			
1	Pins 1 & 2 closed	Pins 1 & 2 closed			
í 3	Pins 2 & 3 closed	Pins 2 & 3 closed			

SERIAL PORT 2 SELECTION						
Setting	JP1	JP2				
Used as COM2	Pins 1 & 2 closed	Pins 1 & 2 closed				
í Used as IR connector	Pins 2 & 3 closed	Pins 2 & 3 closed				