Processor

Chip Set **Video Chip Set**

Cache

Dimensions

I/O Options

NPU Options

BIOS

Processor Speed

CX486S/80486SX/SL80486SX/CX486S2/CX486DX/AM486DXL/80486DX/SL80486 DX/CX486DX2/AM486DXT(NV8T)/80486DX2/SL80486DX2/SL80486DX4 25/33/40/50(internal)/66(internal)/75(internal)/80(internal)/ 100(internal)MHz Unidentified None **Maximum Onboard Memory** 64MB **Maximum Video Memory** None 128/256/512KB Unidentified 330mm x 218mm 32-bit VESA local bus slots (3), green PC connector None



Continued on next page. . .

... continued from previous page

CONNECTIONS				
Purpose	Location	Purpose	Location	
Green PC LED	D6	Green PC connector	JP44	
External battery	JP14	Reset switch	JP45	
Power LED & keylock	JP40	32-bit VESA local bus slots	SL1 – SL3	
Speaker	JP41			

USER CONFIGURABLE SETTINGS					
Function	Label	Position			
í Battery type select external	JP3	Pins 1 & 2 closed			
Battery type select internal	JP3	Pins 2 & 3 closed			
í BIOS type select 27512	JP10	Open			
BIOS type select 28F010	JP10	Closed			
í Factory configured - do not alter	JP15	Closed			

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	
1MB	(1) 256K x 36	None	None	None	
4MB	(1) 256K x 36				
4MB	(1) 1M x 36	None	None	None	
8MB	(1) 1M x 36	(1) 1M x 36	None	None	
16MB	(1) 1M x 36				
16MB	(1) 4M x 36	None	None	None	
32MB	(1) 4M x 36	(1) 4M x 36	None	None	
64MB	(1) 4M x 36				

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG		
128KB	(4) 32K x 8	None	(1) 8K x 8		
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8		
512KB	(4) 128K x 8	None	(1) 32K x 8		

CACHE JUMPER CONFIGURATION						
Size	JP19	JP20	JP21	JP26	JP27	JP28
128KB	Open	2&3	2&3	Closed	Closed	Open
256KB	Open	1&2	1&2	Closed	Closed	Closed
512KB	Closed	2&3	1&2	Closed	Closed	Closed
Note: Pins designated should be in the closed position.						

Continued on next page. . .

. . . continued from previous page

CPU SPEED SELECTION					
Speed	JP36	JP37	S1	S2	S3
25MHz	1 & 2	1&2	Closed	Open	Open
33MHz	1 & 2	1&2	Closed	Closed	Closed
40MHz	1 & 2	1&2	Closed	Closed	Open
50iMHz	1 & 2	1&2	Closed	Open	Open
66iMHz	1 & 2	1&2	Closed	Closed	Closed
75iMHz	1 & 2	1&2	Closed	Open	Open
80iMHz	1 & 2	1&2	Closed	Closed	Open
100iMHz	1 & 2	1&2	Closed	Closed	Closed
120iMHz	1 & 2	1 & 2	Closed	Closed	Open
Note: Pins designated should be in the closed position.					

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION					
Туре	JP17	JP18	JP22	JP23	JP24
CX486S	Closed	Open	2 & 3	2&3	2&3
80486SX	Open	Open	N/A	N/A	N/A
SL80486SX	Open	Open	1 & 2	1 & 2	1&2
CX486S2	Closed	Open	2 & 3	2 & 3	2&3
CX486DX	Closed	Closed	2 & 3	2 & 3	2 & 3
AM486DXL	Open	Open	Open	Open	Open
80486DX	Open	Open	N/A	N/A	N/A
SL80486DX	Open	Open	1 & 2	1 & 2	1&2
CX486DX2	Closed	Closed	2 & 3	2 & 3	2&3
AM486DX2 (NV8T)	Open	Open	Open	Open	Open
80486DX2	Open	Open	N/A	N/A	N/A
SL80486DX2	Open	Open	1 & 2	1 & 2	1&2
SL80486DX4	Open	Open	1 & 2	1 & 2	1&2
Note: Pins designated s	should be in the clo	osed position.			

Continued on next page. . .

... continued from previous page

CPU TYPE SELECTION (CON'T)					
Туре	JP25	JP29	JP30	JP31	JP49
CX486S	Open	2&3	Open	Open	Open
80486SX	N/A	2&3	Open	Open	N/A
SL80486SX	Open	2&3	Open	Open	Open
CX486S2	Open	2&3	Open	Open	Open
CX486DX	Open	1&2	2&3	Closed	Open
AM486DXL	Closed	1&2	2&3	Closed	Closed
80486DX	N/A	1&2	2&3	Closed	N/A
SL80486DX	Open	1&2	2&3	Closed	Open
CX486DX2	Open	1&2	2&3	Closed	Open
AM486DX2 (NV8T)	Open	1&2	2&3	Closed	Open
80486DX2	N/A	1&2	2&3	Closed	N/A
SL80486DX2	Open	1&2	2&3	Closed	Open
SL80486DX4	Open	1&2	2 & 3	Closed	Open
Note: Pins designated should be in the closed position.					

CPU TYPE SELECTION				
Туре	JP38			
SL-enhanced	Open			
Non SL-enhanced	Closed			

CPU VOLTAGE SELECTION				
Voltage	JP32			
3.3v	Pins 5 & 6, 7 & 8 closed			
í 5v	Pins 1 & 2, 3 & 4 closed			

VL BUS WAIT STATE SELECTION				
Setting	JP50			
0	Open			
í 1	Closed			

VL BUS SPEED SELECTION				
Speed	JP51			
<= 33MHz	Open			
í >33 MHz	Closed			