Processor Speed

Processor CX486SX/80486(WB)/80486SX/SL80486SX/CX486DX/80486DX/

AM486DX/AM486DXL/SL80486DX/CX486DX2V/CX486DX2/

AM486DX2/AM486DXL2/80486DX2/SL80486DX2/AM486DX4/ 80486DX4/P24T 25/33/40/50(internal)/66(internal)/75(internal)/80(internal)/100(internal)MHz

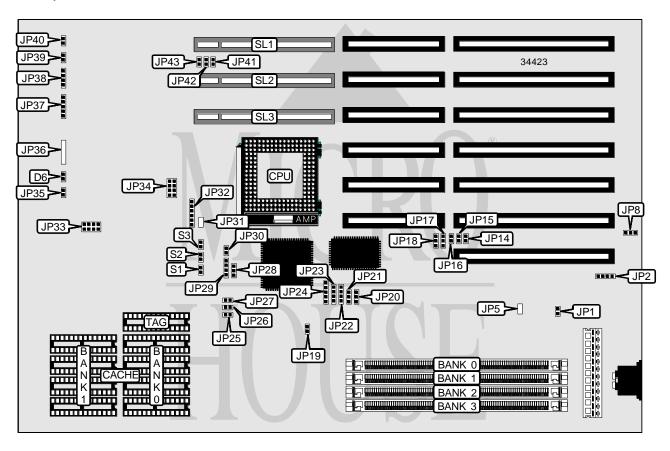
Chip Set Unidentified

Video Chip Set None
Maximum Onboard Memory 64MB
Maximum Video Memory None

Cache 128/256/512KB
BIOS Unidentified
Dimensions 330mm x 218mm

I/O Options 32-bit VESA local bus slots (3), green PC connector

NPU Options None



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CONNECTIONS					
Purpose Location Purpose Location					
Green PC LED	D6	Green PC connector	JP44		
External battery	JP2	Reset switch	JP45		
Power LED & keylock	JP40	32-bit VESA local bus slots	SL1 – SL3		
Speaker	JP41				

USER CONFIGURABLE SETTINGS				
Function	Label	Position		
BIOS type select 28F010	JP1	Closed		
BIOS type select 27512	JP1	Open		
í Factory configured - do not alter	JP5	Unidentified		
Battery type select internal	JP8	Pins 2 & 3 closed		
Battery type select external	JP8	Pins 1 & 2 closed		
í Factory configured - do not alter	JP14	Closed		
í Factory configured - do not alter	JP31	Unidentified		
í Factory configured - do not alter	JP43	Unidentified		

	DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3		
1MB	(1) 256K x 36	None	None	None		
2MB	(1) 512K x 36	None	None	None		
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36		
4MB	(1) 512K x 36	None	(1) 512K x 36	None		
4MB	(1) 1M x 36	None	None	None		
8MB	(1) 1M x 36	(1) 1M x 36	None	None		
8MB	(1) 2M x 36	None	None	None		
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36		
16MB	(1) 2M x 36	None	(1) 2M x 36	None		
16MB	(1) 4M x 36	None	None	None		
32MB	(1) 4M x 36	(1) 4M x 36	None	None		
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36		

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG		
128KB	(4) 32K x 8	None	(1) 8K x 8		
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8		
512KB	(4) 128K x 8	None	(1) 32K x 8		

CACHE JUMPER CONFIGURATION						
Size	JP19	JP20	JP21	JP25	JP26	JP27
128KB	Open	2 & 3	2 & 3	Closed	Closed	Open
256KB	Open	1 & 2	1 & 2	Closed	Closed	Closed
512KB	Closed	2 & 3	1 & 2	Closed	Closed	Closed
Note: Pins desi	lote: Pins designated should be in the closed position.					

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CPU TYPE SELECTION						
Туре	JP15	JP16	JP17	JP18	JP22	
CX486SX(WB)	Closed	Closed	2 & 3	2 & 3	2 & 3	
CX486SX(WT)	Closed	Closed	1 & 2	1 & 2	2 & 3	
80486 (WB 3.3v)	Open	Closed	2 & 3	2 & 3	1 & 2, 3 & 4	
80486 (WB 5v)	Open	Closed	2 & 3	2 & 3	1 & 2, 3 & 4	
80486SX	Open	Open	1 & 2	1 & 2	Open	
SL80486SX	Open	Open	1 & 2	1 & 2	1 & 2	
CX486DX(WB)	Closed	Closed	2 & 3	2 & 3	2 & 3	
CX486DX(WT)	Closed	Closed	1 & 2	1 & 2	2 & 3	
80486DX	Open	Open	1 & 2	1 & 2	Open	
AM486DX	Open	Open	1 & 2	1 & 2	Open	
AM486DXL	Open	Open	1 & 2	1 & 2	Open	
SL80486DX	Open	Open	1 & 2	1 & 2	1 & 2	
CX486DX2V-50(WB)	Closed	Closed	2 & 3	2 & 3	2 & 3	
CX486DX2V-50(WT)	Closed	Closed	1 & 2	1 & 2	2 & 3	
CX486DX2(WB)	Closed	Closed	2 & 3	2 & 3	2 & 3	
CX486DX2(WT)	Closed	Closed	1 & 2	1 & 2	2 & 3	
AM486DX2	Open	Open	1 & 2	1 & 2	Open	
AM486DX2 (NV8T)	Open	Open	1 & 2	1 & 2	Open	
AM486DXL2	Open	Open	1 & 2	1 & 2	Open	
80486DX2	Open	Open	1 & 2	1 & 2	Open	
SL80486DX2	Open	Open	1 & 2	1 & 2	1 & 2	
AM486DX4 (NV8T)	Open	Open	1 & 2	1 & 2	Open	
80486DX4	Open	Open	1 & 2	1 & 2	1 & 2	
P24T(WB)	Open	Closed	2 & 3	2 & 3	1 & 2	
P24T(WT)	Open	Closed	1 & 2	1 & 2	1 & 2	
Note: Pins designated shou	Note: Pins designated should be in the closed position.					

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CPU TYPE SELECTION (CON'T)					
Туре	JP23	JP24	JP28	JP29	JP30
CX486SX(WB)	2 & 3	2 & 3, 4 & 5	2 & 3	Open	Open
CX486SX(WT)	2 & 3	2 & 3	2 & 3	Open	Open
80486 (WB 3.3v)	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2	2 & 3	Closed
80486 (WB 5v)	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2	2 & 3	Closed
80486SX	Open	Open	2 & 3	Open	Open
SL80486SX	1 & 2	1 & 2	2 & 3	Open	Open
CX486DX(WB)	2 & 3	2 & 3, 4 & 5	1 & 2	2 & 3	Closed
CX486DX(WT)	2 & 3	2 & 3	1 & 2	2 & 3	Closed
80486DX	Open	Open	1 & 2	2 & 3	Closed
AM486DX	Open	Open	1 & 2	2 & 3	Closed
AM486DXL	Open	Open	1 & 2	2 & 3	Closed
SL80486DX	1 & 2	1 & 2	1 & 2	2 & 3	Closed
CX486DX2V(WB)	2 & 3	2 & 3, 4 & 5	1 & 2	2 & 3	Closed
CX486DX2V(WT)	2 & 3	2 & 3	1 & 2	2 & 3	Closed
CX486DX2(WB)	2 & 3	2 & 3, 4 & 5	1 & 2	2 & 3	Closed
CX486DX2(WT)	2 & 3	2 & 3	1 & 2	2 & 3	Closed
AM486DX2	Open	Open	1 & 2	2 & 3	Closed
AM486DX2 (NV8T)	Open	Open	1 & 2	2 & 3	Closed
AM486DXL2	Open	Open	1 & 2	2 & 3	Closed
80486DX2	Open	Open	1 & 2	2 & 3	Closed
SL80486DX2	1 & 2	1 & 2	1 & 2	2 & 3	Closed
AM486DX4 (NV8T)	Open	Open	1 & 2	2 & 3	Closed
80486DX4	1 & 2	1 & 2	1 & 2	2 & 3	Closed
P24T(WB)	1 & 2, 3 & 4	1 & 2	1 & 2	1 & 2	Closed
P24T(WT)	1 & 2, 3 & 4	1 & 2	1 & 2	1 & 2	Closed
Note: Pins designated show	uld be in the close	d position.			

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CPU TYPE SELECTION (CON'T)				
Туре	JP32	JP33	JP34	JP36
CX486SX(WB)	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	Open
CX486SX(WT)	Open	1 & 2, 3 & 4	Open	Open
80486 (WB 3.3v)	1 & 2	5 & 6, 7 & 8	Open	Open
80486 (WB 5v)	1 & 2	1 & 2, 3 & 4	Open	Open
80486SX	Open	1 & 2, 3 & 4	Open	Open
SL80486SX	Open	1 & 2, 3 & 4	Open	Open
CX486DX(WB)	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	Open
CX486DX(WT)	Open	1 & 2, 3 & 4	Open	Open
80486DX	Open	1 & 2, 3 & 4	Open	Open
AM486DX	Open	1 & 2, 3 & 4	Open	Open
AM486DXL	Open	1 & 2, 3 & 4	Open	1 & 2, 3 & 4
SL80486DX	Open	1 & 2, 3 & 4	Open	Open
CX486DX2V(WB)	2 & 3, 4 & 5	5 & 6, 7 & 8	Open	Open
CX486DX2V(WT)	Open	5 & 6, 7 & 8	Open	Open
CX486DX2(WB)	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	Open
CX486DX2(WT)	Open	1 & 2, 3 & 4	Open	Open
AM486DX2	Open	1 & 2, 3 & 4	Open	Open
AM486DX2 (NV8T)	Open	5 & 6, 7 & 8	7 & 8	Open
AM486DXL2	Open	1 & 2, 3 & 4	Open	1 & 2, 3 & 4
80486DX2	Open	1 & 2, 3 & 4	Open	Open
SL80486DX2	Open	1 & 2, 3 & 4	Open	Open
AM486DX4 (NV8T)	Open	5 & 6, 7 & 8	5 & 6	Open
80486DX4	Open	5 & 6, 7 & 8	Open	Open
P24T(WB)	1 & 2	1 & 2, 3 & 4	Open	Open
P24T(WT)	Open	1 & 2, 3 & 4	Open	Open
Note: Pins designated sho	uld be in the closed	l position.		

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	CPU SPEED SELECTION					
Speed	S1	S2	S3			
25MHz	Closed	Open	Open			
33MHz	Closed	Closed	Closed			
40MHz	Closed	Closed	Open			
50iMHz	Closed	Open	Open			
66iMHz	Closed	Closed	Closed			
75iMHz	Closed	Open	Open			
80iMHz	Closed	Closed	Open			
100iMHz	Closed	Closed	Closed			

CPU TYPE SELECTION			
Туре	JP35		
Non SL-enhanced	Closed		
SL-enhanced	Open		

VL BUS WAIT STATE SELECTION			
Setting	JP41		
0	Open		
1	Closed		

VL BUS SPEED SELECTION			
Speed	JP42		
<= 33MHz	Open		
>33 MHz	Closed		