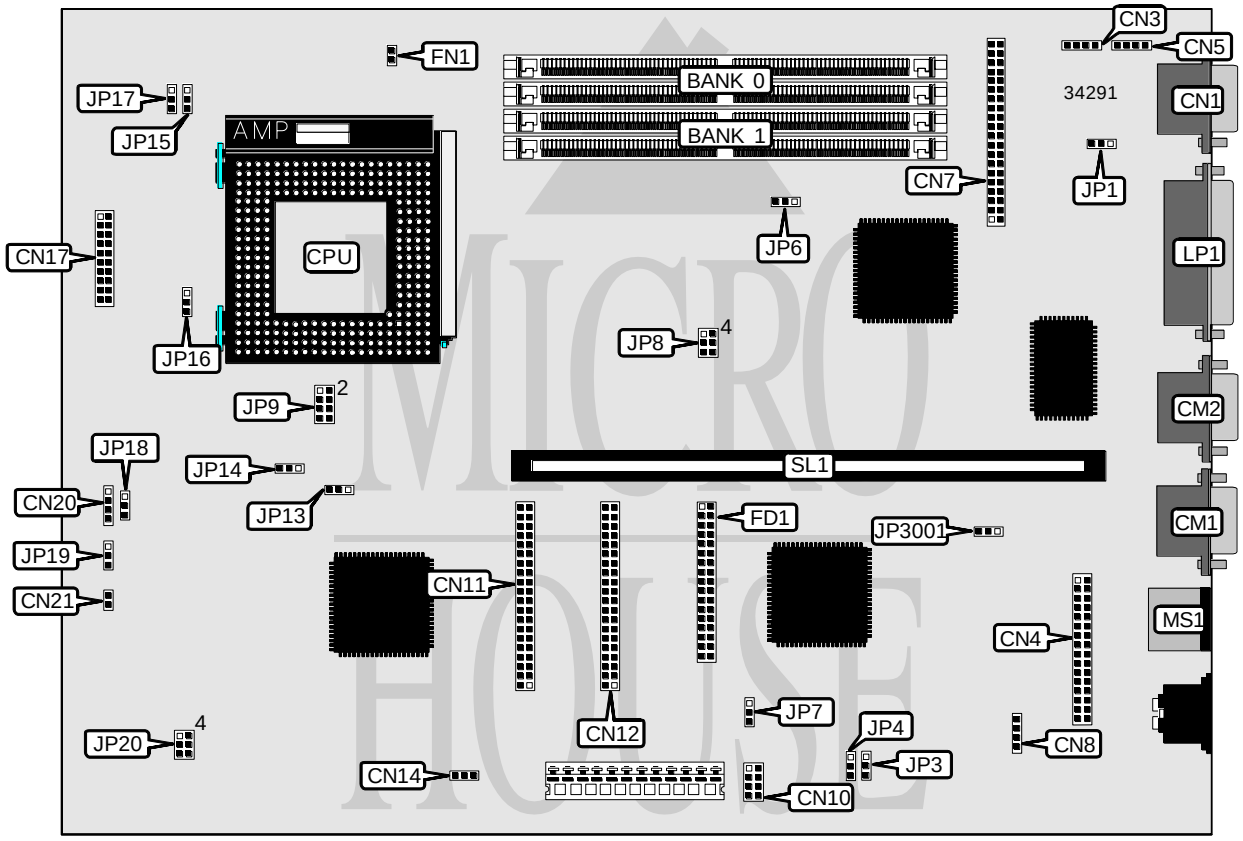
# IBM CORPORATION
# A P T I V A  2 1 3 6

| | |
|---|---|
| **Processor** | CX M1/IBM/Pentium |
| **Processor Speed** | 100/120/133/150/166/200MHz |
| **Chip Set** | Unidentified |
| **Video Chip Set** | Unidentified |
| **Maximum Onboard Memory** | 128MB (EDO supported) |
| **Maximum Video Memory** | 1MB |
| **Cache** | 256/512KB |
| **BIOS** | Unidentified |
| **Dimensions** | 330mm x 218mm |
| **I/O Options** | Floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), VGA feature connector, VGA port, riser slot, fax/modem/voice connector, line in, CD-ROM audio in, audio in connector, |
| **NPU Options** | None |

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# IBM CORPORATION
# A P T I V A  2 1 3 6

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| CONNECTIONS | | | |
|---|---|---|---|
| **Purpose** | **Location** | **Purpose** | **Location** |
| Floppy drive interface | FD1 | Software shutdown connector | CN14 |
| Chassis fan power | FN1 | Power LED & keylock | CN17/pins 3 - 5 |
| VGA port | CN1 | IDE interface LED | CN20 |
| Fax/modem/voice connector | CN3 | Power switch | CN21 |
| Audio in connector | CN4 | Serial port | CM1 |
| Line in | CN5 | Serial port | CM2 |
| VGA feature connector | CN7 | Green PC connector | JP19 |
| CD-ROM audio in | CN8 | Parallel port | LP1 |
| Wavetable connector | CN10 | PS/2 mouse port | MS1 |
| IDE interface 1 | CN11 | Riser slot | SL1 |
| IDE interface 2 | CN12 | | |

| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| **Function** | **Label** | **Position** |
| í On board video enabled | JP1 | Pins 2 & 3 closed |
| On board video disabled | JP1 | Pins 1 & 2 closed |
| í Factory configured - do not alter | JP3 | Pins 1 & 2 closed |
| í Password enabled | JP4 | Pins 1 & 2 closed |
| Password disabled | JP4 | Pins 2 & 3 closed |
| í BIOS select flash BIOS | JP7 | Pins 2 & 3 closed |
| BIOS select block ROM | JP7 | Pins 1 & 2 closed |
| í LED select IDE & floppy LEDs | JP18 | Pins 1 & 2 closed |
| LED select IDE LED only | JP18 | Pins 2 & 3 closed |
| í Factory configured - Software shut down enabled | JP20 | Pins 1 & 2, 4 & 5 closed |
| í Flash BIOS write protect disabled | JP3001 | Pins 2 & 3 closed |
| Flash BIOS write protect enabled | JP3001 | Pins 1 & 2 closed |

| DRAM CONFIGURATION | | |
|---|---|---|
| **Size** | **Bank 0** | **Bank 1** |
| 8MB | (2) 1M x 32 | None |
| 16MB | (2) 2M x 32 | None |
| 16MB | (2) 1M x 32 | (2) 1M x 32 |
| 24MB | (2) 2M x 32 | (2) 1M x 32 |
| 32MB | (2) 4M x 32 | None |
| 32MB | (2) 2M x 32 | (2) 2M x 32 |
| 40MB | (2) 4M x 32 | (2) 1M x 32 |
| 48MB | (2) 4M x 32 | (2) 2M x 32 |
| 64MB | (2) 8M x 32 | None |
| 64MB | (2) 4M x 32 | (2) 4M x 32 |
| 72MB | (2) 8M x 32 | (2) 1M x 32 |
| 80MB | (2) 8M x 32 | (2) 2M x 32 |
| 96MB | (2) 8M x 32 | (2) 4M x 32 |
| 128MB | (2) 8M x 32 | (2) 8M x 32 |
| Note: Board accepts EDO memory. Banks are interchangeable. | | |

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# IBM CORPORATION
# A P T I V A  2 1 3 6

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| CACHE CONFIGURATION | |
|---|---|
| **Size** | **Bank 0** |
| 256KB | (2) 32K x 32 |
| 512KB | (2) 64K x 32 |
| Note: The location of the cache is unidentified. | |

| CACHE JUMPER CONFIGURATION | |
|---|---|
| **Size** | **JP6** |
| 256KB | Pins 1 & 2 closed |
| 512KB | Pins 2 & 3 closed |

| CACHE OPERATION CONFIGURATION | |
|---|---|
| **Type** | **JP16** |
| í Interleave burst | Pins 1 & 2 closed |
| Linear burst | Pins 2 & 3 closed |

| VIDEO MEMORY CONFIGURATION |
|---|
| Note: 1MB video memory is factory installed and is not configurable. The location is unidentified. |

| CPU SPEED SELECTION (CYRIX) | | | | | |
|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP8** | **JP15** | **JP17** |
| 120MHz | 50MHz | 2x | 1 & 4 | 1 & 2 | 2 & 3 |
| 150MHz | 60MHz | 2x | 2 & 5 | 1 & 2 | 2 & 3 |
| 166MHz | 66MHz | 2x | 3 & 6 | 1 & 2 | 2 & 3 |
| Note: Pins designated should be in the closed position. | | | | | |

| CPU SPEED SELECTION (IBM) | | | | | |
|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP8** | **JP15** | **JP17** |
| 120MHz | 50MHz | 2x | 1 & 4 | 1 & 2 | 2 & 3 |
| 150MHz | 60MHz | 2x | 2 & 5 | 1 & 2 | 2 & 3 |
| 166MHz | 66MHz | 2x | 3 & 6 | 1 & 2 | 2 & 3 |
| Note: Pins designated should be in the closed position. | | | | | |

| CPU SPEED SELECTION (INTEL) | | | | | |
|---|---|---|---|---|---|
| **CPU speed** | **Clock speed** | **Multiplier** | **JP8** | **JP15** | **JP17** |
| 100MHz | 66MHz | 1.5x | 3 & 6 | 1 & 2 | 1 & 2 |
| 120MHz | 60MHz | 2x | 2 & 5 | 1 & 2 | 2 & 3 |
| 133MHz | 66MHz | 2x | 3 & 6 | 1 & 2 | 2 & 3 |
| 150MHz | 60MHz | 2.5x | 2 & 5 | 2 & 3 | 2 & 3 |
| 166MHz | 66MHz | 2.5x | 3 & 6 | 2 & 3 | 2 & 3 |
| 200MHz | 66MHz | 3x | 3 & 6 | 2 & 3 | 1 & 2 |
| Note: Pins designated should be in the closed position. | | | | | |

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# IBM CORPORATION
# A P T I V A  2 1 3 6

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| CPU VOLTAGE SELECTION | |
|---|---|
| **Type** | **JP9** |
| Single | Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8 closed |
| Dual | Open |

| CPU VOLTAGE SELECTION (SINGLE) | |
|---|---|
| **Voltage** | **JP14** |
| 3.35v | Pins 2 & 3 closed |
| 3.5v | Pins 1 & 2 closed |

| CPU VOLTAGE SELECTION (DUAL) | | | |
|---|---|---|---|
| **Voltage** | **V core** | **JP13** | **JP14** |
| 3.35v | 2.5v | Pins 2 & 3 closed | Pins 2 & 3 closed |
| 3.35v | 2.8v | Pins 1 & 2 closed | Pins 2 & 3 closed |
| 3.5v | 2.5v | Pins 2 & 3 closed | Pins 1 & 2 closed |
| 3.5v | 2.8v | Pins 1 & 2 closed | Pins 1 & 2 closed |