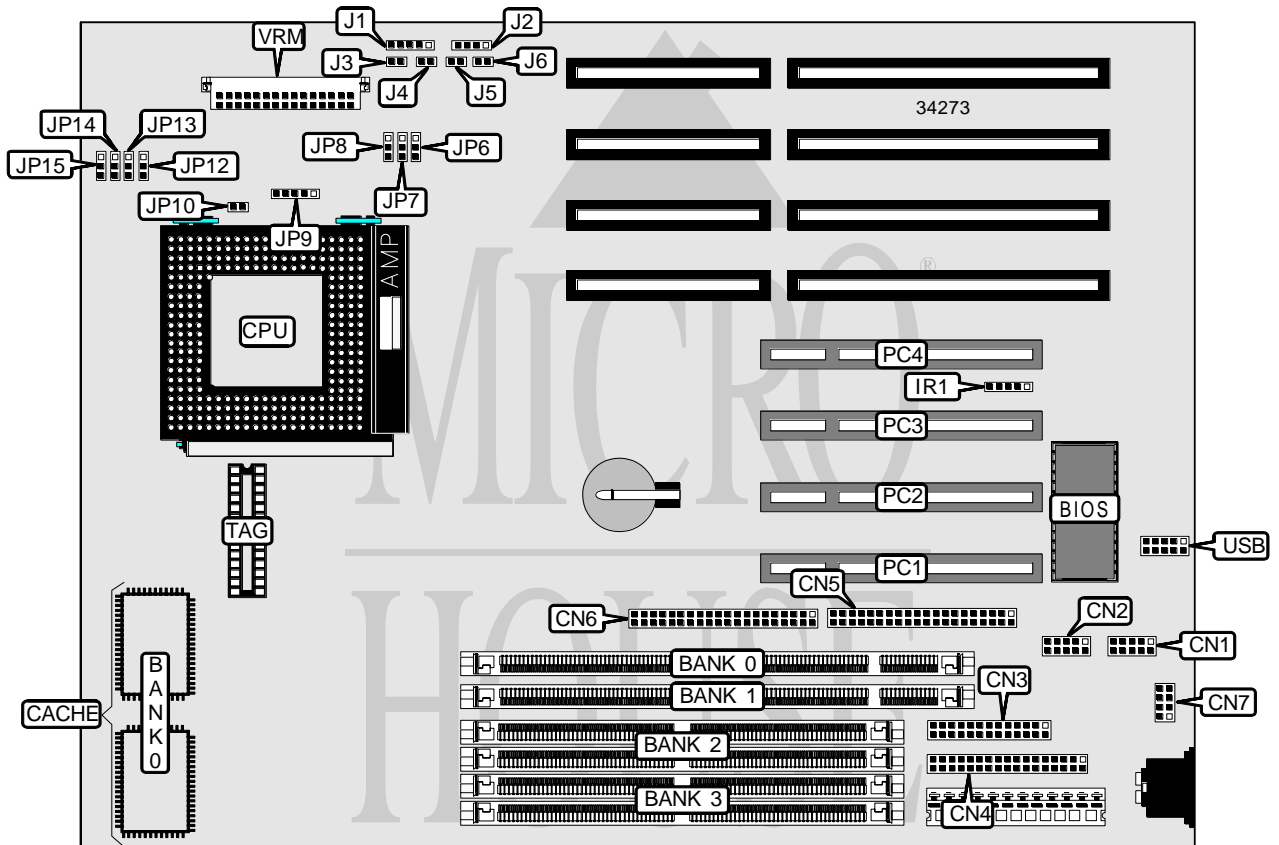


ACTIVEI SYSTEMS, INC. SARATOGA

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, VRM connector, USB connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Speaker	J2
Serial port 1	CN2	Green PC connector	J3
Parallel port	CN3	Turbo LED	J4
Floppy drive interface	CN4	Reset switch	J5
IDE interface 1	CN5	IDE interface LED	J6
IDE interface 2	CN6	32-bit PCI slots	PC1 – PC4
PS/2 mouse interface	CN7	USB connector	USB
IR connector	IR1	VRM connector	VRM
Power LED & keylock	J1		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP10	Open
CMOS memory clear	JP10	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	None	None	(2) 1M x 36	None
8MB	(1) 1M x 64	None	None	None
16MB	None	None	(2) 2M x 36	None
16MB	None	None	(2) 1M x 36	(2) 1M x 36
16MB	(1) 2M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
16MB	None	(1) 1M x 64	(2) 1M x 36	None
24MB	None	None	(2) 2M x 36	(2) 1M x 36
24MB	(1) 2M x 64	(1) 1M x 64	None	None
24MB	None	(1) 1M x 64	(2) 2M x 36	None
24MB	None	(1) 2M x 64	(2) 1M x 36	None
24MB	(1) 2M x 64	None	None	(2) 1M x 36
32MB	None	None	(2) 4M x 36	None
32MB	None	None	(2) 2M x 36	(2) 2M x 36
32MB	(1) 4M x 64	None	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	None	(1) 2M x 64	(2) 2M x 36	None
40MB	None	None	(2) 4M x 36	(2) 1M x 36
40MB	(1) 4M x 64	(1) 1M x 64	None	None
40MB	None	(1) 1M x 64	(2) 4M x 36	None
40MB	None	(1) 4M x 64	(2) 1M x 36	None
48MB	None	None	(2) 4M x 36	(2) 2M x 36
48MB	(1) 4M x 64	(1) 2M x 64	None	None

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DIMM/DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
48MB	None	(1) 2M x 64	(2) 4M x 36	None
48MB	None	(1) 4M x 64	(2) 2M x 36	None
64MB	None	None	(2) 8M x 36	None
64MB	None	None	(2) 4M x 36	(2) 4M x 36
64MB	(1) 8M x 64	None	None	None
64MB	(1) 4M x 64	(1) 4M x 64	None	None
64MB	None	(1) 4M x 64	(2) 4M x 36	None
72MB	None	None	(2) 8M x 36	(2) 1M x 36
72MB	(1) 8M x 64	(1) 1M x 64	None	None
72MB	None	(1) 1M x 64	(2) 8M x 36	None
72MB	None	(1) 8M x 64	(2) 1M x 36	None
80MB	None	None	(2) 8M x 36	(2) 2M x 36
80MB	(1) 8M x 64	(1) 2M x 64	None	None
80MB	None	(1) 2M x 64	(2) 8M x 36	None
80MB	None	(1) 8M x 64	(2) 2M x 36	None
96MB	None	None	(2) 8M x 36	(2) 4M x 36
96MB	(1) 8M x 64	(1) 4M x 64	None	None
96MB	None	(1) 4M x 64	(2) 8M x 36	None
96MB	None	(1) 8M x 64	(2) 4M x 36	None
128MB	None	None	(2) 8M x 36	(2) 8M x 36
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	None	(1) 8M x 64	(2) 8M x 36	None

Note: Board accepts EDO memory. Banks 2 & 3 are interchangeable if DIMMs are not installed.

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(2) 32K x 32	Unidentified
512KB	(2) 64K x 32	Unidentified

CPU SPEED SELECTION (CYRIX)						
CPU speed	Clock speed	Multiplier	JP6	JP7	JP8	JP9
120MHz	50MHz	2x	2 & 3	2 & 3	2 & 3	2 & 3
133MHz	55MHz	2x	2 & 3	1 & 2	2 & 3	2 & 3
150MHz	60MHz	2x	2 & 3	2 & 3	1 & 2	2 & 3
166MHz	66MHz	2x	1 & 2	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

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CPU SPEED SELECTION (AMD)						
CPU speed	Clock speed	Multiplier	JP6	JP7	JP8	JP9
75MHz	50MHz	1.5x	2 & 3	2 & 3	2 & 3	Open
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	Open
100MHz	66MHz	1.5x	1 & 2	2 & 3	2 & 3	Open
120MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	Open
133MHz	66MHz	1.5x	1 & 2	2 & 3	2 & 3	Open
150MHz	60MHz	1.75x	2 & 3	2 & 3	1 & 2	2 & 3, 4 & 5
166MHz	66MHz	1.75x	1 & 2	2 & 3	2 & 3	2 & 3, 4 & 5

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JP6	JP7	JP8	JP9
75MHz	50MHz	1.5x	2 & 3	2 & 3	2 & 3	Open
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	Open
100MHz	66MHz	1.5x	1 & 2	2 & 3	2 & 3	Open
120MHz	60MHz	2x	2 & 3	2 & 3	1 & 2	2 & 3
133MHz	66MHz	2x	1 & 2	2 & 3	2 & 3	2 & 3
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3, 4 & 5
166MHz	66MHz	2.5x	1 & 2	2 & 3	2 & 3	2 & 3, 4 & 5
200MHz	66MHz	3x	1 & 2	2 & 3	2 & 3	1 & 2, 4 & 5

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION (SINGLE)				
Voltage	JP12	JP13	JP14	JP15
3.3v	Pins 1 & 2 closed	Pins 1 & 2 closed	Open	Open
3.5v	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Open
3.5v (K5 FX only)	Pins 1 & 2 closed	Pins 1 & 2 closed	Open	Open

CPU VOLTAGE SELECTION (DUAL)					
Voltage	V core	JP12	JP13	JP14	JP15
3.3v	2.5v	2 & 3	2 & 3	2 & 3	2 & 3
3.3v	2.6v	2 & 3	2 & 3	2 & 3	1 & 2
3.3v	2.7v	2 & 3	2 & 3	1 & 2	2 & 3
3.3v	2.8v	2 & 3	2 & 3	Open	2 & 3
3.3v	2.9v	2 & 3	2 & 3	2 & 3	2 & 3
3.3v	3.2v	2 & 3	2 & 3	1 & 2	Open

Note: Pins designated should be in the closed position.