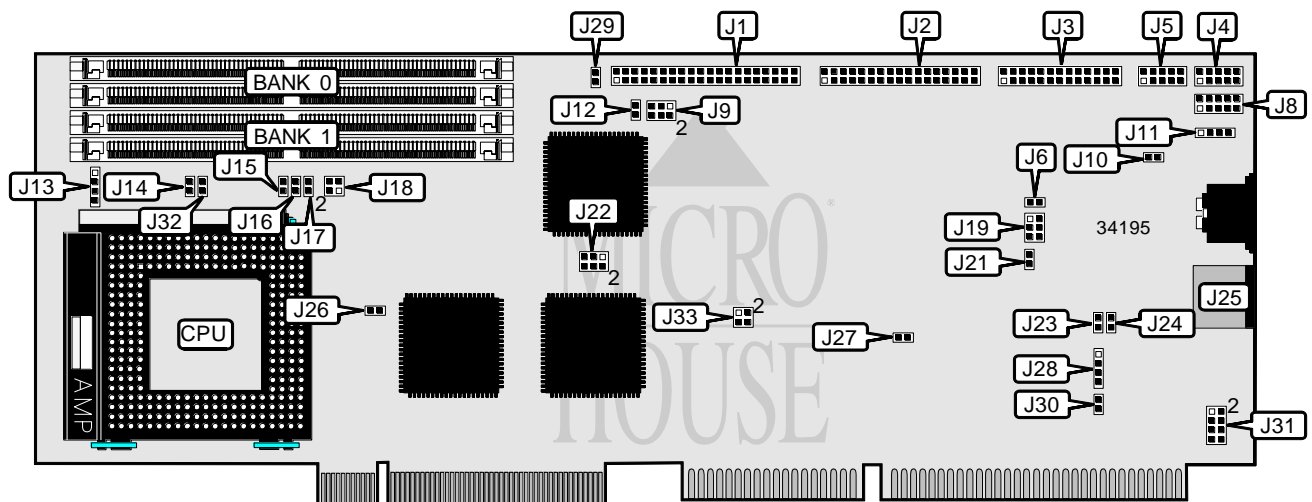


I-BUS SHARK II

Processor	Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	OPTI
Video Chip Set	None
Maximum Onboard Memory	256MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Unidentified
Dimensions	Unidentified
I/O Options	Floppy drive interface, green PC connector, IDE interface, parallel port, PS/2 mouse port, serial ports (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface	J1	Speaker	J11
Floppy drive interface	J2	Chassis fan power	J13
Parallel port	J3	Green PC connector	J16
Serial port 2	J4	PS/2 mouse port	J25
Serial port 1	J5	IDE interface LED	J27
Auxiliary keyboard connector	J8	External battery	J28
Reset switch	J10		

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í IDE IRQ14 enabled	J6	Closed
IRQ14 disabled	J6	Open
í Factory configured - do not alter	J15	Open
Cache always invalidates	J17	Open
Cache invalidates on writes only	J17	Closed
í Factory configured - do not alter	J18	Pins 1 & 4, 2 & 3 closed
Floppy drive size select 1.2MB/1.44MB	J19	Pins 1 & 2 closed
Floppy drive size select 2.88MB	J19	Pins 3 & 4, 5 & 6 closed
í Watchdog timer enabled	J21	Closed
Watchdog timer disabled	J21	Open
í Factory configured - do not alter	J23	Open
Password enabled	J24	Closed
Password disabled	J24	Open
í CMOS memory normal operation	J29	Open
CMOS memory clear	J29	Closed
PS/2 mouse IRQ12 enabled	J30	Closed
PS/2 mouse IRQ12 disabled	J30	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

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CACHE CONFIGURATION	
Note: The location of the 256KB/512KB cache is unidentified.	

CACHE JUMPER CONFIGURATION		
Size	J9	J12
256KB	Pins 1 & 3, 2 & 4 closed	Open
512KB	Pins 3 & 5, 4 & 6 closed	Closed

CPU SPEED SELECTION						
CPU speed	Clock speed	Multiplier	J14	J22	J26	J32
75MHz	50MHz	1.5x	Open	Open	1 & 2	Open
90MHz	60MHz	1.5x	Open	1 & 2, 3 & 4	1 & 2	Open
100MHz	66MHz	1.5x	Open	1 & 2	1 & 2	Open
120MHz	60MHz	2x	1 & 2	1 & 2, 3 & 4	Open	Open
133MHz	66MHz	2x	1 & 2	1 & 2	Open	Open
150MHz	60MHz	2.5x	1 & 2	1 & 2, 3 & 4	Open	1 & 2
166MHz	66MHz	2.5x	1 & 2	1 & 2	Open	1 & 2
200MHz	66MHz	3x	Open	1 & 2	Open	1 & 2

Note: Pins designated should be in the closed position.

DMA CHANNEL SELECTION	
Channel	J31
1	Pins 3 & 4, 7 & 8 closed
3	Pins 1 & 2, 5 & 6 closed

WATCH DOG TIMER SELECTION	
Setting	J33
IRQ11 for WDT only	Pins 1 & 3 closed
í IRQ11 shared with WDT & ISA bus	Pins 1 & 3, 2 & 4 closed
IRQ11 for ISA bus only	Pins 3 & 4 closed