**Processor** CX M1/AM K5/Pentium

**Processor Speed** 75/90/100/120/133/150/166/200MHz

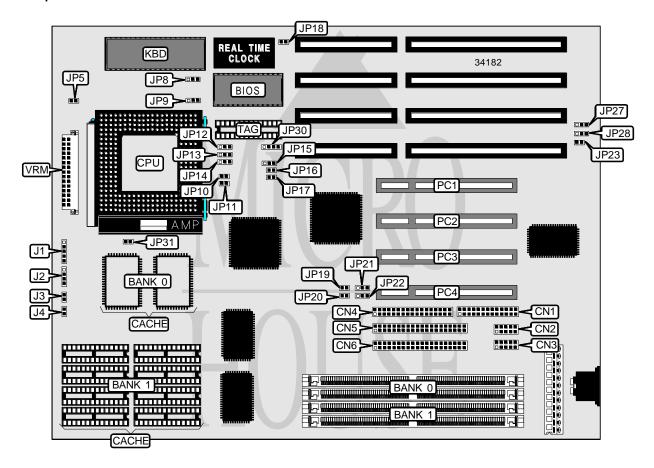
Chip Set Unidentified
Video Chip Set None
Maximum Onboard Memory 128MB
Maximum Video Memory None
Cache 256/512KB
BIOS AMI

**Dimensions** 290mm x 220mm

I/O Options 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces

(2), parallel port, serial ports (2), VRM connector

NPU Options None



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CONNECTIONS				
Purpose	Location	Purpose	Location	
Parallel port	CN1	Reset switch	J3	
Serial port 2	CN2	Turbo LED	J4	
Serial port 1	CN3	Green PC connector	JP19	
Floppy drive interface	CN4	IDE interface LED	JP20	
IDE interface 2	CN5	Chassis fan power	JP30	
IDE interface 1	CN6	32-bit PCI slots	PC1 – PC4	
Power LED & keylock	J1	VRM connector	VRM	
Speaker	J2			

USER CONFIGURABLE SETTINGS					
Function	Function Label Position				
í Cache type select write back	JP10	Open			
Cache type select write through	JP10	Closed			
í Pipeline mode enabled	JP11	Closed			
Pipeline mode disabled	JP11	Open			
í CMOS memory normal operation	JP18	Open			
CMOS memory clear	JP18	Closed			
í Parallel port mode select standard port	JP23	Closed			
Parallel port mode select EPP mode	JP23	Open			

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
2MB	(2) 256K x 36	None		
4MB	(2) 512K x 36	None		
6MB	(2) 512K x 36	(2) 256K x 36		
8MB	(2) 1M x 36	None		
8MB	(2) 512K x 36	(2) 512K x 36		
10MB	(2) 1M x 36	(2) 256K x 36		
12MB	(2) 1M x 36	(2) 512K x 36		
16MB	(2) 2M x 36	None		
16MB	(2) 1M x 36	(2) 1M x 36		
18MB	(2) 2M x 36	(2) 256K x 36		
20MB	(2) 2M x 36	(2) 512K x 36		
24MB	(2) 2M x 36	(2) 1M x 36		
32MB	(2) 4M x 36	None		
32MB	(2) 2M x 36	(2) 2M x 36		
34MB	(2) 256K x 36	(2) 4M x 36		
36MB	(2) 4M x 36	(2) 512K x 36		
40MB	(2) 4M x 36	(2) 1M x 36		
48MB	(2) 4M x 36	(2) 2M x 36		
64MB	(2) 8M x 36	None		
64MB	(2) 4M x 36	(2) 4M x 36		
66MB	(2) 8M x 36	(2) 256K x 36		

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DRAM CONFIGURATION (CON'T)					
Size	Bank 0	Bank 1			
68MB	(2) 8M x 36	(2) 512K x 36			
72MB	(2) 8M x 36	(2) 1M x 36			
80MB	(2) 8M x 36	(2) 2M x 36			
96MB	(2) 8M x 36	(2) 4M x 36			
128MB	(2) 8M x 36	(2) 8M x 36			

CACHE CONFIGURATION					
Size Bank 0 Bank 1 TAG					
256KB (A)	None	(8) 32K x 8	(1) 8K/16K/32K x 8		
256KB (B)	None				
512KB	None	(8) 64K x 8	(1) 16K/32K x 8		

CACHE JUMPER CONFIGURATION					
Size JP12 JP13 JP14					
256KB (A)	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed		
512KB	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed		

	CPU SPEED SELECTION (CYRIX)						
CPU speed	Clock speed	Multiplier	JP5	JP15	JP16	JP17	JP31
120MHz	50MHz	2x	Closed	1 & 2	Open	Open	Open
150MHz	60MHz	2x	Closed	2 & 3	Open	Closed	Open
166MHz	66MHz	2x	Closed	2 & 3	Closed	Closed	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP5	JP15	JP16	JP17	JP31
75MHz	50MHz	1.5x	Open	1 & 2	Open	Open	Open
90MHz 60MHz 1.5x Open 2 & 3 Open Closed Open							
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP5	JP15	JP16	JP17	JP31
75MHz	50MHz	1.5x	Open	1 & 2	Open	Open	Open
90MHz	60MHz	1.5x	Open	2 & 3	Open	Closed	Open
100MHz	66MHz	1.5x	Open	2 & 3	Closed	Closed	Open
120MHz	60MHz	2x	Closed	2 & 3	Open	Closed	Open
133MHz	66MHz	2x	Closed	2 & 3	Closed	Closed	Open
150MHz	60MHz	2.5x	Closed	2 & 3	Open	Closed	Closed
166MHz	66MHz	2.5x	Closed	2 & 3	Closed	Closed	Closed
200MHz	66MHz	3x	Open	2 & 3	Closed	Closed	Closed
Note: Pins design	gnated should be i	n the closed posi	tion.				

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DMA CHANNEL SELECTION				
Channel	JP27	JP28		
1	Pins 2 & 3 closed	Pins 2 & 3 closed		
3	Pins 1 & 2 closed	Pins 1 & 2 closed		

IDE FEATURE FUNCTION SELECTION				
Setting JP21 JP22				
Disabled Pins 2 & 3 closed Pins 2 & 3 closed				
Enabled	Pins 1 & 2 closed	Pins 1 & 2 closed		

	BIOS SELECTION	
Setting	JP8	JP9
í 5v	Pins 2 & 3 closed	Pins 1 & 2 closed
12v	Pins 2 & 3 closed	Pins 2 & 3 closed
EPROM	Pins 1 & 2 closed	Pins 1 & 2 closed