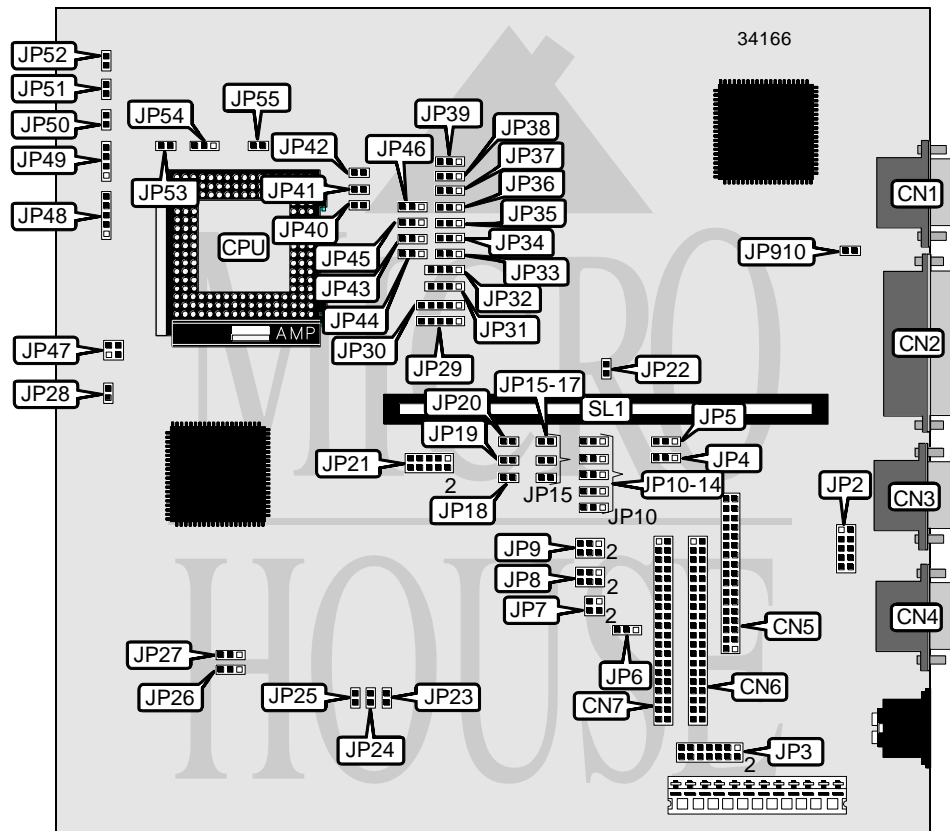


FORCOM TECHNOLOGY CORPORATION

FT-1307A, FT-1307N

Processor	S-series/AM486SX PLUS/80486SX/AM486DX PLUS/80486DX/CX M7/ 80486DX2/80486DX4/P24D/P24T/CX M9
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/80(internal)/ 100(internal)MHz
Chip Set	SIS
Video Chip Set	Cirrus Logic
Maximum Onboard Memory	32MB
Maximum Video Memory	Unidentified
Cache	128/256/512KB
BIOS	Award
Dimensions	250mm x 220mm
I/O Options	Ethernet 10BaseT connector, floppy drive interface, IDE interfaces (2), parallel port, serial ports (2), VGA port, riser slot
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	CN1	IDE interface LED	JP28
Parallel port	CN2	Chassis fan power	JP47
Serial port 1	CN3	Power LED & keylock	JP48
Serial port 2/Ethernet 10BaseT port	CN4	Speaker	JP49
Floppy drive interface	CN5	Turbo LED	JP50
IDE interface 1	CN6	Turbo switch	JP51
IDE interface 2	CN7	Reset switch	JP52
Serial port 2	JP2	Riser slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Floppy drive interface enabled	JP6	Pins 2 & 3 closed
Floppy drive interface disabled	JP6	Pins 1 & 2 closed
í Factory configured - do not alter	JP22	Unidentified
í Factory configured - do not alter	JP38	Unidentified
í On board video enabled	JP910	Closed
On board video disabled	JP910	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(1) 256K x 36	None
2MB	(1) 512K x 36	None
2MB	(1) 256K x 36	(1) 256K x 36
3MB	(1) 512K x 36	(1) 256K x 36
4MB	(1) 1M x 36	None
4MB	(1) 512K x 36	(1) 512K x 36
5MB	(1) 1M x 36	(1) 256K x 36
6MB	(1) 1M x 36	(1) 512K x 36
8MB	(1) 2M x 36	None
8MB	(1) 1M x 36	(1) 1M x 36
9MB	(1) 2M x 36	(1) 256K x 36
10MB	(1) 2M x 36	(1) 512K x 36
12MB	(1) 2M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None
16MB	(1) 2M x 36	(1) 2M x 36
17MB	(1) 4M x 36	(1) 256K x 36
18MB	(1) 4M x 36	(1) 512K x 36
20MB	(1) 4M x 36	(1) 1M x 36
24MB	(1) 4M x 36	(1) 2M x 36
32MB	(1) 4M x 36	(1) 4M x 36

Note: The location of banks 0 & 1 is unidentified.

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CACHE CONFIGURATION	
Size	Bank 0
128KB	(4) 32K x 8
256KB	(4) 64K x 8
512KB	(4) 128K x 8

Note: The location of bank 0 is unidentified.

CACHE JUMPER CONFIGURATION		
Size	JP26	JP27
128KB	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB	Pins 1 & 2 closed	Pins 2 & 3 closed
512KB	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU SPEED SELECTION			
Speed	JP23	JP24	JP25
25MHz	Open	Open	Closed
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed
75iMHz	Open	Open	Closed
80iMHz	Open	Closed	Closed
100iMHz	Closed	Closed	Closed

CPU TYPE SELECTION					
Type	JP29	JP30	JP31	JP32	JP33
S-series	1 & 2	4 & 5	1 & 2, 3 & 4	3 & 4	Open
AM486SX PLUS	4 & 5	4 & 5	2 & 3	3 & 4	Open
80486SX	Open	Open	2 & 3	Open	Open
AM486DX PLUS	4 & 5	4 & 5	1 & 2, 3 & 4	3 & 4	Open
80486DX	Open	Open	1 & 2, 3 & 4	Open	Open
CX M7	Open	2 & 3	1 & 2, 3 & 4	2 & 3	2 & 3
80486DX2	Open	Open	1 & 2, 3 & 4	Open	Open
80486DX4	Open	4 & 5	1 & 2, 3 & 4	3 & 4	Open
P24D	2 & 3	4 & 5	1 & 2, 3 & 4	1 & 2, 3 & 4	Open
P24T (WB)	Open	1 & 2	1 & 2	3 & 4	3 & 4
P24T (WT)	Open	1 & 2	1 & 2	3 & 4	3 & 4
CX M9	2 & 3	4 & 5	1 & 2, 3 & 4	1 & 2, 3 & 4	Open

Note: Pins designated should be in the closed position.

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CPU TYPE SELECTION (CON'T)					
Type	JP34	JP35	JP36	JP37	JP39
S-series	Open	2 & 3	2 & 3	2 & 3	Open
AM486SX PLUS	1 & 2	Open	2 & 3	2 & 3	Open
80486SX	Open	Open	2 & 3	Open	Open
AM486DX PLUS	1 & 2	2 & 3	2 & 3	2 & 3	Open
80486DX	Open	2 & 3	2 & 3	Open	Open
CX M7	2 & 3	2 & 3	2 & 3	1 & 2	Open
80486DX2	Open	2 & 3	2 & 3	Open	Open
80486DX4	Open	2 & 3	2 & 3	2 & 3	Open
P24D	1 & 2	2 & 3	1 & 2	2 & 3	Open
P24T (WB)	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
P24T (WT)	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
CX M9	1 & 2	2 & 3	1 & 2	2 & 3	Open

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION (CON'T)			
Type	JP45	JP53	JP55
S-series	Open	Open	Closed
AM486SX PLUS	Pins 2 & 3 closed	Open	Closed
80486SX	Open	Open	Open
AM486DX PLUS	Pins 2 & 3 closed	Open	Closed
80486DX	Open	Open	Open
CX M7	Open	Open	Closed
80486DX2	Open	Open	Open
80486DX4	Open	Open	Closed
P24D	Pins 1 & 2 closed	Open	Closed
P24T (WB)	Open	Open	Closed
P24T (WT)	Open	Open	Closed
CX M9	Pins 1 & 2 closed	Closed	Closed

CPU INTERNAL CLOCK SELECTION		
Type	JP46	JP54
CX M7	Pins 2 & 3 closed	Pins 2 & 3 closed
80486DX2	Pins 2 & 3 closed	Pins 2 & 3 closed
80486DX4	Open	Pins 1 & 2 closed
CX M9	Open	Pins 1 & 2 closed

CPU VOLTAGE SELECTION					
Voltage	JP40	JP41	JP42	JP43	JP44
3v	Open	Closed	Open	2 & 3	2 & 3
3.3v	Open	Open	Closed	2 & 3	2 & 3
3.45v	Closed	Open	Open	2 & 3	2 & 3
5v	Open	Open	Open	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

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DMA CHANNEL SELECTION		
Channel	JP7	JP9
í 1	Pins 1 & 2 closed	Pins 3 & 4 closed
3	Pins 3 & 4 closed	Pins 5 & 6 closed
1 way	N/A	Pins 1 & 2 closed
2 way	N/A	Open

PARALLEL PORT ADDRESS SELECTION		
Setting	JP11	JP12
278	Pins 1 & 2 closed	Pins 2 & 3 closed
í 378	Pins 2 & 3 closed	Pins 1 & 2 closed
3BC	Pins 2 & 3 closed	Pins 2 & 3 closed
Disabled	Pins 1 & 2 closed	Pins 1 & 2 closed

SERIAL PORT 1 ADDRESS SELECTION		
Setting	JP13	JP14
2E8	Pins 2 & 3 closed	Pins 2 & 3 closed
3E8	Pins 2 & 3 closed	Pins 1 & 2 closed
í 3F8	Pins 1 & 2 closed	Pins 2 & 3 closed
Disabled	Pins 1 & 2 closed	Pins 1 & 2 closed

SERIAL PORT 2 ADDRESS SELECTION		
Setting	JP4	JP5
2E8	Pins 2 & 3 closed	Pins 1 & 2 closed
í 2F8	Pins 1 & 2 closed	Pins 2 & 3 closed
3E8	Pins 2 & 3 closed	Pins 2 & 3 closed
Disabled	Pins 1 & 2 closed	Pins 1 & 2 closed

SERIAL PORT 1 INTERRUPT SELECTION			
IRQ	JP15	JP16	JP17
IRQ3	Closed	Open	Open
í IRQ4	Open	Closed	Open
IRQ5	Open	Open	Closed

SERIAL PORT 2 INTERRUPT SELECTION			
IRQ	JP18	J19	JP20
í IRQ3	Closed	Open	Open
IRQ4	Open	Closed	Open
IRQ5	Open	Open	Closed

PARALLEL PORT INTERRUPT SELECTION	
IRQ	JP10
IRQ5	Pins 1 & 2 closed
í IRQ7	Pins 2 & 3 closed

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PARALLEL PORT MODE SELECTION	
Setting	JP8
í EPP/SPP	Pins 2 & 4, 3 & 5 closed
EPP/ECP	Pins 1 & 3, 4 & 6 closed
Printer	Pins 1 & 3, 2 & 4 closed
EXT2FDD	Pins 3 & 5, 4 & 6 closed

ETHERNET ADDRESS SELECTION	
Setting	JP3
200h	Pins 1 & 2 closed
220h	Pins 1 & 2, 5 & 6 closed
240h	Pins 1 & 2, 3 & 4 closed
260h	Pins 1 & 2, 3 & 4, 5 & 6 closed
í 300h	Open
320h	Pins 5 & 6 closed
340h	Pins 3 & 4 closed
360h	Pins 3 & 4, 5 & 6 closed

ETHERNET INTERRUPT SELECTION	
IRQ	JP21
3	Pins 5 & 6 closed
í 5	Pins 3 & 4 closed
9	Pins 1 & 2 closed
10	Pins 7 & 8 closed
11	Pins 9 & 10 closed

MISCELLANEOUS TECHNICAL NOTE

JP3 pins 7 & 8, 9 & 10, 11 & 12, 13 & 14 set the boot ROM address. The addresses are unidentified.