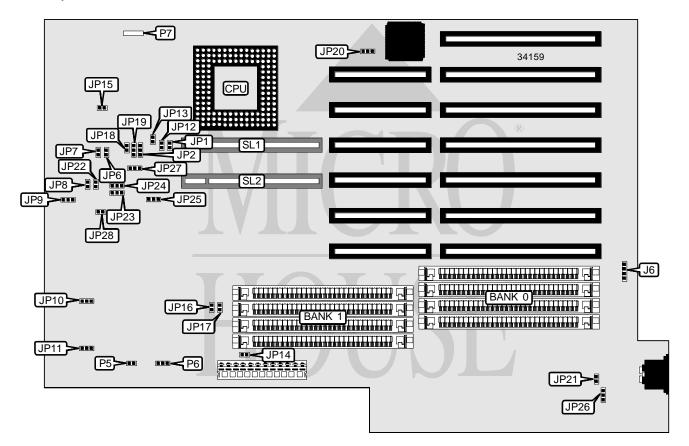
RINGO TECHNOLOGY, INC. RT486SX/DX (VLB)

Processor
Processor Speed
Chip Set
Video Chip Set
Maximum Onboard Memory
Maximum Video Memory
Cache
BIOS
Dimensions
I/O Options
NPU Options

804865X/80486DX/80486DX2 33/50(internal)/50/66(internal)MHz OPTI None 64MB None 256KB AMI 330mm x 218mm 32-bit VESA local bus slots (2) None



CONNECTIONS					
Purpose Location Purpose Location					
External battery	J6	Chassis fan power	P7		
Speaker	P5	32-bit VESA local bus slots	SL1 & SL2		
Power LED & keylock	P6				

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USER CONFIGURABLE SETTINGS					
Function	Label	Position			
í Battery type select internal	JG	Pins 2 & 3 closed			
Battery type select Vectra battery	JG	Open			
CMOS memory clear	JG	Pins 3 & 4 closed			
í Factory configured - do not alter	JP1	Open			
í Factory configured - do not alter	JP12	Open			
í Factory configured - do not alter	JP13	Open			
í Monitor type select color/EGA/VGA	JP21	Closed			
Monitor type select monochrome	JP21	Open			
í Factory configured - do not alter	JP23	Open			
í Standard VESA card in SL1	JP24	Pins 2 & 3 closed			
í Battery type select Ni Cad	JP26	Pins 2 & 3 closed			
í Factory configured - do not alter	JP27	Pins 1 & 2 closed			
í Factory configured - do not alter	JP28	Closed			

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	None
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	None

CACHE CONFIGURATION

Note: Board is factory installed with 256KB cache and is not configurable. The location of the cache is unidentified.

CACHE JUMPER CONFIGURATION							
Size JP10 JP11 JP16 JP17							
256KB	Pins 2 & 3 closed	Pins 2 & 3 closed	Closed	Closed			

CPU SPEED SELECTION						
Speed	JP6	JP7	JP8	JP9	JP22	
33MHz	Open	Open	Closed	1&2	Open	
50iMHz	Open	Open	Open	1&2	Closed	
50MHz	Open	Closed	Open	1&2	Open	
66iMHz	Open	Open	Closed	1&2	Open	
Note: Pins designa	nted should be in th	e closed position.			-	

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CPU TYPE SELECTION							
Туре	JP2	JP14	JP15	JP18	JP19	JP20	JP25
80486SX	Open	Closed	Closed	Open	2&3	1&2	2&3
80486DX	Closed	Closed	Closed	Closed	1&2	1&2	2&3
80486DX2	Closed	Closed	Closed	Closed	1&2	1&2	2&3
Note: Pins designated should be in the closed position.							