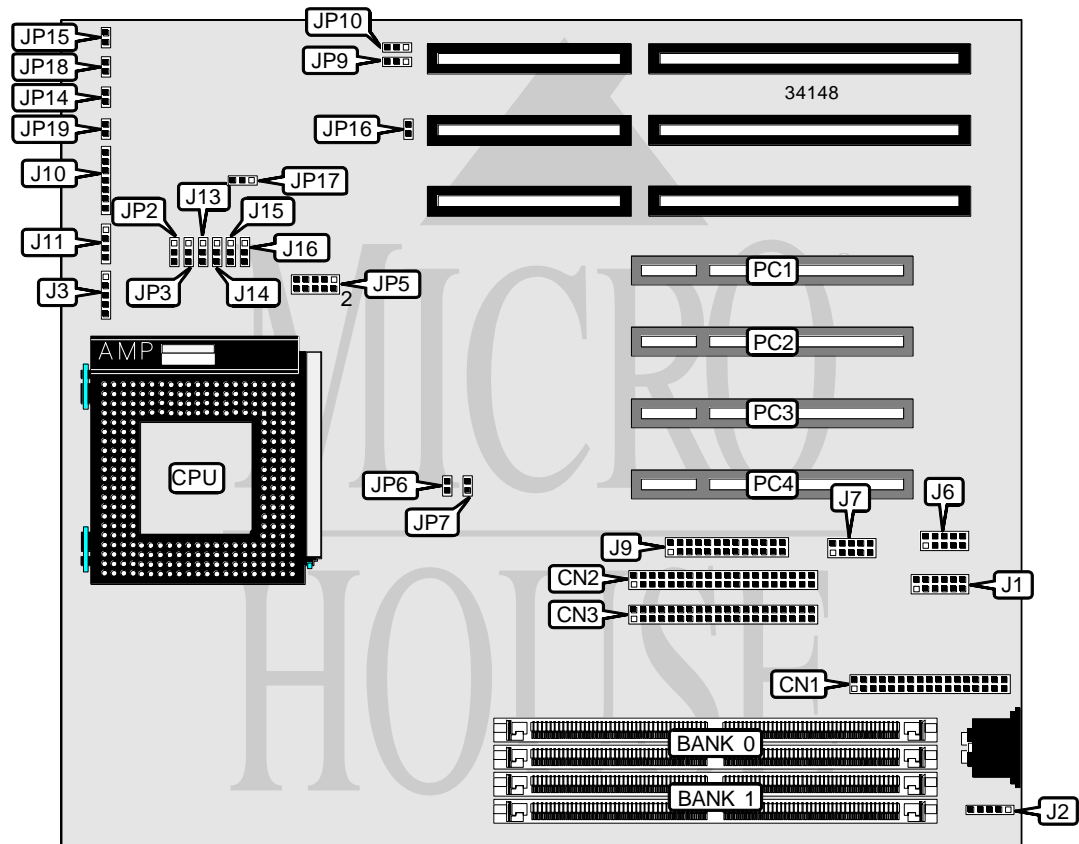


VTECH INDUSTRIES, INC.

MB520N

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI
Dimensions	230mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, USB connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Floppy drive interface	CN1	Parallel port	J9
IDE interface 2	CN2	IR connector	J10
IDE interface 1	CN3	Speaker	J11
USB connector	J1	IDE interface LED	JP14
PS/2 mouse interface	J2	Green PC connector	JP15
Power LED & keylock	J3	Chassis fan power	JP18
Serial port 2	J6	Reset switch	JP19
Serial port 1	J7	32-bit PCI slots	PC1 – PC4

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Flash BIOS type select SST/Winbond/ATMEL/MX	JP9	Pins 1 & 2 closed
Flash BIOS type select Intel	JP9	Pins 2 & 3 closed
Flash BIOS voltage select 12v	JP10	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP10	Pins 1 & 2 closed
í CMOS memory normal operation	JP16	Open
CMOS memory clear	JP16	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory. Banks are interchangeable.

CACHE CONFIGURATION
Note: The location of the banks is unidentified. The size of the chips is unidentified.

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CPU SPEED SELECTION (AMD)						
CPU speed	Clock speed	Multiplier	JP2	JP3	JP6	JP7
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed
120MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open
133MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed
150MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open
166MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CYRIX)						
CPU speed	Clock speed	Multiplier	JP2	JP3	JP6	JP7
120MHz	50MHz	2x	1 & 2	2 & 3	Closed	Closed
133MHz	55MHz	2x	1 & 2	2 & 3	Open	Open
150MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open
166MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed
200MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JP2	JP3	JP6	JP7
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed
120MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open
133MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed
150MHz	60MHz	2.5x	2 & 3	2 & 3	Closed	Open
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	Closed
180MHz	60MHz	3x	2 & 3	1 & 2	Closed	Open
200MHz	66MHz	3x	2 & 3	1 & 2	Open	Closed

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION				
Type	J13	J14	J15	J16
CX 6X86	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
AM K5 (B, C, F)	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
AM K5 (H, J, K)	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
P54C	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
P55C	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

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CPU VOLTAGE SELECTION (SINGLE)		
Voltage	JP5	JP17
3.38v	Pins 7 & 8 closed	Pins 2 & 3 closed
3.52v	Pins 9 & 10 closed	Pins 1 & 2 closed

CPU VOLTAGE SELECTION (DUAL)			
Voltage	V core	JP5	JP17
3.38v	2.5v	Pins 1 & 2 closed	Pins 2 & 3 closed
3.38v	2.7v	Pins 3 & 4 closed	Pins 2 & 3 closed
3.38v	2.93v	Pins 5 & 6 closed	Pins 2 & 3 closed
3.52v	2.5v	Pins 1 & 2 closed	Pins 1 & 2 closed
3.52v	2.7v	Pins 3 & 4 closed	Pins 1 & 2 closed
3.52v	2.93v	Pins 5 & 6 closed	Pins 1 & 2 closed