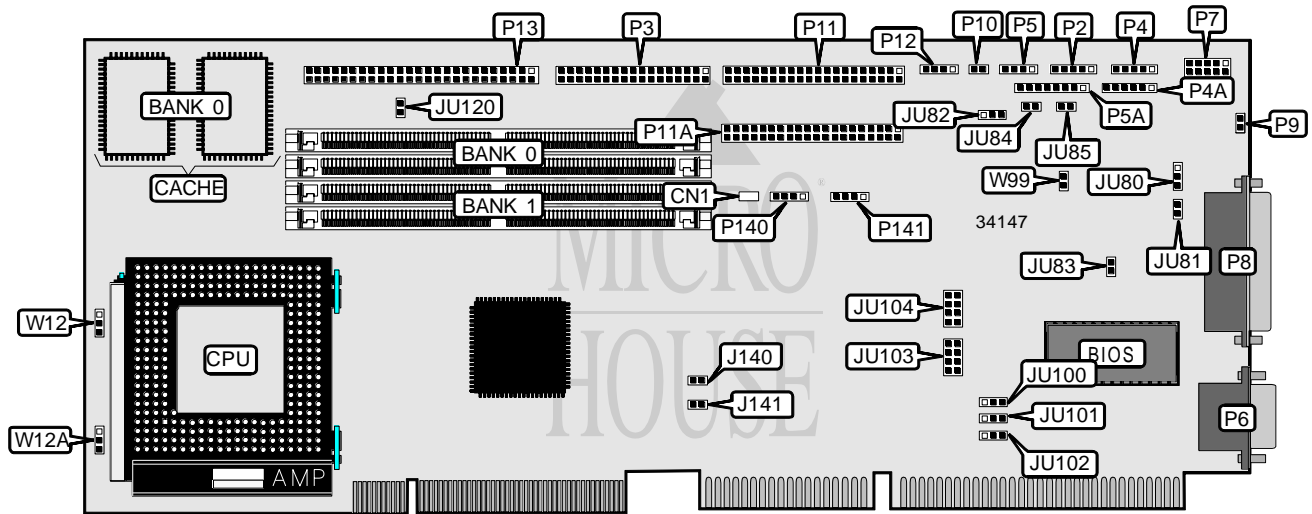


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TR-T2PCI

Processor	Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	256MB (EDO supported)
Maximum Video Memory	None
Cache	512KB
BIOS	AMI
Dimensions	Unidentified
I/O Options	Floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), USB connectors (2), combo I/O connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Temperature monitor	CN1	Parallel port	P8
Power LED & keylock	P2	Chassis fan power	P9
Floppy drive interface	P3	Reset switch	P10
Keyboard connector	P4	IDE interface 1	P11
PS/2 mouse interface	P4A	IDE interface 2	P11A
Speaker	P5	IDE interface LED	P12
Combo I/O connector	P5A	SCSI interface	P13
Serial port 1	P6	USB connector 1	P140
Serial port 2	P7	USB connector 2	P141

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TR - T2PCI

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Monitor type select color	JU80	Pins 1 & 2 closed
Monitor type select monochrome	JU80	Pins 2 & 3 closed
í Password normal operation	JU81	Open
Password clear	JU81	Closed
í Watchdog timer reset	JU82	Pins 1 & 2 closed
Watchdog timer normal operation	JU82	Pins 2 & 3 closed
í CMOS memory normal operation	JU83	Open
CMOS memory clear	JU83	Closed
í Speaker data to pin 8 of P5A enabled	JU84	Closed
Speaker data to pin 8 of P5A disabled	JU84	Open
í Reset data to pin 1 of P5A enabled	JU85	Closed
Reset data to pin 1 of P5A disabled	JU85	Open
í Serial port 1 IRQ select IRQ4	JU100	Pins 2 & 3 closed
Serial port 1 IRQ select IRQ3	JU100	Pins 1 & 2 closed
í Serial port 2 IRQ select IRQ3	JU101	Pins 1 & 2 closed
Serial port 2 IRQ select IRQ4	JU101	Pins 2 & 3 closed
í Parallel port IRQ select IRQ7	JU102	Pins 2 & 3 closed
Parallel port IRQ select IRQ5	JU102	Pins 1 & 2 closed
í SCSI term power enabled	JU120	Open
SCSI term power disabled	JU120	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

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TR-T2PCI

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory.

CACHE CONFIGURATION	
Size	Bank 0
512KB	(2) 64K x 32

CPU SPEED SELECTION							
CPU speed	Clock speed	Multiplier	J140	J141	W12	W12A	W99
75MHz	50MHz	1.5x	Closed	Closed	2 & 3	2 & 3	Open
90MHz	60MHz	1.5x	Closed	Open	2 & 3	2 & 3	Closed
100MHz	66MHz	1.5x	Open	Closed	2 & 3	2 & 3	Closed
120MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3	Closed
133MHz	66MHz	2x	Open	Closed	1 & 2	2 & 3	Closed
150MHz	60MHz	2.5x	Closed	Open	1 & 2	1 & 2	Closed
166MHz	66MHz	2.5x	Open	Closed	1 & 2	1 & 2	Closed
200MHz	66MHz	3x	Open	Closed	2 & 3	1 & 2	Closed

Note: Pins designated should be in the closed position.

DMA CHANNEL SELECTION		
Channel	JU103	JU104
3	Pins 7 & 8 closed	Pins 7 & 8 closed
5	Pins 5 & 6 closed	Pins 5 & 6 closed
6	Pins 3 & 4 closed	Pins 3 & 4 closed
7	Pins 1 & 2 closed	Pins 1 & 2 closed