# TYAN COMPUTER CORPORATION S 1 4 6 9

Processor CX M1/IBM/SGS/Pentium

**Processor Speed** 75/90/100/120/133/150/166/200MHz

Chip SetIntelVideo Chip SetNone

Maximum Onboard Memory 128MB (EDO supported)

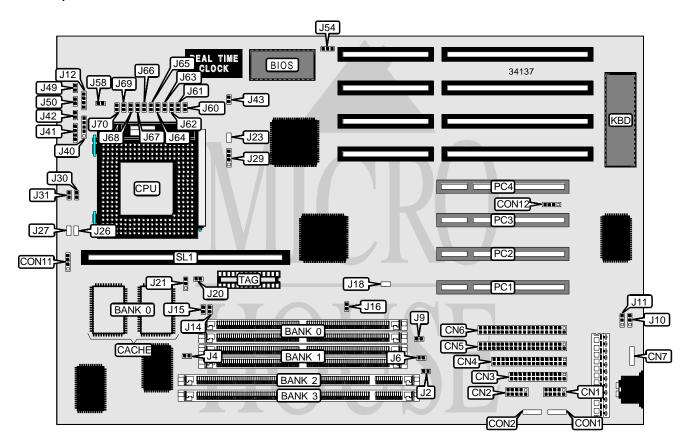
Maximum Video Memory1MBCache256/512KBBIOSAMI/AwardDimensions330mm x 218mm

I/O Options 32-bit PCI slots (4), bus mouse port, CD-ROM interface, cassette drive interface,

Ethernet 10Base2 connector, floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), cache slot, IR

connector, VRM connector, USB connector

NPU Options None



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CONNECTIONS				
Purpose	Location	Purpose	Location	
Serial port 1	CN1	IDE interface LED	J12 pins 1 & 2	
Serial port 2	CN2	Power LED & keylock	J40	
Parallel port	CN3	Speaker	J41	
Floppy drive interface	CN4	Turbo switch	J42	
IDE interface 1	CN5	Reset switch	J49	
IDE interface 2	CN6	Turbo LED	J50	
PS/2 mouse interface	CN7	32-bit PCI slots	PC1 – PC4	
IR connector	CON11	Cache slot	SL1	
IR connector	CON12			

USER CONFIGURABLE SETTINGS				
Function	Label	Position		
í Factory configured - do not alter	CON1	Unidentified		
í Factory configured - do not alter	CON2	Unidentified		
í Factory configured - do not alter	J18	Unidentified		
í Factory configured - do not alter	J23	Unidentified		
í Factory configured - do not alter	J27	Unidentified		
í Factory configured - do not alter	J28	Unidentified		
í CMOS memory normal operation	J43	Open		
CMOS memory clear	J43	Closed		
í Factory configured - do not alter	J54	Pins 1 & 2 closed		

	DIN	MM/DRAM CONFIGURAT	TION	
Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(2) 1M x 32	None	None	None
8MB	None	None	(1) 1M x 64	None
16MB	(2) 2M x 32	None	None	None
16MB	None	None	(1) 2M x 64	None
16MB	(2) 1M x 32	(2) 1M x 32	None	None
32MB	(2) 4M x 36	None	None	None
32MB	None	None	(1) 4M x 64	None
32MB	(2) 2M x 32	(2) 2M x 32	None	None
64MB	(2) 8M x 32	None	None	None
64MB	(2) 4M x 32	(2) 4M x 32	None	None
64MB	(2) 4M x 32	None	None	(1) 4M x 64
64MB	None	None	(1) 4M x 64	(1) 4M x 64
96MB	(2) 8M x 32	(2) 4M x 32	None	None
96MB	(2) 8M x 32	None	None	(1) 4M x 64
128MB	(2) 8M x 32	(2) 8M x 32	None	None
Note: Board accept	ts EDO memory.			

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DRAM VOLTAGE CONFIGURATION							
Voltage	J2	J4	J6	19	J14	J15	J16
3.3v	Open	Closed	Open	Open	Closed	Closed	Open
5v	Closed	Open	Closed	Closed	Open	Open	Closed

CACHE CONFIGURATION					
Size Bank 0 SL1 TAG					
256KB	256KB (2) 32K x 32		Unidentified		
512KB	(2) 32K x 32	256KB module installed	None		

CACHE JUMPER CONFIGURATION			
Setting	J21		
On board cache only	Pins 2 & 3 closed		
Cache module	Pins 1 & 2 closed		

CACHE TAG CONFIGURATION			
Size	J20		
Enabled	Closed		
Disabled (when using cache module)	Open		

CPU SPEED SELECTION (CYRIX)						
CPU speed Clock speed Multiplier J29 J30 J31						
120MHz	50MHz	1.5x	1 & 2, 3 & 4	Closed	Open	
150MHz	60MHz	1.5x	3 & 4	Closed	Open	
166MHz 66MHz 1.5x 1 & 2 Closed Op						
Note: Pins design	Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (IBM)						
CPU speed Clock speed Multiplier J29 J30 J31						
120MHz	50MHz	1.5x	1 & 2, 3 & 4	Closed	Open	
150MHz	60MHz	1.5x	3 & 4	Closed	Open	
166MHz	66MHz	1.5x	1 & 2	Closed	Open	
Note: Pins designa	Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (SGS)							
CPU speed Clock speed Multiplier J29 J30 J31							
120MHz	50MHz	1.5x	1 & 2, 3 & 4	Closed	Open		
150MHz	60MHz	1.5x	3 & 4	Closed	Open		
166MHz	Closed	Open					
Note: Pins designa	Note: Pins designated should be in the closed position.						

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	CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	J29	J30	J31		
75MHz	50MHz	1.5x	1 & 2, 3 & 4	Open	Open		
90MHz	60MHz	1.5x	3 & 4	Open	Open		
100MHz	66MHz	1.5x	1 & 2	Open	Open		
120MHz	60MHz	2x	3 & 4	Closed	Open		
133MHz	66MHz	2x	1 & 2	Closed	Open		
150MHz	60MHz	2.5x	3 & 4	Closed	Closed		
166MHz	66MHz	2.5x	1 & 2	Closed	Closed		
200MHz	66MHz	3x	1 & 2	Open	Closed		
Note: Pins design	ated should be in th	e closed position.					

CPU VOLTAGE SELECTION (SINGLE)						
Voltage J58 J60 J61 J62 J63 J64						
3.3v	Open	Open	Closed	Open	Open	Open
í 3.5v	Open	Open	Open	Closed	Open	Open
3.6v	Open	Closed	Open	Open	Open	Open

CPU VOLTAGE SELECTION (SINGLE, CON'T)						
Voltage	J65	J66	J67	J68	J69	J70
3.3v	Open	Open	Closed	Closed	Closed	Closed
í 3.5v	Open	Open	Closed	Closed	Closed	Closed
3.6v	Open	Open	Closed	Closed	Closed	Closed

CPU VOLTAGE SELECTION (DUAL)							
Voltage	V core	J58	J60	J61	J62	J63	J64
3.3v	2.65v	Closed	Open	Closed	Open	Closed	Open
3.3v	2.85v	Closed	Open	Closed	Open	Open	Closed
3.3v	3v	Closed	Open	Closed	Open	Open	Open
3.5v	2.65v	Closed	Open	Open	Closed	Closed	Open
3.5v	2.85v	Closed	Open	Open	Closed	Open	Closed
3.5v	3v	Closed	Open	Open	Closed	Open	Open
3.6v	2.65v	Closed	Closed	Open	Open	Closed	Open
3.6v	2.85v	Closed	Closed	Open	Open	Open	Closed
3.6v	3v	Closed	Closed	Open	Open	Open	Open

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CPU VOLTAGE SELECTION (DUAL, CON'T)							
Voltage	V core	J65	J66	J67	J68	J69	J70
3.3v	2.65v	Open	Closed	Open	Open	Open	Open
3.3v	2.85v	Open	Closed	Open	Open	Open	Open
3.3v	3v	Closed	Closed	Open	Open	Open	Open
3.5v	2.65v	Open	Closed	Open	Open	Open	Open
3.5v	2.85v	Open	Closed	Open	Open	Open	Open
3.5v	3v	Closed	Closed	Open	Open	Open	Open
3.6v	2.65v	Open	Closed	Open	Open	Open	Open
3.6v	2.85v	Open	Closed	Open	Open	Open	Open
3.6v	3v	Closed	Closed	Open	Open	Open	Open

	SERIAL PORT SELECTION	
Setting	J10	J11
Used as serial port	Pins 1 & 2 closed	Pins 1 & 2 closed
Used as IR port	Pins 2 & 3 closed	Pins 2 & 3 closed