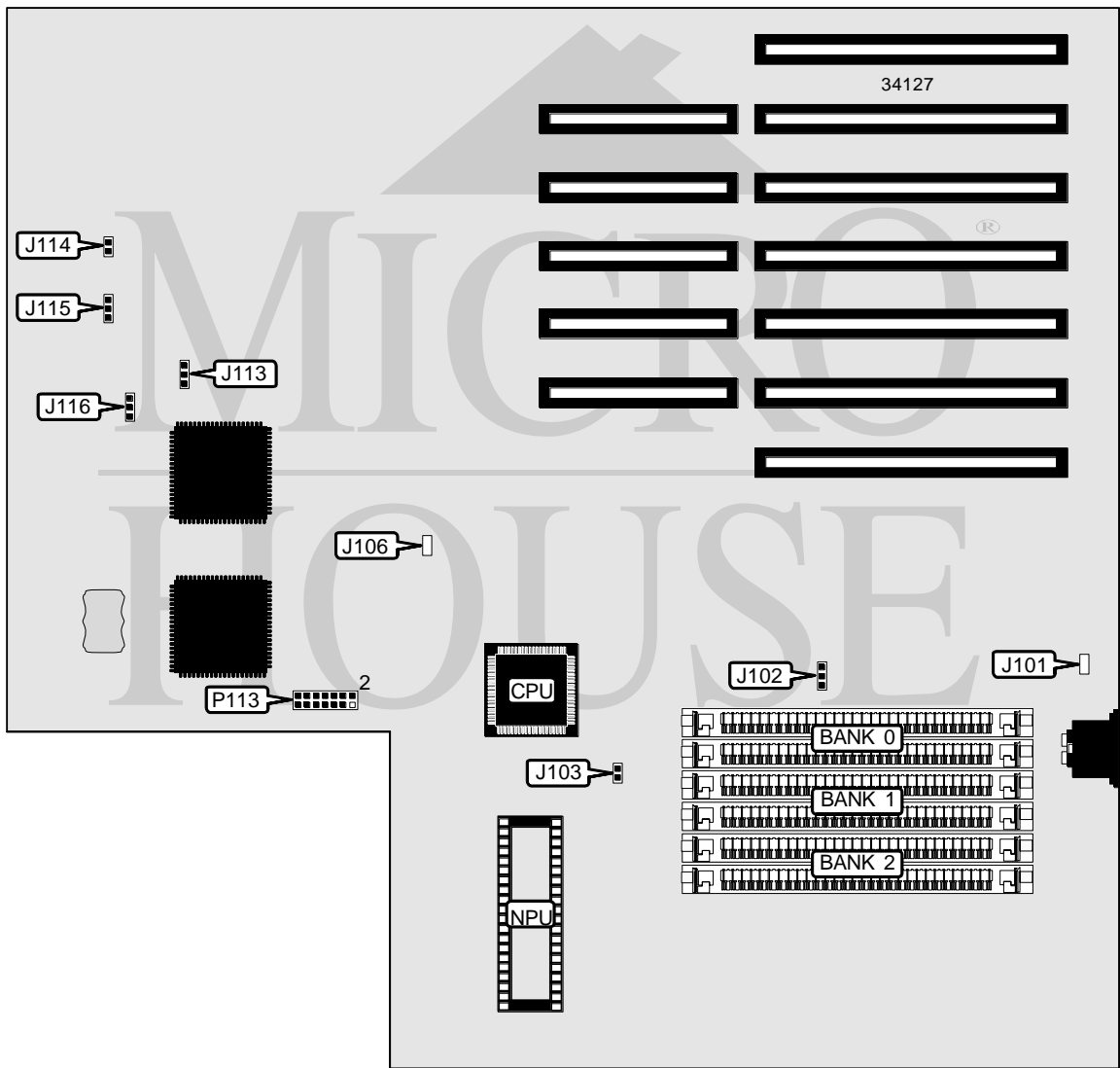


ZENITH DATA SYSTEMS

Z-248/12

Processor	80286
Processor Speed	12MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	6MB
Maximum Video Memory	None
Cache	None
BIOS	Unidentified
Dimensions	355mm x 304mm
I/O Options	None
NPU Options	80287



Continued on next page. . .

ZENITH DATA SYSTEMS

Z-248/12

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	J101	Unidentified
í NPU clock mode select /3	J103	Closed
NPU clock mode select direct	J103	Open
í Factory configured - do not alter	J106	Unidentified
í Factory configured - do not alter	J113	Pins 1 & 2 closed
í Speaker enabled	J114	Closed
Speaker disabled	J114	Open
í Chip at U-169 is Poach 7	J115	Pins 1 & 2 closed
Chip at U-169 is Poach 1	J115	Pins 2 & 3 closed
í Factory configured - do not alter	J116	Pins 2 & 3 closed

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
1MB	(2) 256K x 9	(2) 256K x 9	None
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9

DRAM JUMPER CONFIGURATION	
Size	J102
1MB	Pins 2 & 3 closed
3MB	Pins 2 & 3 closed
6MB	Pins 1 & 2 closed

NPU SPEED SELECTION		
Speed	Type	P113
6MHz	80287	Pins 13 & 14 closed
8MHz	80C87	Pins 13 & 14 closed
8MHz	80287	Pins 7 & 8 closed
12MHz	80C87	Pins 7 & 8 closed