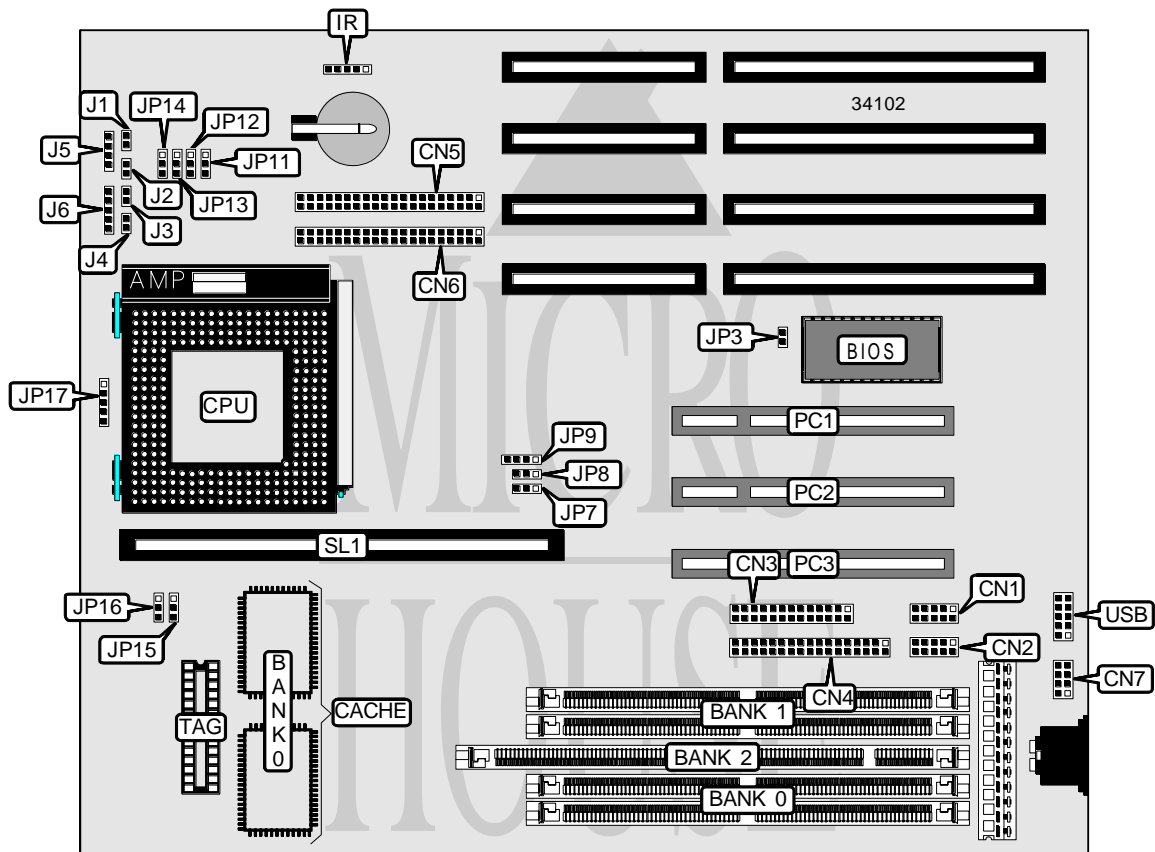


GENOA SYSTEMS CORPORATION TURBOEXPRESS 586-VX

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), cache slot, IR connector, USB connector
NPU Options	None



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GENOA SYSTEMS CORPORATION TURBOEXPRESS 586-FX

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Turbo LED	J2
Serial port 1	CN2	Green PC connector	J3
Parallel port	CN3	IDE interface LED	J4
Floppy drive interface	CN4	Speaker	J5
IDE interface 2	CN5	Power LED & keylock	J6
IDE interface 1	CN6	32-bit PCI slots	PC1 – PC3
PS/2 mouse interface	CN7	Cache slot	SL1
IR connector	IR	USB connector	USB
Reset switch	J1		

USER CONFIGURABLE SETTINGS		
í CMOS memory normal operation	JP3	Open
CMOS memory clear	JP3	Closed

DIMM/DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
8MB	None	(2) 1M x 36	None
8MB	None	None	(1) 1M x 64
16MB	None	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36	None
16MB	None	None	(1) 2M x 64
16MB	None	(2) 1M x 36	(1) 1M x 64
24MB	(2) 1M x 36	(2) 2M x 36	None
24MB	None	(2) 2M x 36	(1) 1M x 64
32MB	None	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36	None
32MB	None	None	(1) 4M x 64
32MB	None	(2) 2M x 36	(1) 2M x 64
40MB	(2) 1M x 36	(2) 4M x 36	None
40MB	None	(2) 4M x 36	(1) 1M x 64
48MB	(2) 2M x 36	(2) 4M x 36	None
48MB	None	(2) 4M x 36	(1) 2M x 64
64MB	None	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36	None
64MB	None	None	(1) 8M x 64
64MB	None	(2) 4M x 36	(1) 4M x 64
72MB	(2) 1M x 36	(2) 8M x 36	None
72MB	None	(2) 8M x 36	(1) 1M x 64
80MB	(2) 2M x 36	(2) 8M x 36	None

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GENOA SYSTEMS CORPORATION

TURBOEXPRESS 586-FX

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DIMM/DRAM CONFIGURATION (CON'T)			
Size	Bank 0	Bank 1	Bank 2
80MB	None	(2) 8M x 36	(1) 2M x 64
96MB	(2) 4M x 36	(2) 8M x 36	None
96MB	None	(2) 8M x 36	(1) 4M x 64
128MB	(2) 8M x 36	(2) 8M x 36	None
128MB	None	(2) 8M x 36	(1) 8M x 64

Note: Board accepts EDO memory. Board also accepts X32 SIMMs.

CACHE CONFIGURATION			
Size	Bank 0	SL1	TAG
256KB (A)	(2) 32K x 32	Not installed	(1) 16K/32K x 8
512KB (A)	(2) 32K x 32	256KB module installed	(1) 16K/32K x 8
512KB (B)	(2) 64K x 32	Not installed	(1) 16K/32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP7	JP8	JP15	JP16
256KB (A)	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
512KB (A)	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
512KB (B)	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed

CPU SPEED SELECTION (CYRIX)				
CPU speed	Clock speed	Multiplier	JP9	JP17
120MHz	50MHz	2x	1 & 2, 3 & 4	2 & 3
133MHz	55MHz	2x	Open	2 & 3
150MHz	66MHz	2x	3 & 4	2 & 3
166MHz	60MHz	2x	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AMD)				
CPU speed	Clock speed	Multiplier	JP9	JP17
75MHz	50MHz	1.5x	1 & 2, 3 & 4	1 & 2
90MHz	60MHz	1.5x	3 & 4	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2
120MHz	60MHz	2x	3 & 4	2 & 3
133MHz	66MHz	2x	1 & 2	2 & 3
150MHz	60MHz	2.5x	3 & 4	2 & 3, 4 & 5
166MHz	66MHz	2.5x	1 & 2	2 & 3, 4 & 5

Note: Pins designated should be in the closed position.

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GENOA SYSTEMS CORPORATION

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CPU SPEED SELECTION (INTEL)				
CPU speed	Clock speed	Multiplier	JP9	JP17
75MHz	50MHz	1.5x	1 & 2, 3 & 4	1 & 2
90MHz	60MHz	1.5x	3 & 4	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2
120MHz	60MHz	2x	3 & 4	2 & 3
133MHz	66MHz	2x	1 & 2	2 & 3
150MHz	60MHz	2.5x	3 & 4	2 & 3, 4 & 5
166MHz	66MHz	2.5x	1 & 2	2 & 3, 4 & 5
200MHz	66MHz	3x	1 & 2	4 & 5

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION (SINGLE)				
Voltage	JP11	JP12	JP13	JP14
3.2v	Open	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
3.3v	Open	Open	Pins 1 & 2 closed	Pins 1 & 2 closed
3.5v	Open	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU VOLTAGE SELECTION (DUAL)					
Voltage	V core	JP11	JP12	JP13	JP14
3.3v	2.5v	1 & 2	Open	2 & 3	2 & 3
3.3v	2.7v	2 & 3	1 & 2	2 & 3	2 & 3
3.3v	2.8v	2 & 3	Open	2 & 3	2 & 3
3.3v	2.9v	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.