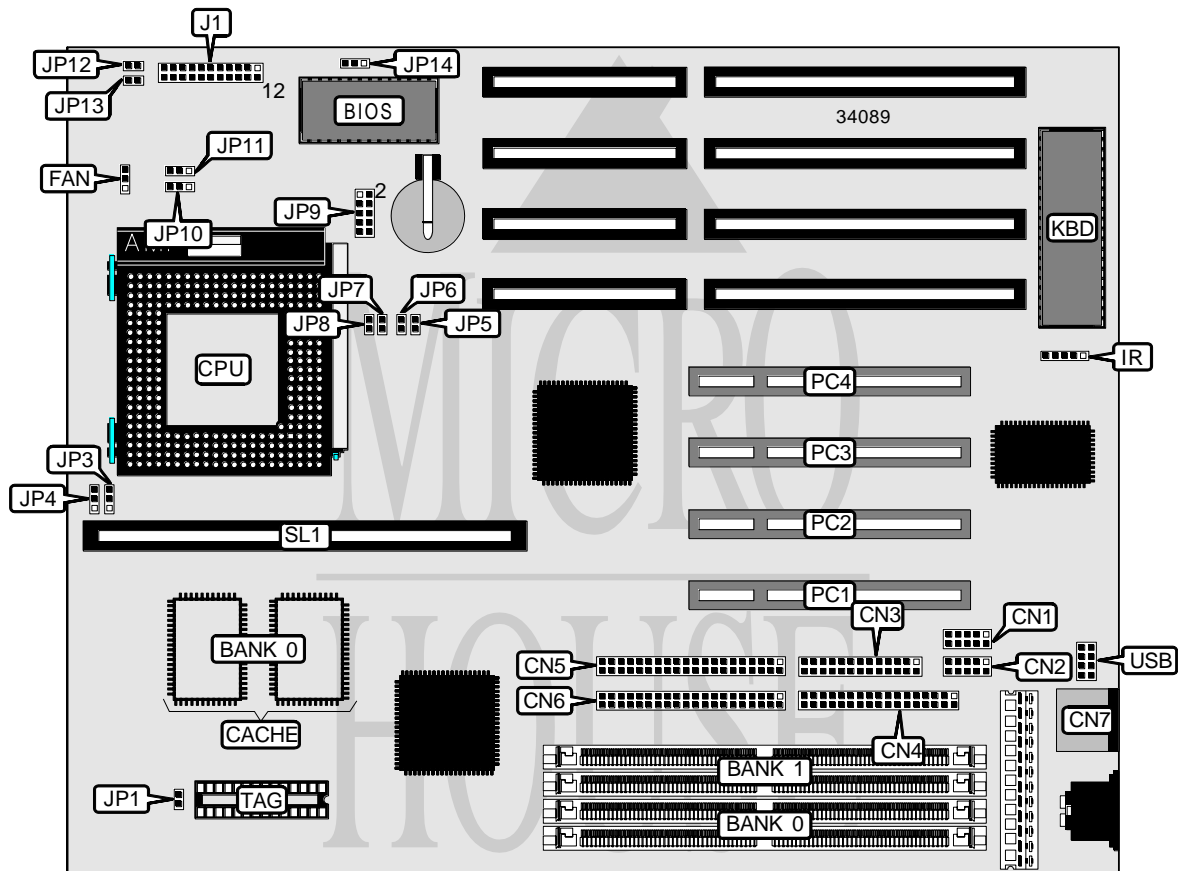


A-TREND TECHNOLOGY CORPORATION

ATC-2000

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	256MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot, IR connector, USB connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Reset switch	J1 pins 1 & 2
Serial port 1	CN2	Green PC connector	J1 pins 4 & 5
Parallel port	CN3	Turbo LED	J1 pins 7 & 8
Floppy drive interface	CN4	IDE interface LED	J1 pins 10 & 11
IDE interface 1	CN5	Speaker	J1 pins 12 - 15
IDE interface 2	CN6	Power LED & keylock	J1 pins 17 - 21
PS/2 mouse port	CN7	32-bit PCI slots	PC1 – PC4
Chassis fan power	FAN	Cache slot	SL1
IR connector	IR	USB connector	USB

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP5	Open
CMOS memory clear	JP5	Closed
Flash BIOS voltage select 12v	JP14	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP14	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(2) 512K x 36	None
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory. Banks are interchangeable.		

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	None	256KB module installed	(1) 16K/32K x 8
256KB	(2) 32K x 32	Not installed	(1) 16K/32K x 8
512KB	(2) 64K x 32	Not installed	(1) 16K/32K x 8

CACHE TAG CONFIGURATION	
Setting	JP1
Enabled	Closed
Disabled	Open

CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JP3	JP4	JP6	JP7	JP8
120MHz	50MHz	2x	2 & 3	1 & 2	Open	Closed	Closed
133MHz	55MHz	2x	2 & 3	1 & 2	N/A	Open	Open
150MHz	60MHz	2x	2 & 3	1 & 2	Closed	Open	Closed
166MHz	66MHz	2x	2 & 3	1 & 2	Closed	Closed	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP3	JP4	JP6	JP7	JP8
75MHz	50MHz	1.5x	1 & 2	1 & 2	Open	Closed	Closed
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Closed
100MHz	66MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
120MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Closed
133MHz	66MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
150MHz	60MHz	2x	2 & 3	1 & 2	Closed	Open	Closed
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP3	JP4	JP6	JP7	JP8
75MHz	50MHz	1.5x	1 & 2	1 & 2	Open	Closed	Closed
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Closed
100MHz	66MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
120MHz	60MHz	2x	2 & 3	1 & 2	Closed	Open	Closed
133MHz	66MHz	2x	2 & 3	1 & 2	Closed	Closed	Open
150MHz	60MHz	2.5x	2 & 3	2 & 3	Closed	Open	Closed
166MHz	66MHz	2.5x	2 & 3	2 & 3	Closed	Closed	Open
180MHz	60MHz	3x	1 & 2	2 & 3	Closed	Open	Closed
200MHz	66MHz	3x	1 & 2	2 & 3	Closed	Closed	Open
Note: Pins designated should be in the closed position.							

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CPU TYPE SELECTION			
Type	JP9	JP10	JP11
CX6X86	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
AM K5	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
P54C STD	Pins 3 & 4 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
P54C VRE	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU VOLTAGE SELECTION (P55C ONLY)						
Voltage	V core	JP9	JP10	JP11	JP12	JP13
3.3v	2.7v	9 & 10	2 & 3	2 & 3	Closed	Open
3.3v	2.8v	7 & 8	2 & 3	2 & 3	Closed	Open
3.3v	2.9v	5 & 6	2 & 3	2 & 3	Closed	Open
3.3v	3.3v	3 & 4	2 & 3	2 & 3	Closed	Open
3.3v	3.52v	1 & 2	2 & 3	2 & 3	Closed	Open
Note: Pins designated should be in the closed position.						