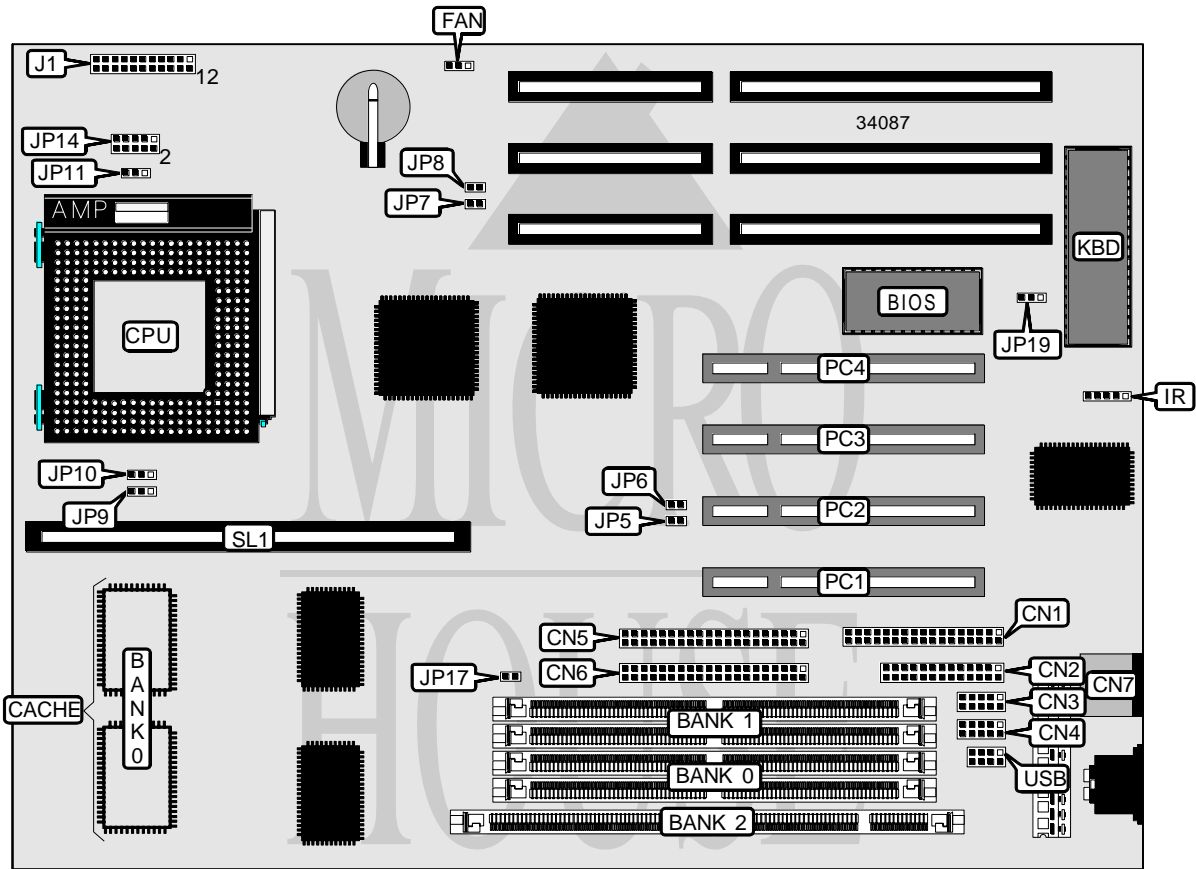


A-TREND TECHNOLOGY CORPORATION

ATC-1020

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot, IR connector, USB connector
NPU Options	None



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A-TREND TECHNOLOGY CORPORATION

ATC-1020

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CONNECTIONS			
Purpose	Location	Purpose	Location
Floppy drive interface	CN1	Reset switch	J1 pins 1 & 2
Parallel port	CN2	Green PC connector	J1 pins 4 & 5
Serial port 1	CN3	Turbo LED	J1 pins 7 & 8
Serial port 2	CN4	IDE interface LED	J1 pins 10 & 11
IDE interface 1	CN5	Speaker	J1 pins 12 - 15
IDE interface 2	CN6	Power LED & keylock	J1 pins 17 - 21
PS/2 mouse port	CN7	32-bit PCI slots	PC1 - PC4
Chassis fan power	FAN	Cache slot	SL1
IR connector	IR	USB connector	USB

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP8	Open
CMOS memory clear	JP8	Closed
í Flash BIOS write protect enabled	JP19	Pins 2 & 3 closed
Flash BIOS write protect disabled	JP19	Pins 1 & 2 closed

DIMM CONFIGURATION	
Size	Bank 2
8MB	(1) 1M x 64
16MB	(1) 2M x 64
32MB	(1) 4M x 64
64MB	(1) 8M x 64

Note: Do not install DIMM and SIMM at the same time.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36

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A-TREND TECHNOLOGY CORPORATION

ATC1020

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None

Note: Board accepts EDO memory. Banks are interchangeable.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	None	256KB module installed	(1) 16K/32K x 8
256KB	(2) 32K x 32	Not installed	(1) 16K/32K x 8
512KB	(2) 64K x 32	Not installed	(1) 16K/32K x 8

Note: The location of the TAG is unidentified.

CACHE TAG CONFIGURATION	
Setting	JP17
Enabled	Closed
Disabled	Open

CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JP5	JP6	JP7	JP9	JP10
120MHz	50MHz	2x	Open	Open	Open	1 & 2	2 & 3
133MHz	55MHz	50MHz	Closed	Closed	N/A	1 & 2	2 & 3
150MHz	60MHz	2x	Open	Closed	Closed	1 & 2	2 & 3
166MHz	66MHz	2x	Closed	Open	Closed	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP5	JP6	JP7	JP9	JP10
75MHz	50MHz	1.5x	Open	Open	Open	1 & 2	1 & 2
90MHz	60MHz	1.5x	Open	Closed	Closed	1 & 2	1 & 2
100MHz	66MHz	1.5x	Closed	Open	Closed	1 & 2	1 & 2
120MHz	60MHz	1.5x	Open	Closed	Closed	1 & 2	1 & 2
133MHz	66MHz	1.5x	Closed	Open	Closed	1 & 2	1 & 2
166MHz	66MHz	2x	Closed	Open	Closed	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

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A-TREND TECHNOLOGY CORPORATION

ATC-1020

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CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP5	JP6	JP7	JP9	JP10
75MHz	50MHz	1.5x	Open	Open	Open	1 & 2	1 & 2
90MHz	60MHz	1.5x	Open	Closed	Closed	1 & 2	1 & 2
100MHz	66MHz	1.5x	Closed	Open	Closed	1 & 2	1 & 2
120MHz	60MHz	2x	Open	Closed	Closed	1 & 2	2 & 3
133MHz	66MHz	2x	Closed	Open	Closed	1 & 2	2 & 3
150MHz	60MHz	2.5x	Open	Closed	Closed	2 & 3	2 & 3
166MHz	66MHz	2.5x	Closed	Open	Closed	2 & 3	2 & 3
180MHz	60MHz	3x	Open	Closed	Closed	2 & 3	1 & 2
200MHz	66MHz	3x	Closed	Open	Closed	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION		
Type	JP11	JP14
CX6X86	Pins 1 & 2 closed	Pins 1 & 2 closed
CX6X86L	Pins 2 & 3 closed	Pins 7 & 8 closed
AM K5	Pins 1 & 2 closed	Pins 1 & 2 closed
P54C STD	Pins 1 & 2 closed	Pins 3 & 4 closed
P54C VRE	Pins 1 & 2 closed	Pins 1 & 2 closed
P55C	Pins 2 & 3 closed	Pins 1 & 2 closed

CPU VOLTAGE SELECTION (P55C ONLY)	
Voltage	JP14
2.7v	Pins 9 & 10 closed
2.8v	Pins 7 & 8 closed
2.9v	Pins 5 & 6 closed
3.3v	Pins 3 & 4 closed
3.5v	Pins 1 & 2 closed