J-MARK COMPUTER CORPORATION

J-656C

Processor	CX M1/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	255mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot
NPU Options	None
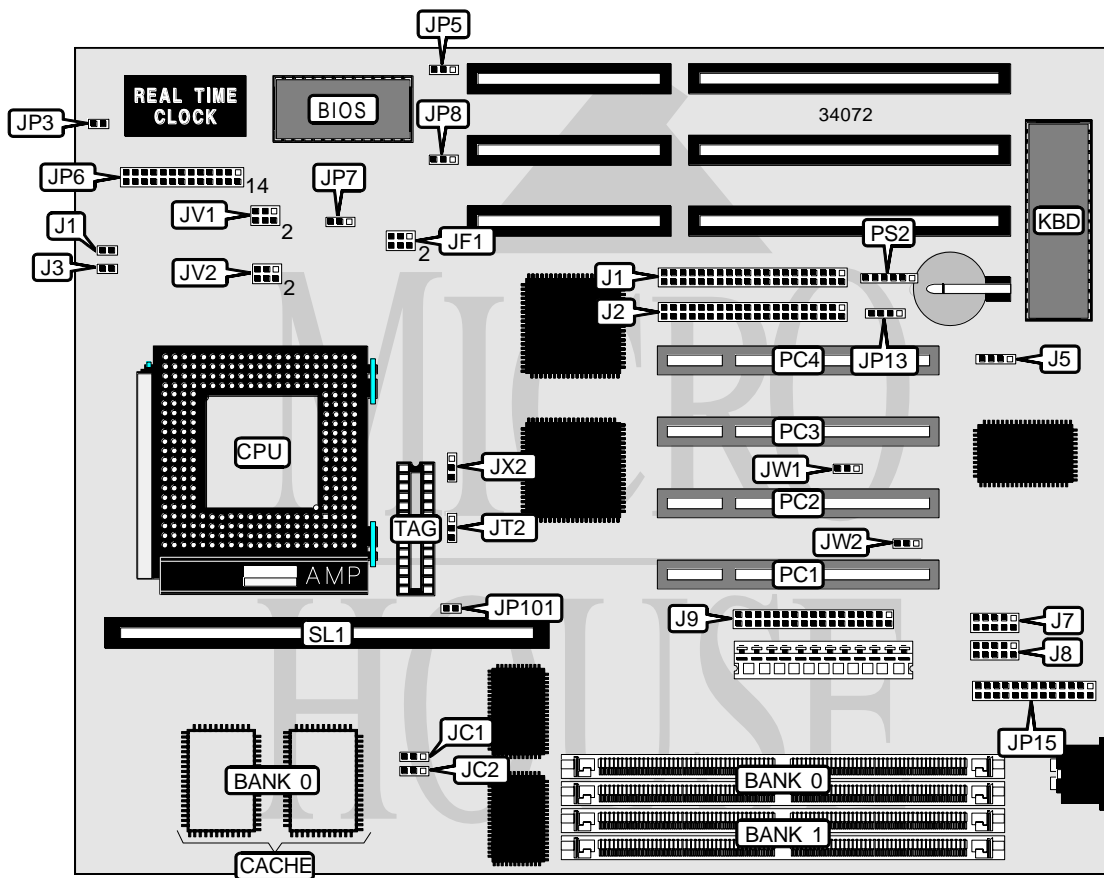
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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
IDE interface 2	J1	IDE interface LED	JP6 pins 14 & 15
IDE interface 1	J2	Turbo switch	JP6 pins 18 & 19
Serial port 2	J7	Reset switch	JP6 pins 22 & 23
Serial port 1	J8	Turbo LED	JP6 pins 25 & 26
Floppy drive interface	J9	Parallel port	JP15
Power LED & keylock	JP6 pins 1 - 5	32-bit PCI slots	PC1 – PC4
Green PC connector	JP6 pins 7 & 8	PS/2 mouse interface	PS2
Speaker	JP6 pins 10 - 13	Cache slot	SL1

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	J5	Unidentified
í CMOS memory normal operation	JP3	Open
CMOS memory clear	JP3	Closed
Flash BIOS voltage select 12v	JP5	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP5	Pins 2 & 3 closed
í ISA clock select PCI CLK/4	JP8	Pins 2 & 3 closed
ISA clock select PCI CLK/3	JP8	Pins 1 & 2 closed
í Factory configured - do not alter	JP13	Unidentified
í Factory configured - do not alter	JV1	Unidentified
í Factory configured - do not alter	JW1	Unidentified
í Factory configured - do not alter	JW2	Unidentified

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Banks are interchangeable at 64MB and above.		

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CACHE CONFIGURATION			
Size	**Bank 0**	**SL1**	**TAG**
256KB (A)	(2) 32K x 32	Not installed	(1) 32K x 8
256KB (B)	None	256KB module installed	(1) 32K x 8
512KB (A)	(2) 32K x 32	256KB module installed	(1) 32K x 8
512KB (B)	None	512KB module installed	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	**JC1**	**JC2**	**JT2**	**JX2**
256KB (A)	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB (B)	N/A	N/A	N/A	N/A
512KB (A)	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
512KB (B)	N/A	N/A	N/A	N/A

CACHE TYPE CONFIGURATION	
Type	**JP101**
Asynchronous	Closed
Synchronous	Open

CPU SPEED SELECTION (CYRIX)					
CPU speed	**Clock speed**	**Multiplier**	**JF1**	**J1**	**J3**
120MHz	50MHz	2x	3 & 4	Closed	Open
150MHz	60MHz	2x	1 & 2	Closed	Open
166MHz	66MHz	2x	Open	Closed	Open
Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (INTEL)					
CPU speed	**Clock speed**	**Multiplier**	**JF1**	**J1**	**J3**
75MHz	50MHz	1.5x	3 & 4	Open	Open
90MHz	60MHz	1.5x	1 & 2	Open	Open
100MHz	66MHz	1.5x	Open	Open	Open
120MHz	60MHz	2x	1 & 2	Closed	Open
133MHz	66MHz	2x	Open	Closed	Open
150MHz	60MHz	2.5x	1 & 2	Closed	Closed
166MHz	66MHz	2.5x	Open	Closed	Closed
180MHz	60MHz	3x	1 & 2	Open	Closed
200MHz	66MHz	3x	Open	Open	Closed
Note: Pins designated should be in the closed position.					

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CPU VOLTAGE SELECTION	
Voltage	**JV2**
3.3v	Pins 1 & 2 closed
í 3.45v	Pins 3 & 4 closed
3.6v	Pins 5 & 6 closed

CPU CLOCK TOLERANCE SELECTION	
Tolerance	**JP7**
3.3v	Pins 2 & 3 closed
í 5v	Pins 1 & 2 closed