ASPEN SYSTEMS, INC. DURANGO

Processor Alpha

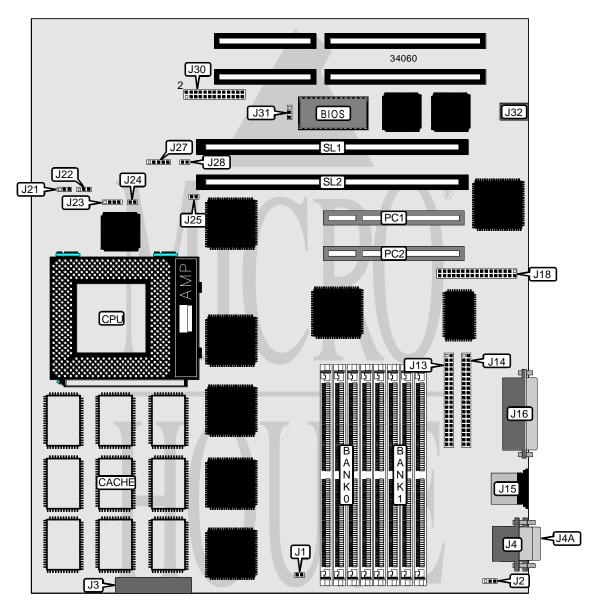
300/333/366/400/433MHz **Processor Speed**

Chip Set Digital **Video Chip Set** None **Maximum Onboard Memory** 512MB **Maximum Video Memory** None Cache 1024KB **BIOS** Unidentified **Dimensions** 305mm x 244mm

I/O Options 32-bit PCI slots (2), 64-bit PCI slots (2), floppy drive interface, IDE interfaces (2),

parallel port, PS/2 mouse port, serial ports (2)

NPU Options None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Chassis fan power	J2	Chassis fan power	J22
Power connector	J3	Speaker	J23
Serial port 1	J4	Reset switch	J24
Serial port 2	J4A	Halt connector	J25
IDE interface 2	J13	Power LED & keylock	J27
IDE interface 1	J14	IDE interface LED	J28
PS/2 mouse port	J15	SROM test port	J32
Parallel port	J16	32-bit PCI slots	PC1 & PC2
Floppy drive interface	J18	64-bit PCI slots	SL1 & SL2
Chassis fan power	J21		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	J30 pins 9 & 10	Open
í Factory configured - do not alter	J30 pins 15 & 16	Open
í Mini debugger disabled	J30 pins 21 & 22	Open
Mini debugger enabled	J30 pins 21 & 22	Closed
í Boot option select Windows NT ARC firmware loaded	J30 pins 23 & 24	Open
Boot option select fail safe boot loaded	J30 pins 23 & 24	Closed
í Jumper not used	J30 pins 25 & 26	N/A
í Flash BIOS update enabled	J31	Pins 2 & 3 closed
Flash BIOS update disabled	J31	Pins 1 & 2 closed

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
16MB	(4) 1M x 36	None
32MB (A)	(4) 1M x 36	(4) 1M x 36
32MB (B)	(4) 2M x 36	None
64MB (A)	(4) 2M x 36	(4) 2M x 36
64MB (B)	(4) 4M x 36	None
128MB (A)	(4) 4M x 36	(4) 4M x 36
128MB (B)	(4) 8M x 36	None
256MB (A)	(4) 8M x 36	(4) 8M x 36
256MB (B)	(4) 16M x 36	None
512MB (B)	(4) 16M x 36	(4) 16M x 36

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DRAM JUMPER CONFIGURATION	
Size	J1
16MB	Closed
32MB (A)	Open
32MB (B)	Closed
64MB (A)	Open
64MB (B)	Closed
128MB (A)	Open
128MB (B)	Closed
256MB (A)	Open
256MB (B)	Closed
512MB (B)	Open

CACHE CONFIGURATION

Note: Board can be installed with either 1MB or 2MB of cache. The size of the chips is unidentified.

CACHE JUMPER CONFIGURATION		
Size	J30	
1MB	Pins 11 & 12, 13 & 14 open	
2MB	Pins 11 & 12 open, 13 & 14 closed	

CACHE SPEED CONFIGURATION		
Speed J30		
9ns	Pins 17 & 18, 19 & 20 open	

CPU SPEED SELECTION		
Speed	J30	
300MHz	Pins 3 & 4, 5 & 6 closed	
333MHz	Pins 3 & 4, 7 & 8 closed	
366MHz	Pins 3 & 4 closed	
400MHz	Pins 5 & 6, 7 & 8 closed	
433MHz	Pins 5 & 6 closed	