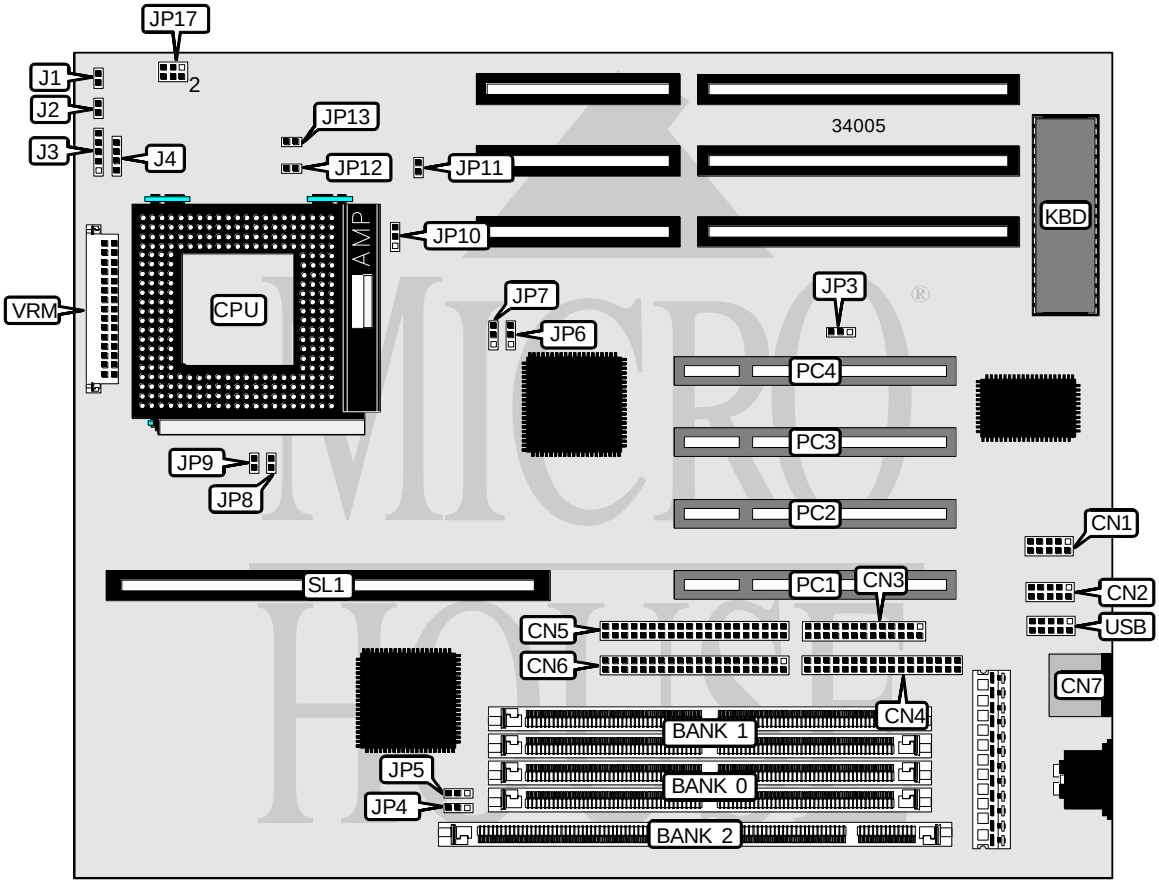
DAYTON MICRO
P5510VX

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot, IR connector, VRM connector, USB connector
NPU Options	None



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Serial port 2	CN1	IDE interface LED	J2
Serial port 1	CN2	Power LED & keylock	J3
Parallel port	CN3	Speaker	J4
Floppy drive interface	CN4	Green PC connector	JP6
IDE interface 2	CN5	32-bit PCI slots	PC1 – PC4
IDE interface 1	CN6	Cache slot	SL1
PS/2 mouse port	CN7	USB connector	USB
Reset switch	J1	VRM connector	VRM

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Flash BIOS voltage select 5v	JP3	Pins 1 & 2 closed
Flash BIOS voltage select 12v	JP3	Pins 2 & 3 closed
í CMOS memory normal operation	JP11	Open
CMOS memory clear	JP11	Closed

DIMM/DRAM CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**Bank 2**
8MB	(2) 1M x 36	None	None
8MB	None	None	(1) 1M x 64
16MB	(2) 2M x 36	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None
16MB	None	None	(1) 2M x 64
16MB	(2) 1M x 36	None	(1) 1M x 64
24MB	(2) 2M x 36	(2) 1M x 36	None
24MB	(2) 1M x 36	None	(1) 2M x 64
24MB	(2) 2M x 36	None	(1) 1M x 64
32MB	(2) 4M x 36	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None
32MB	None	None	(1) 4M x 64
32MB	(2) 2M x 36	None	(1) 2M x 64
40MB	(2) 4M x 36	(2) 1M x 36	None
40MB	(2) 1M x 36	None	(1) 4M x 64
40MB	(2) 4M x 36	None	(1) 1M x 64
48MB	(2) 4M x 36	(2) 2M x 36	None
48MB	(2) 2M x 36	None	(1) 4M x 64
48MB	(2) 4M x 36	None	(1) 2M x 64
64MB	(2) 8M x 36	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None
64MB	None	None	(1) 8M x 64
64MB	(2) 4M x 36	None	(1) 4M x 64

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DIMM/DRAM CONFIGURATION (CON'T)			
Size	**Bank 0**	**Bank 1**	**Bank 2**
72MB	(2) 8M x 36	(2) 1M x 36	None
72MB	(2) 1M x 36	None	(1) 8M x 64
72MB	(2) 8M x 36	None	(1) 1M x 64
80MB	(2) 8M x 36	(2) 2M x 36	None
80MB	(2) 2M x 36	None	(1) 8M x 64
80MB	(2) 8M x 36	None	(1) 2M x 64
96MB	(2) 8M x 36	(2) 4M x 36	None
96MB	(2) 4M x 36	None	(1) 8M x 64
96MB	(2) 8M x 36	None	(1) 4M x 64
128MB	(2) 8M x 36	(2) 8M x 36	None
128MB	(2) 8M x 36	None	(1) 8M x 64
Note: Board accepts EDO memory.			

DIMM VOLTAGE CONFIGURATION		
Voltage	**JP4**	**JP5**
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed
5v	Pins 1 & 2 closed	Pins 1 & 2 closed

CACHE CONFIGURATION			
Size	**Bank 0**	**SL1**	**TAG**
256KB	(2) 32K x 32	Not installed	(1) 32K x 8
512KB (A)	(2) 32K x 32	256KB module installed	(1) 32K x 8
512KB (B)	(2) 64K x 64	Not installed	(1) 32K x 8
Note: The location of bank 0 & the TAG are unidentified.			

CACHE JUMPER CONFIGURATION	
Size	**JP10**
256KB	Pins 2 & 3 closed
512KB (A)	Pins 1 & 2 closed
512KB (B)	Pins 1 & 2 closed

CPU SPEED SELECTION (CYRIX)							
CPU speed	**Clock speed**	**Multiplier**	**JP7**	**JP8**	**JP9**	**JP12**	**JP13**
120MHz	50MHz	2x	2 & 3	Closed	Closed	Closed	Open
150MHz	60MHz	2x	1 & 2	Open	Closed	Closed	Open
166MHz	66MHz	2x	1 & 2	Closed	Open	Closed	Open
Note: Pins designated should be in the closed position.							

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CPU SPEED SELECTION (AMD)							
CPU speed	**Clock speed**	**Multiplier**	**JP7**	**JP8**	**JP9**	**JP12**	**JP13**
75MHz	50MHz	1.5x	2 & 3	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	1 & 2	Open	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	Closed	Open	Open	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	**Clock speed**	**Multiplier**	**JP7**	**JP8**	**JP9**	**JP12**	**JP13**
75MHz	50MHz	1.5x	2 & 3	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	1 & 2	Open	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	Closed	Open	Open	Open
120MHz	60MHz	2x	1 & 2	Open	Closed	Closed	Open
133MHz	66MHz	2x	1 & 2	Closed	Open	Closed	Open
150MHz	60MHz	2.5x	1 & 2	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	1 & 2	Closed	Open	Closed	Closed
180MHz	60MHz	3x	1 & 2	Open	Closed	Open	Closed
200MHz	66MHz	3x	1 & 2	Closed	Open	Open	Closed
Note: Pins designated should be in the closed position.							

CPU VOLTAGE SELECTION	
Voltage	**JP17**
3.3v (STD/VR)	Pins 1 & 2 closed
3.4v	Pins 3 & 4 closed
í 3.5v (VRE)	Pins 5 & 6 closed