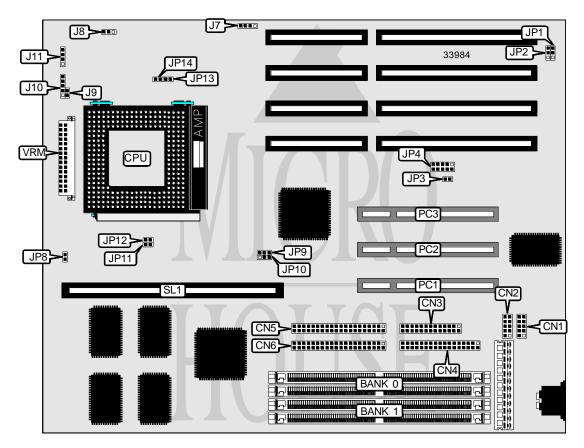
PC WARE INTERNATIONAL, INC. MB-TRITON W

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512B
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces
	(2), parallel port, serial ports (2), VRM connector, cache slot
NPU Options	None



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CONNECTIONS				
Purpose	Location	Purpose	Location	
Serial port 1	CN1	Power LED & keylock	J10	
Serial port 2	CN2	Speaker	J11	
Parallel port	CN3	IDE interface LED	JP8	
Floppy drive interface	CN4	Green PC connector	JP9	
IDE interface 2	CN5	32-bit PCI slots	PC1 - PC3	
IDE interface 1	CN6	Cache slot	SL1	
External battery	J7	VRM connector	VRM	
Reset switch	19			

USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
Battery type select internal	J7	Closed		
Battery type select external	J7	Pins 2 & 3 closed		
CMOS memory clear	J7	Pins 3 & 4 closed		
í Flash BIOS voltage select 5v	JP3	Open		
Flash BIOS voltage select 12v	JP3	Closed		
í Factory configured – do not alter	JP4	Unidentified		

DRAM CONFIGURATION						
Size	Bank 0	Bank 1				
8MB	(2) 1M x 36	None				
16MB	(2) 2M x 36	None				
16MB	(2) 1M x 36	(2) 1M x 36				
24MB	(2) 2M x 36	(2) 1M x 36				
32MB	(2) 4M x 36	None				
32MB	(2) 2M x 36	(2) 2M x 36				
40MB	(2) 4M x 36	(2) 1M x 36				
48MB	(2) 4M x 36	(2) 2M x 36				
64MB	(2) 8M x 36	None				
64MB	(2) 4M x 36	(2) 4M x 36				
72MB	(2) 8M x 36	(2) 1M x 36				
80MB	(2) 8M x 36	(2) 2M x 36				
96MB	(2) 8M x 36	(2) 4M x 36				
128MB	(2) 8M x 36	(2) 8M x 36				

	CACHE CONFIGURATION	
Size	SL1	TAG
256KB	256KB module installed	(1) 8K/16K/32K x 8
512KB	512KB module installed	(1) 16K/32K x 8
Note: The location of the TAG is unide	ntified.	

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CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JP10	JP11	JP12	JP13	JP14
120MHz	60MHz	2x	1&2	Open	Closed	Closed	Open
133MHz	66MHz	2x	1&2	Closed	Open	Closed	Open
150MHz	60MHz	2x	1&2	Open	Closed	Closed	Open
166MHz	66MHz	2x	1&2	Closed	Open	Closed	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP10	JP11	JP12	JP13	JP14
75MHz	50MHz	1.5x	2&3	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	1&2	Open	Closed	Open	Open
100MHz	66MHz	1.5x	1&2	Closed	Open	Open	Open
Note: Pins desi	Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP10	JP11	JP12	JP13	JP14
75MHz	50MHz	1.5x	2&3	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	1 & 2	Open	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	Closed	Open	Open	Open
120MHz	60MHz	2x	1 & 2	Open	Closed	Closed	Open
133MHz	66MHz	2x	1&2	Closed	Open	Closed	Open
150MHz	60MHz	2.5x	1 & 2	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	1 & 2	Closed	Open	Closed	Closed
180MHz	60MHz	3x	1 & 2	Open	Closed	Open	Closed
200MHz	66MHz	Зx	1&2	Closed	Open	Open	Closed
Note: Pins desi	Note: Pins designated should be in the closed position.						

CPU VOLTAGE SELECTION			
Voltage J8			
í 3.3v (STD/VR)	Pins 2 & 3 closed		
3.5v (VRE)	Pins 1 & 2 closed		

DMA CHANNEL SELECTION					
Channel	JP1	JP2			
í 1	Pins 1 & 2 closed	Pins 1 & 2 closed			
3	Pins 2 & 3 closed	Pins 2 & 3 closed			