

ELITEGROUP COMPUTER SYSTEMS, INC.

SC58P VIO/S

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	J2	Turbo LED	J14
Floppy drive interface	J5	Reset switch	J15
VGA feature connector	J6	IR connector	JP1
IDE interface 2	J7	Parallel port	P1
IDE interface 1	J8	Serial port 1	P2
IDE interface LED	J12 pins 1 & 2	Serial port 2	P3
Power LED	J12 pins 3 - 5	VGA port	P4
Chassis fan power	J13	Riser slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	J9	Pins 1 & 2 closed
CMOS memory clear	J9	Pins 2 & 3 closed
í On board video enabled	JP3	Closed
On board video disabled	JP3	Open
í Flash BIOS voltage select 12v	JP11	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP11	Pins 2 & 3 closed
í Floppy drive write protect disabled	JP21	Pins 1 & 2 closed
Floppy drive write protect enabled	JP21	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory.

Continued on next page. . .

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... continued from previous page

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
None	None	None	(1) 32K x 8
256KB (A)	None	(8) 32K x 8	(1) 32K x 8
256KB (B)	(2) 32K x 32	None	(1) 32K x 8
512KB	None	(8) 64K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION	
Size	JP13
None	Pins 1 & 2 closed
256KB (A)	Pins 1 & 2 closed
256KB (B)	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed

VIDEO MEMORY CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(2) 256K x 16	None
2MB	(2) 256K x 16	(2) 256K x 16

Note: Bank 0 is factory installed and is not configurable.

CPU SPEED SELECTION (CYRIX)				
CPU speed	Clock speed	Multiplier	JP14	JP17
120MHz	50MHz	2x	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)				
CPU speed	Clock speed	Multiplier	JP14	JP17
75MHz	50MHz	1.5x	1 & 2	Open
90MHz	60MHz	1.5x	3 & 4	Open
100MHz	66MHz	1.5x	1 & 2, 3 & 4	Open
120MHz	60MHz	2x	3 & 4	1 & 2
133MHz	66MHz	2x	1 & 2, 3 & 4	1 & 2
150MHz	60MHz	2.5x	3 & 4	1 & 2, 3 & 4
166MHz	66MHz	2.5x	1 & 2, 3 & 4	1 & 2, 3 & 4

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION	
Type	JP22
Cyrix	Pins 2 & 3 closed
í Intel	Pins 1 & 2 closed

CPU VOLTAGE SELECTION	
Voltage	JP19
3.3v	Pins 1 & 2 closed
í 3.525v (VRE)	Pins 3 & 4 closed