

ELITEGROUP COMPUTER SYSTEMS, INC.

SI56P AVIO

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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse interface	J1	External battery	J15
VGA feature connector	J5	Turbo LED	J16 pins 2 & 3
VGA interface	J6	Green PC connector	J16 pins 4 & 5
Floppy drive interface	J8	Turbo switch	J16 pins 6 & 7
IDE interface LED	J9	Reset switch	J16 pins 9 & 10
Serial port 2	J10	Power LED & keylock	J16 pins 11 - 15
Serial port 1	J11	Speaker	J16 pins 17 - 20
IDE interface 1	J12	Chassis fan power	J26
Parallel port	J13	IR connector (optional)	JP8
IDE interface 2	J14	32-bit PCI slots	PC1 - PC3

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Super I/O enabled	JP11	Open
Super I/O disabled	JP11	Closed
í Flash BIOS voltage select 5v	JP25	Pins 1 & 2 closed
Flash BIOS voltage select 12v	JP25	Pins 2 & 3 closed
í CMOS memory normal operation	JP26	Open
CMOS memory clear	JP26	Closed
í On board video enabled	JP27	Closed
On board video disabled	JP27	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory.

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB (A)	(2) 32K x 32	None	(1) 32K x 8
256KB (B)	None	(8) 32K x 8	(1) 32K x 8
512KB	None	(8) 64K x 8	(1) 32K x 8
1MB	None	(8) 128K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP6	JP7
256KB (A)	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB (B)	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed	Pins 1 & 2 closed
1MB	Pins 2 & 3 closed	Pins 2 & 3 closed

CACHE TYPE CONFIGURATION		
Size	JP1	JP2
í Mixed	Pins 2 & 3 closed	Pins 2 & 3 closed
Pure	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU SPEED SELECTION (CYRIX)					
CPU speed	Clock speed	Multiplier	JP15	JP22	JP24
120MHz	50MHz	2x	1 & 2	Open	Closed
150MHz	60MHz	2x	3 & 4	Open	Closed
166MHz	66MHz	2x	1 & 2, 3 & 4	Open	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)					
CPU speed	Clock speed	Multiplier	JP15	JP22	JP24
75MHz	50MHz	1.5x	1 & 2	Open	Open
90MHz	60MHz	1.5x	3 & 4	Open	Open
100MHz	66MHz	1.5x	1 & 2, 3 & 4	Open	Open
120MHz	60MHz	2x	3 & 4	Open	Closed
133MHz	66MHz	2x	1 & 2, 3 & 4	Open	Closed
150MHz	60MHz	2.5x	3 & 4	Closed	Closed
166MHz	66MHz	2.5x	1 & 2, 3 & 4	Closed	Closed

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION	
Voltage	JP16
3.3v (STD)	Pins 1 & 2 closed
3.385v (VR)	Pins 3 & 4 closed
3.525v (VRE)	Pins 5 & 6 closed

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DMA CHANNEL SELECTION		
Channel	JP12	JP13
1	Pins 1 & 2 closed	Pins 1 & 2 closed
3	Pins 2 & 3 closed	Pins 2 & 3 closed