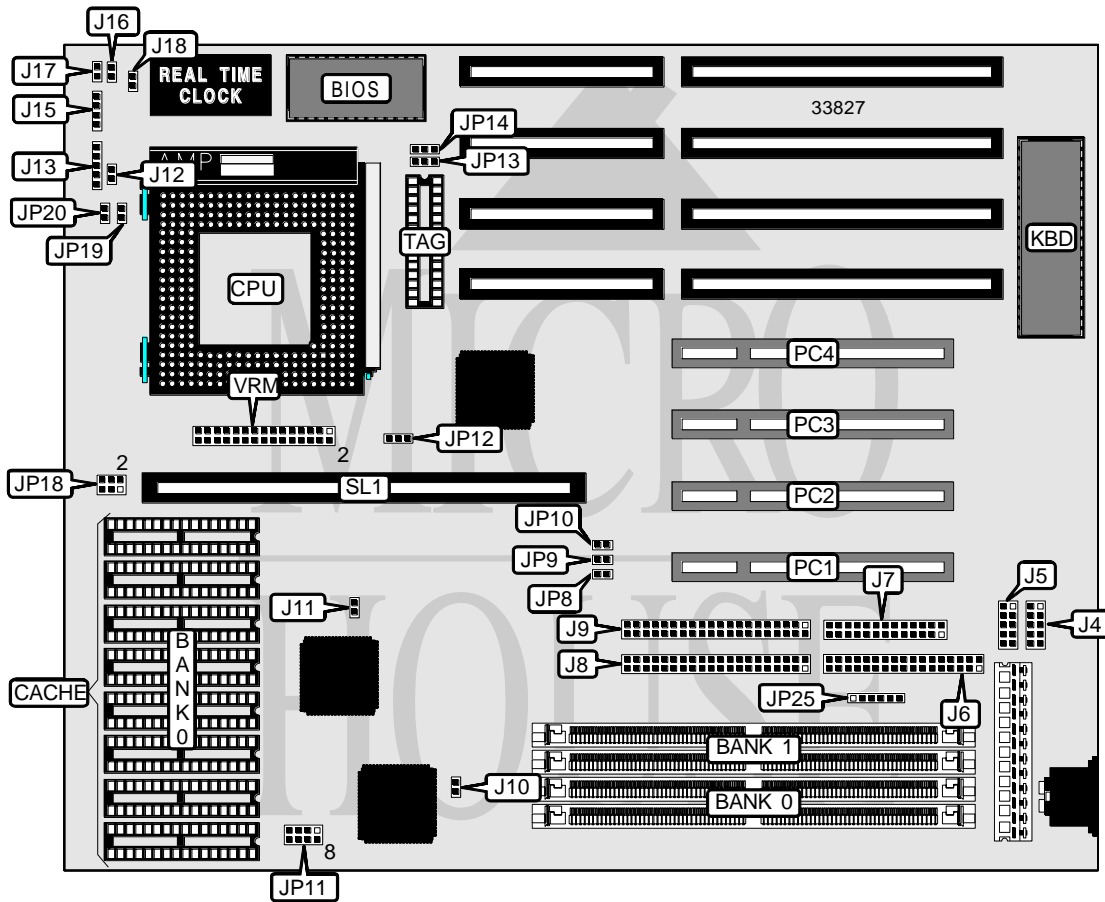


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5 S E M 4 . 1

Processor	Pentium
Processor Speed	75/90/100/120/133/150MHz
Chip Set	SIS
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512/1024KB
BIOS	Award
Dimensions	275mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), cache slot, IR connector, VRM connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	J4	Power LED & keylock	J13
Serial port 2	J5	Speaker	J15
Floppy drive interface	J6	Reset switch	J16
Parallel port	J7	Turbo LED	J17
IDE interface 1	J8	IDE interface LED	J18
IDE interface 2	J9	IR connector	JP25
Green PC LED	J10	32-bit PCI slots	PC1 - PC4
Green PC connector	J11	Cache slot	SL1
Turbo switch	J12	VRM connector	VRM

USER CONFIGURABLE SETTINGS		
Function	Label	Position
CPU clock select clock/2 (60/66MHz)	JP8	Open
CPU clock select clock = clock (50MHz)	JP8	Closed
IR connector type select Intel	JP25	Pins 1 - 4 closed
IR connector type select Hewlett-Packard	JP25	Pins 2 - 6 closed
VRM on board regulator disabled	VRM	Open
VRM on board regulator enabled	VRM	Pins 6 & 7, 21 & 22 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	None
4MB	(2) 512K x 36	None
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 512K x 36	(2) 256K x 36
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 1M x 36	(2) 256K x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 2M x 36	(2) 256K x 36
20MB	(2) 2M x 36	(2) 512K x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
36MB	(2) 4M x 36	(2) 512K x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
66MB	(2) 8M x 36	(2) 256K x 36
68MB	(2) 8M x 36	(2) 512K x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs.

CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB	(8) 32K x 8	(1) 8K x 8	Not installed
256KB	None	None	256KB module installed
512KB	(8) 64K x 8	(1) 32K x 8	Not installed
512KB	None	None	512KB module installed
1MB	(8) 128K x 8	(1) 32K x 8	Not installed

CACHE JUMPER CONFIGURATION		
Size	JP13	JP14
256KB	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB	Pins 1 & 2 closed	Pins 2 & 3 closed
1MB	Pins 2 & 3 closed	Pins 2 & 3 closed

CACHE VOLTAGE CONFIGURATION	
Voltage	JP11
Mixed	Pins 3 & 4, 5 & 6 closed
3.3v	Pins 1 & 2, 7 & 8 closed

CACHE TYPE CONFIGURATION	
Type	JP12
Asynchronous	Pins 1 & 2 closed
Pipeline burst	Pins 2 & 3 closed

CPU SPEED SELECTION						
CPU speed	Clock speed	Multiplier	JP9	JP10	JP19	JP20
75MHz	50MHz	1.5x	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	Closed	Open	Open	Open
100MHz	66MHz	1.5x	Open	Closed	Open	Open
120MHz	60MHz	2x	Closed	Open	Closed	Open
133MHz	66MHz	2x	Closed	Open	Closed	Open
150MHz	60MHz	2.5x	Closed	Open	Closed	Closed

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CPU VOLTAGE SELECTION	
Voltage	JP18
3.3v	Pins 1 & 2 closed
3.4v	Pins 3 & 4 closed
3.5v	Pins 5 & 6 closed