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Processor	CX M1/Pentium
Processor Speed	75/90/100/120/133/150/166MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	275mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), VRM connector, cache slot, IR connector
NPU Options	None
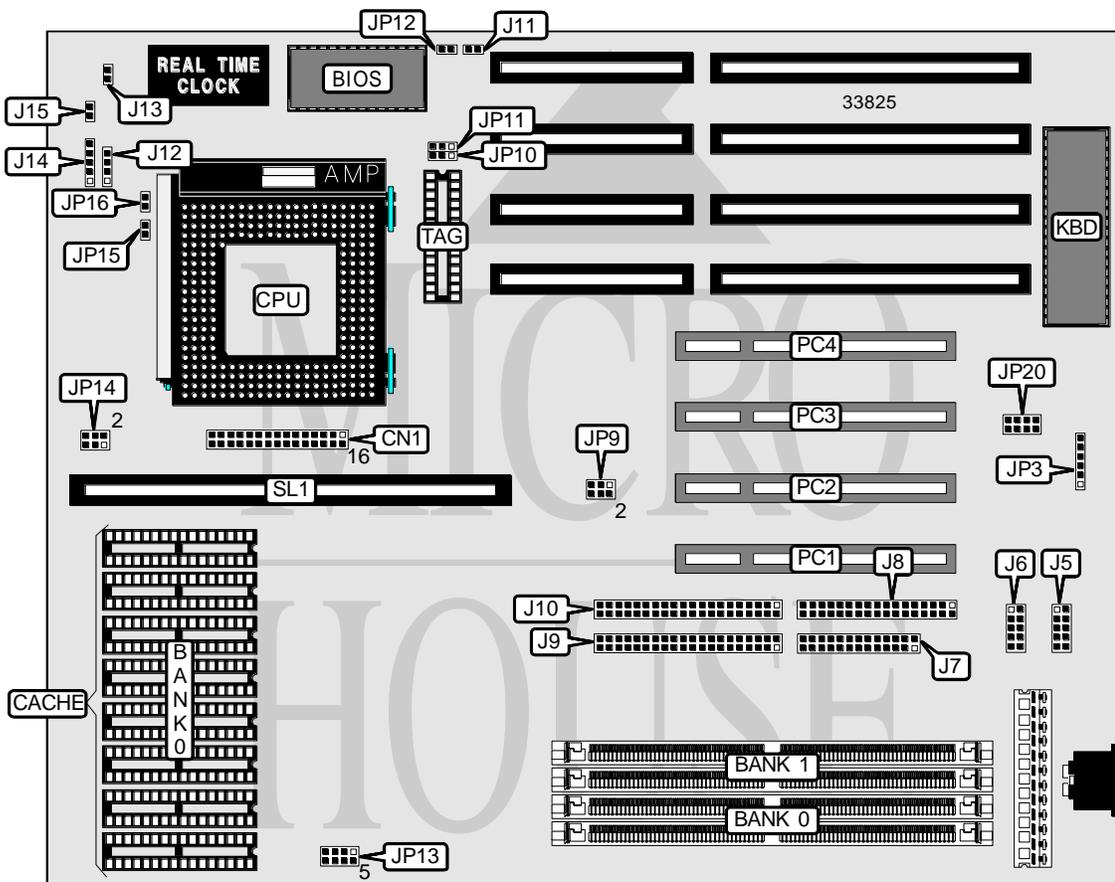
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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
VRM module connector	CN1	Speaker	J12
Serial port 1	J5	Turbo LED	J13
Serial port 2	J6	Power LED & keylock	J14
Parallel port	J7	Reset switch	J15
Floppy drive interface	J8	IR connector	JP3
IDE interface 1	J9	Green PC connector	JP12
IDE interface 2	J10	32-bit PCI slots	PC1 - PC4
IDE interface LED	J11	Cache slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í VRM on board regulator disabled	CN1	Open
VRM on board regulator enabled	CN1	Pins 6 & 7, 21 & 22 closed
í IR connector type select Intel	JP3	Pins 1 - 5 closed
IR connector type select HP	JP3	Pins 3 - 6 closed
í Factory configured - do not alter	JP20	Unidentified

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(2) 512K x 36	None
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 256K x 36	(2) 2M x 36
20MB	(2) 2M x 36	(2) 512K x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	None	(2) 4M x 36
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 256K x 36	(2) 4M x 36
36MB	(2) 4M x 36	(2) 512K x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 256K x 36	(2) 8M x 36
68MB	(2) 8M x 36	(2) 512K x 36

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Banks are interchangeable.		

CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB	(8) 32K x 8	(1) 8K x 8	Not installed
256KB	None	None	256KB module installed
512KB	(8) 64K x 8	(1) 16K or (1) 32K x 8	Not installed
512KB	None	None	512KB module installed

CACHE JUMPER CONFIGURATION	
Size	JP10
256KB	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed

CACHE TYPE CONFIGURATION	
Type	JP13
Mixed cache	Pins 1 & 5, 2 & 6 closed
3.3v cache	Pins 3 & 7, 4 & 8 closed

CPU SPEED SELECTION (CYRIX)						
CPU speed	Clock speed	Multiplier	JP9	JP11	JP15	JP16
120MHz	66MHz	1.5x	1 & 3, 4 & 6	2 & 3	Open	Open
150MHz	60MHz	2x	2 & 4, 3 & 5	2 & 3	Open	Closed
166MHz	66MHz	2x	1 & 3, 4 & 6	2 & 3	Open	Closed
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JP9	JP11	JP15	JP16
75MHz	50MHz	1.5x	3 & 5, 4 & 6	1 & 2	Open	Open
90MHz	60MHz	1.5x	2 & 4, 3 & 5	2 & 3	Open	Open
100MHz	66MHz	1.5x	1 & 3, 4 & 6	2 & 3	Open	Open
120MHz	60MHz	2x	2 & 4, 3 & 5	2 & 3	Open	Closed
133MHz	66MHz	2x	1 & 3, 4 & 6	2 & 3	Open	Closed
150MHz	60MHz	2.5x	2 & 4, 3 & 5	2 & 3	Closed	Closed
166MHz	66MHz	2.5x	1 & 3, 4 & 6	2 & 3	Closed	Closed
Note: Pins designated should be in the closed position.						

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CPU VOLTAGE SELECTION	
Voltage	**JP14**
3.3v	Pins 1 & 2 closed
3.4v	Pins 3 & 4 closed
3.5v	Pins 5 & 6 closed