Processor CX M1/AM K5/Pentium

Processor Speed 66/75/90/100/120/125/133/150/166/180/200/MHz

Chip Set **Video Chip Set** None

Maximum Onboard Memory 256MB (EDO supported)

Maximum Video Memory None Cache 256/512KB **BIOS** Award

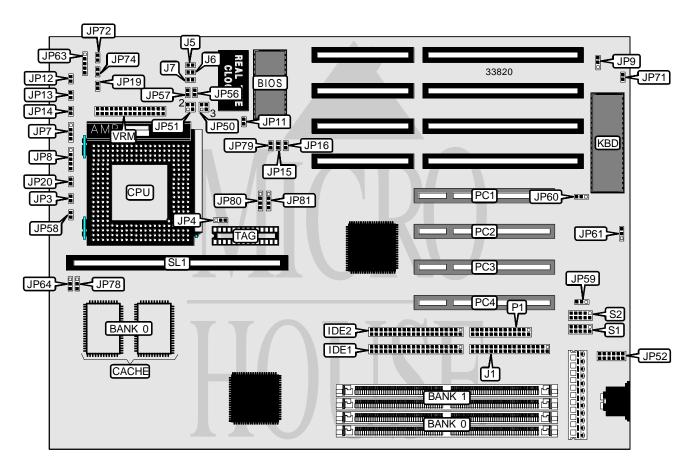
Dimensions 280mm x 220mm

I/O Options 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces

(2), parallel port, PS/2 mouse interface, serial ports (2), cache slot, IR connector,

VRM connector, USB connectors (2)

NPU Options None



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	CONNECTIONS						
Purpose	Location	Purpose	Location				
IDE interface 1	IDE1	IR connector	JP63				
IDE interface 2	IDE2	Green PC LED	JP74				
Floppy drive interface	J1	USB connector	JP80				
Speaker	JP7	USB connector	JP81				
Power LED & keylock	JP8	Parallel port	P1				
Reset switch	JP12	32-bit PCI slots	PC1 - PC4				
Turbo switch	JP13	Serial port 1	S1				
Turbo LED	JP14	Serial port 2	S2				
Green PC connector	JP19	Cache slot	SL1				
IDE interface LED	JP20	VRM connector	VRM				
PS/2 mouse interface	JP52						

USER CONFIGURABLE SETTINGS					
Function	Label	Position			
Flash BIOS voltage select 12v	JP9	Pins 2 & 3 closed			
Flash BIOS voltage select 5v	JP9	Pins 1 & 2 closed			
í CMOS memory normal operation	JP11	Open			
CMOS memory clear	JP11	Closed			
í Factory configured - do not alter	JP59	Pins 2 & 3 closed			
Monitor type select	JP71	Unidentified			
í Password normal operation	JP72	Open			
Password clear	JP72	Closed			

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36
136MB	(2) 16M x 36	(2) 1M x 36

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DRAM CONFIGURATION (CON'T)						
Size	Bank 0	Bank 1				
144MB	(2) 16M x 36	(2) 2M x 36				
160MB	(2) 16M x 36	(2) 4M x 36				
192MB	(2) 16M x 36	(2) 8M x 36				
256MB	(2) 16M x 36	(2) 16M x 36				
Note: Board accepts EDO memory. Banks are interchangeable.						

CACHE CONFIGURATION						
Size Bank 0 SL1 TAG						
256KB	(2) 32K x 32	None installed	(1) 32K x 8			
512KB (2) 32K x 32 256KB module installed (1) 32K x 8						

CACHE JUMPER CONFIGURATION					
Size	JP4	JP64			
256KB	Pins 1 & 2 closed	Pins 2 & 3 closed			
512KB	Pins 2 & 3 closed	Pins 1 & 2 closed			

CACHE TYPE CONFIGURATION				
Type JP78				
Mixed voltage	Pins 2 & 3 closed			
3.3v	Pins 1 & 2 closed			

CPU SPEED SELECTION (CYRIX)							
CPU speed Clock speed Multiplier JP3 JP15 JP16 JP58 JP79							JP79
90MHz	40MHz	2x	Open	Open	Closed	Open	Closed
120MHz	50MHz	2x	Open	Open	Open	Open	Open
150MHz	60MHz	2x	Open	Closed	Closed	Open	Open
166MHz	66MHz	2x	Open	Closed	Open	Open	Open

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP3	JP15	JP16	JP58	JP79
60MHz	66MHz	1x	Closed	Closed	Open	Open	Open
75MHz	50MHz	1.5x	Open	Open	Open	Open	Open
90MHz	60MHz	1.5x	Open	Closed	Closed	Open	Open
100MHz	66MHz	1.5x	Open	Closed	Open	Open	Open

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CPU SPEED SELECTION (INTEL)								
CPU speed	Clock speed	Multiplier	JP3	JP15	JP16	JP58	JP79	
75MHz	50MHz	1.5x	Open	Open	Open	Open	Open	
90MHz	60MHz	1.5x	Open	Closed	Closed	Open	Open	
100MHz	66MHz	1.5x	Open	Closed	Open	Open	Open	
120MHz	60MHz	2x	Closed	Closed	Closed	Open	Open	
125MHz	60MHz	2.5x	Closed	Open	Open	Closed	Open	
133MHz	66MHz	2x	Closed	Closed	Open	Open	Open	
150MHz	60MHz	2.5x	Closed	Closed	Closed	Closed	Open	
150MHz	50MHz	3x	Open	Open	Open	Closed	Open	
166MHz	66MHz	2.5x	Closed	Closed	Open	Closed	Open	
180MHz	60MHz	3x	Open	Closed	Closed	Closed	Open	
200MHz	66MHz	3x	Open	Closed	Open	Closed	Open	

CPU VOLTAGE SELECTION						
Voltage J5 J6 J7						
3.3v	Closed	Open	Open			
3.45v	Open	Closed	Open			
3.6v	Open	Open	Closed			

VL BUS WAIT STATE SELECTION								
Voltage Output Power Supply Path JP50 JP51 JP56 JP57								
On board VRM	3.3v - 3.6v onboard VRM	1 & 2, 3 & 4	Open	Closed	Closed			
Add on VRM for P55C	3.3v & 2.5v add on VRM	Open	1 & 2, 3 & 4	Open	Open			
Add on VRM for P55C	3.3v onboard VRM. 2.5v add on VRM	Open	Open	Closed	Closed			
Note: Pins designa	ated should be in the clo	sed position.		_				

DMA CHANNEL SELECTION		
Channel	JP60	JP61
í 1	Pins 1 & 2 closed	Pins 1 & 2 closed
3	Pins 2 & 3 closed	Pins 2 & 3 closed

MOUSE SELECTION		
Туре	JP52	
6 pin mouse	Pins 1 - 6 closed	
10 pin mouse	Pins 2 & 8, 3 & 9, 4 & 10, 5 & 11, 6 & 12 closed	
Note: Pins 1 is not shown on the mainboard. Pin labeling may be shown different on board than shown in this		

table.