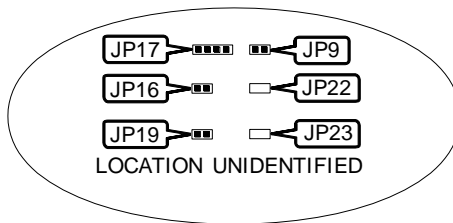
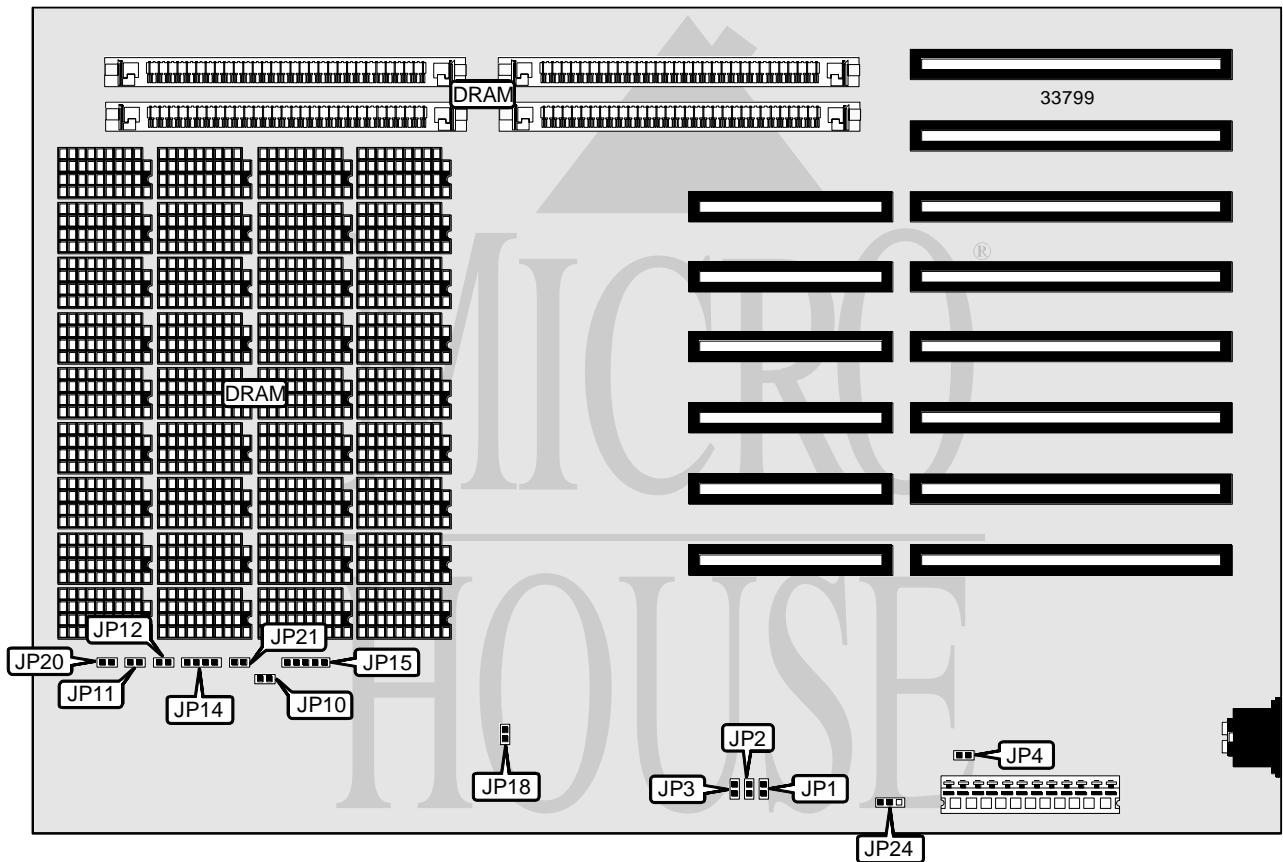


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Processor	80286
Processor Speed	12MHz
Chip Set	VLSI
Video Chip Set	None
Maximum Onboard Memory	4MB
Maximum Video Memory	None
Cache	None
BIOS	AMI/Award
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287



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CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP10	Power LED & keylock	JP15
Turbo switch	JP11	External battery	JP17
Speaker	JP14	Reset switch	JP21

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Monitor type select color	JP4	Closed
Monitor type select monochrome	JP4	Open
Turbo enabled	JP11	Closed
Turbo disabled & switchable through keyboard (CRTL, ALT, +/-)	JP11	Open
ROM wait state select 2 wait states	JP9	Closed
ROM wait state select 1 wait state	JP9	Open
í Battery type select internal	JP16	Closed
Battery type select external	JP16	Open
í BIOS type select 27128	JP18	Open
BIOS type select 27256	JP18	Closed
í Fast mode enabled	JP19	Closed
Fast mode disabled	JP19	Open
Wait state select 0 wait states	JP20	Closed
Wait state select 1 wait state	JP20	Open
í Factory configured - do not alter	JP22	Unidentified
í Factory configured - do not alter	JP23	Unidentified
Parity check disabled	JP24	Pins 1 & 2 closed
Parity check enabled	JP24	Pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(18) 41256	None	None	None
512KB	None	None	(2) 256K x 9	None
640KB	(18) 41256	(18) 4164	None	None
1MB	(18) 41256	(18) 41256	None	None
1MB	None	None	(2) 256K x 9	(2) 256K x 9
2MB	None	None	(2) 1M x 9	None
2MB	(18) 411000	None	None	None
4MB	(18) 411000	(18) 411000	None	None
4MB	None	None	(2) 1M x 9	(2) 1M x 9

Note: The location of banks 0, 1, 2 & 3 are unidentified.

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DRAM JUMPER CONFIGURATION			
Size	JP1	JP2	JP3
512KB	Closed	Closed	Closed
640KB	Closed	Open	Closed
1MB	Closed	Closed	Open
2MB	Closed	Open	Open
4MB	Open	Closed	Closed

CPU SPEED SELECTION	
Speed	JP12
6/12MHz	Open
8/12MHz	Closed