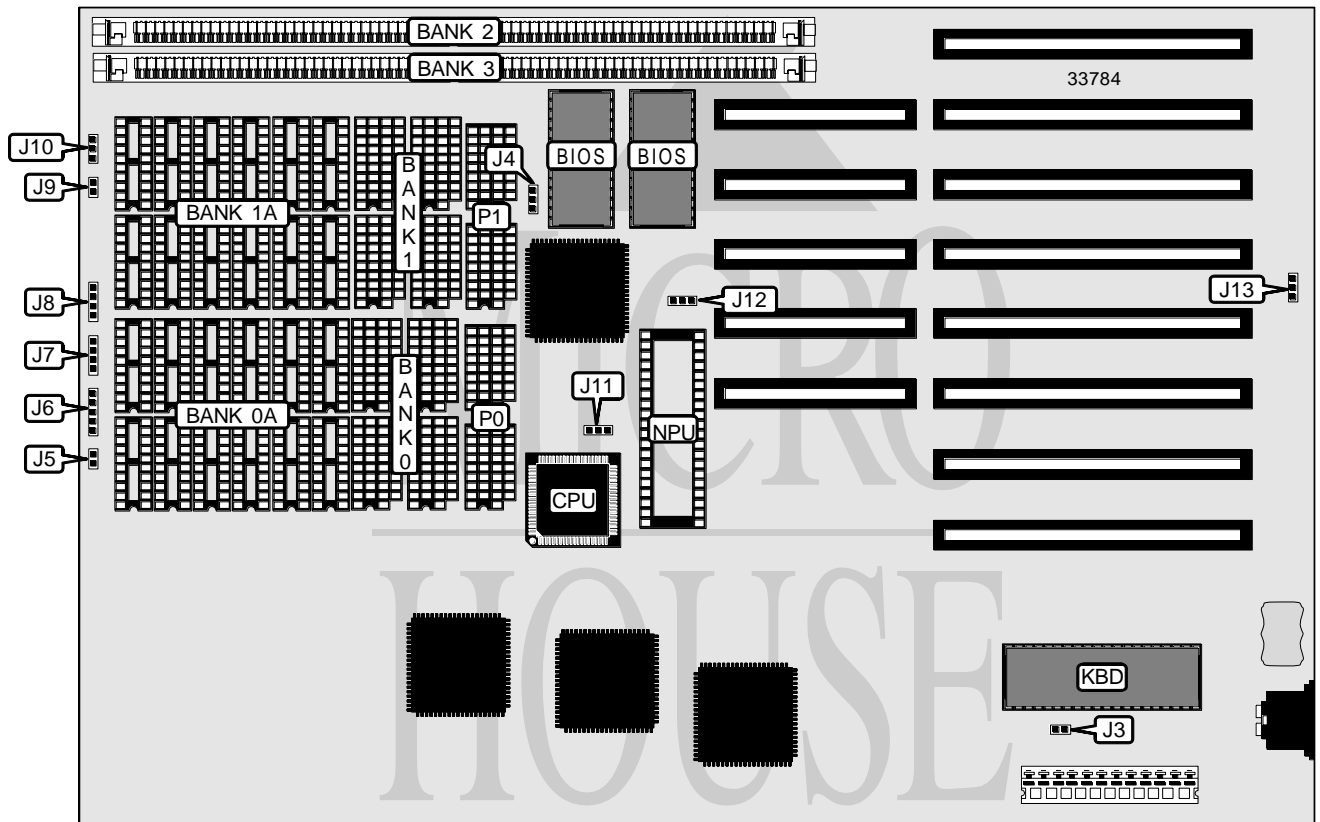


UNIDENTIFIED KW - 220 - 16

Processor	80286
Processor Speed	16MHz
Chip Set	VLSI
Video Chip Set	None
Maximum Onboard Memory	8MB
Maximum Video Memory	None
Cache	None
BIOS	Award/Phoenix
Dimensions	331mm x 220mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J5	Speaker	J8
Power LED & keylock	J6	Turbo LED	J9
PS/2 mouse port	J7	Turbo switch	J10

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Monitor type select color	J3	Closed
Monitor type select monochrome	J3	Open
í BIOS type select 27256	J4	Pins 2 & 3 closed
BIOS type select 27128	J4	Pins 1 & 2 closed
í Turbo enabled	J10	Pins 2 & 3 closed
Turbo disabled	J10	Pins 1 & 2 closed
í NPU timing input select OSC3	J11	Pins 1 & 2 closed
NPU timing input select PROCCLK	J11	Pins 2 & 3 closed
í NPU speed select normal speed	J12	Pins 2 & 3 closed
NPU speed select speed /3	J12	Pins 1 & 2 closed
í CMOS memory normal operation	J13	Pins 1 & 2 closed
CMOS memory clear	J13	Pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	Bank 0A	Bank 1	Bank 1A	Bank 2	Bank 3
512KB	(4) 44256	None	None	None	None	None
1MB	(4) 44256	None	(4) 44256	None	None	None
1.5MB	(4) 44256	None	(4) 44256	None	(2) 256K x 9	None
2MB	(4) 411000	(12) 411000	None	None	None	None
2MB	(4) 44256	None	(4) 44256	None	(2) 256K x 9	(2) 256K x 9
3MB	(4) 44256	None	(4) 44256	None	(2) 1M x 9	None
4MB	(4) 411000	(12) 411000	(4) 411000	(12) 411000	None	None
5MB	(4) 44256	None	(4) 44256	None	(2) 1M x 9	(2) 1M x 9
6MB	(4) 411000	(12) 411000	(4) 411000	(12) 411000	(2) 1M x 9	None
8MB	(4) 411000	(12) 411000	(4) 411000	(12) 411000	(2) 1M x 9	(2) 1M x 9

DRAM PARITY CONFIGURATION		
Size	P 0	P 1
512KB	(2) 41256	None
1MB	(2) 41256	(2) 41256
1.5MB	(2) 41256	(2) 41256
2MB	(2) 411000	None
2MB	(2) 41256	(2) 41256
3MB	(2) 41256	(2) 41256
4MB	(2) 411000	(2) 411000
5MB	(2) 41256	(2) 41256
6MB	(2) 411000	(2) 411000
8MB	(2) 411000	(2) 411000