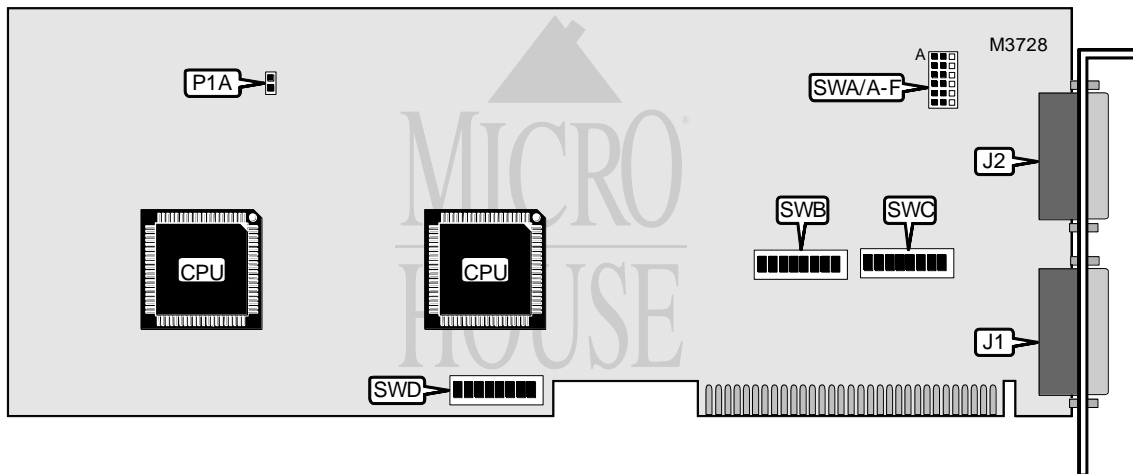


# ADVANCED DIGITAL CORPORATION

## PC II

<b>Processor</b>	80188 (2)
<b>Processor Speed</b>	8MHz
<b>Video Chipset</b>	Unidentified
<b>Chipset</b>	Unidentified
<b>Maximum Onboard Memory</b>	1MB DRAM (512KB per processor)
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	336.6 x 111.1mm
<b>I/O Options</b>	Video ports (1 per processor)
<b>Video Types Supported</b>	Monochrome
<b>Highest Resolution Supported</b>	Text only



CONNECTIONS			
Function	Label	Function	Label
Video and keyboard for machine 1	J1	12V DC power out	P1A
Video and keyboard for machine 2	J2		

Note: Standard video and keyboard connectors are provided on an external adapter box.

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	SWA/A	Pins 1 & 2 closed
í Factory configured - do not alter	SWA/B	Pins 1 & 2 closed
í Factory configured - do not alter	SWA/C	Pins 1 & 2 closed
í Factory configured - do not alter	SWA/D	Pins 1 & 2 closed
í Factory configured - do not alter	SWA/E	Pins 2 & 3 closed
í Factory configured - do not alter	SWA/F	Pins 2 & 3 closed
í Interrupt terminator enabled	SWD/5	On
Interrupt terminator disabled	SWD/5	Off
í Factory configured - do not alter	SWD/6	Off
í Factory configured - do not alter	SWD/7	Off
í Factory configured - do not alter	SWD/8	Off

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ADVANCED DIGITAL CORPORATION  
P C II

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BASE I/O ADDRESS SELECTION								
Setting	SWB/1	SWB/2	SWB/3	SWB/4	SWB/5	SWB/6	SWB/7	SWB/8
200h	On	On	On	On	On	On	On	On
202h	Off	On	On	On	On	On	On	On
204h	On	Off	On	On	On	On	On	On
206h	Off	Off	On	On	On	On	On	On
208h	On	On	Off	On	On	On	On	On
230h	On	On	On	Off	Off	On	On	On
3F6h	Off	Off	On	Off	Off	Off	Off	Off
3F8h	On	On	Off	Off	Off	Off	Off	Off
3FAh	Off	On	Off	Off	Off	Off	Off	Off
3FCh	On	Off	Off	Off	Off	Off	Off	Off
3FEh	Off	Off	Off	Off	Off	Off	Off	Off

**Note:** A total of 256 base address settings are available. The switches are a binary representation of the decimal memory addresses. SWB/8 is the Most Significant Bit and switch SWB/1 is the Least Significant Bit. The switches have the following decimal values: SWB/1=2, SWB/2=4, SWB/3=8, SWB/4=16, SWB/5=32, SWB/6=64, SWB/7=128, SWB/8=256. Turn off the switches and add the values of the switches that are off to 512 to obtain the correct memory address. (Off=1, On=0)

SHARED RAM ADDRESS								
Setting	SWC/1	SWC/2	SWC/3	SWC/4	SWC/5	SWC/6	SWC/7	SWC/8
0000h	On	On	On	On	On	On	On	On
0100h	Off	On	On	On	On	On	On	On
0200h	On	Off	On	On	On	On	On	On
0300h	Off	Off	On	On	On	On	On	On
0400h	On	On	Off	On	On	On	On	On
C000h	On	On	On	On	On	On	Off	Off
FB00h	Off	Off	On	Off	Off	Off	Off	Off
FC00h	On	On	Off	Off	Off	Off	Off	Off
FD00h	Off	On	Off	Off	Off	Off	Off	Off
FE00h	On	Off	Off	Off	Off	Off	Off	Off
FF00h	Off	Off	Off	Off	Off	Off	Off	Off

**Note:** A total of 256 base address settings are available. The switches are a binary representation of the decimal memory addresses. SWC/8 is the Most Significant Bit and switch SWC/1 is the Least Significant Bit. The switches have the following decimal values: SWC/8=524,288, SWC/7=262,144, SWC/6=131,072, SWC/5=65,536, SWC/4=32,768, SWC/3=16,384, SWC/2=8,192, SWC/1=4,096. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

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# ADVANCED DIGITAL CORPORATION

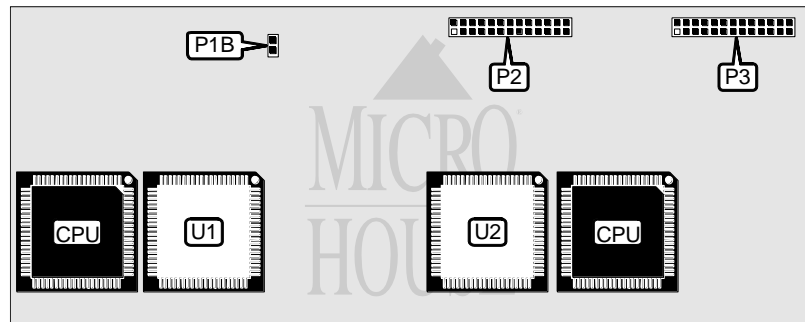
## PC II

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INTERRUPT SELECTION				
Setting	SWD/1	SWD/2	SWD/3	SWD/4
IRQ2	On	Off	Off	Off
IRQ3	Off	On	Off	Off
IRQ4	Off	Off	On	Off
IRQ5	Off	Off	Off	On

## PC II (SERIAL DAUGHTERBOARD)

**Processor** 80188 (2)  
**Processor Speed** 8MHz  
**I/O Options** Serial ports (1 per processor)



CONNECTIONS			
Function	Label	Function	Label
Serial port for machine 1	P2	Header to main board CPU socket (on back of board)	U1
Serial port for machine 2	P3	Header to main board CPU socket (on back of board)	U2
12V DC power in from main board (on back of board)	P1B		

### MISCELLANEOUS TECHNICAL NOTES

Remove the CPUs from the main card, and install them into the daughterboard's sockets.