Processor

Processor Speed

Chip Set Video Chip Set Maximum Onboard Memory Maximum Video Memory Cache BIOS Dimensions I/O Options NPU Options CX4865/80486SX/SL80486SX/486 Overdrive/SL486 Overdrive/ 80487SX/CX486DX/80486DX/SL80486DX/80486DX2/SL80486DX2/CX486DX2/80 486DX4/P24T 20/25/33/40/50(internal)/50/66(internal)/75(internal)/ 100(internal)MHz SIS None 64MB None 128/256/512/1024KB Award 254mm x 218mm 32-bit VESA local bus slots (2), green PC connector None



Continued on next page. . .

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Green PC connector	JP2
Speaker	J2	Green PC connector	JP3
Reset switch	J3	Green PC connector	JP4
Turbo LED	J4	Green PC connector	JP28
Turbo switch	J5	32-bit VESA local bus slots	SL1 & SL2
External battery	JG		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Battery type select internal	JP1	Pins 1 & 2 closed
Battery type select external	JP1	Pins 2 & 3 closed
í IRQ12 enabled	JP7	Pins 1 & 2 closed
IRQ12 disabled	JP7	Pins 2 & 3 closed
í Factory configured - do not alter	JP8	Unidentified
Monitor type select monochrome/VGA	JP9	Pins 1 & 2 closed
Monitor type select CGA	JP9	Pins 2 & 3 closed
Green PC enabled (JP28)	JP10	Pins 2 & 3 closed
Green PC disabled (RC control)	JP10	Pins 1 & 2 closed
í VESA clock delay enabled	JP24	Pins 1 & 2 closed
VESA clock delay disabled	JP24	Pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	None	None	None
2MB	(1) 256K x 36	(1) 256K x 36	None	None
2MB	(1) 512K x 36	None	None	None
4MB	(1) 1M x 36	None	None	None
5MB	(1) 256K x 36	(1) 1M x 36	None	None
6MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	None
6MB	(1) 512K x 36	None	(1) 1M x 36	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
10MB	(1) 256K x 36	(1) 256K x 36	(1) 2M x 36	None
10MB	(1) 512K x 36	None	(1) 1M x 36	(1) 1M x 36
10MB	(1) 512K x 36	None	(1) 2M x 36	None
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
12MB	(1) 2M x 36	None	(1) 1M x 36	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	None
16MB	(1) 2M x 36	None	(1) 2M x 36	None

Continued on next page...

... continued from previous page

DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
16MB	(1) 2M x 36	None	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None	None	None
17MB	(1) 256K x 36	(1) 4M x 36	None	None
18MB	(1) 256K x 36	(1) 256K x 36	(1) 4M x 36	None
18MB	(1) 512K x 36	None	(1) 4M x 36	None
20MB	(1) 1M x 36	(1) 4M x 36	None	None
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	None
24MB	(1) 2M x 36	None	(1) 4M x 36	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 8M x 36	None	None	None
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	None
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	None
40MB	(1) 2M x 36	None	(1) 4M x 36	(1) 4M x 36
40MB	(1) 2M x 36	None	(1) 8M x 36	None
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	None
64MB	(1) 8M x 36	None	(1) 8M x 36	None
Note: The location o	f banks 0, 1, 2, & 3 are ι	unidentified.		

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB (B)	(4) 64K x 8	None	(1) 32K x 8
512KB (A)	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
512KB (B)	(4) 128K x 8	None	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8

CACHE JUMPER CONFIGURATION			
Size	JP13	JP14	JP25
128KB	Open	Pins 2 & 3 closed	Pins 1 & 2 closed
256KB (A)	Open	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB (B)	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
512KB (A)	Pins 4 & 5 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
512KB (B)	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
1MB	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed

Continued on next page. . .

... continued from previous page

CPU SPEED SELECTION			
Speed	JP21	JP22	JP23
20MHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
25MHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
33MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
40MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
50iMHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
50MHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
66iMHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
75iMHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
100iMHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU TYPE SELECTION					
Туре	JP16	JP17	JP18	JP19	JP20
CX468S	2 & 3, 4 & 5	1&2	1 & 2, 3 & 4	2&3	2&3
80486SX	Open	Open	Open	2&3	2 & 3
SL80486SX	3 & 4, 5 & 6	Open	4 & 5	2&3	2 & 3
486 Overdrive	Open	Open	Open	1&2	2&3
SL486 Overdrive	5&6	Open	4 & 5	1&2	2&3
80487SX	Open	Open	Open	1&2	2 & 3
CX486DX	2 & 3, 4 & 5	1&2	1 & 2, 3 & 4	1&2	2 & 3
80486DX	Open	Open	Open	1&2	2&3
SL80486DX	3 & 4, 5 & 6	Open	4 & 5	1&2	2&3
80486DX2	Open	Open	Open	1&2	2 & 3
SL80486DX2	3 & 4, 5 & 6	Open	4 & 5	1&2	2 & 3
CX486DX2	2 & 3, 4 & 5	1&2	1 & 2, 3 & 4	1&2,4&5	2&3
80486DX4 (2x)	3 & 4, 5 & 6	5&6	4 & 5	1&2	2&3
80486DX4 (2.5x)	3 & 4, 5 & 6	4 & 5	4 & 5	1&2	2&3
80486DX4 (3x)	3 & 4, 5 & 6	Open	4 & 5	1&2	2 & 3
P24T	5&6	Open	4 & 5	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					

CPU TYPE SELECTION		
Туре	JP11	
Cyrix	Pins 1 & 2 closed	
Intel	Pins 2 & 3 closed	

Continued on next page. . .

... continued from previous page

	CPU HARDWARE TRAP SELECTION	
Туре	JP5	JP6
CX468S	Pins 2 & 3 closed	Pins 1 & 2 closed
80486SX	Pins 1 & 2 closed	Pins 2 & 3 closed
SL80486SX	Pins 1 & 2 closed	Pins 2 & 3 closed
CX486DX	Pins 2 & 3 closed	Pins 1 & 2 closed
80486DX	Pins 1 & 2 closed	Pins 2 & 3 closed
SL80486DX	Pins 1 & 2 closed	Pins 2 & 3 closed
80486DX2	Pins 1 & 2 closed	Pins 2 & 3 closed
SL80486DX2	Pins 1 & 2 closed	Pins 2 & 3 closed
CX486DX2	Pins 2 & 3 closed	Pins 1 & 2 closed
80486DX4	Pins 1 & 2 closed	Pins 2 & 3 closed
P24T	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU VOLTAGE SELECTION		
Voltage	JP12	JP15
3v	Pins 2 & 3 closed	Pins 2 & 3 closed
5v	Pins 1 & 2 closed	Pins 1 & 2 closed

VL BUS WAIT STATE SELECTION		
Setting	JP26	
0	Pins 1 & 2 closed	
1	Pins 2 & 3 closed	

VL BUS SPEED SELECTION	
Speed	JP27
<= 33MHz	Pins 1 & 2 closed
>33 MHz	Pins 2 & 3 closed