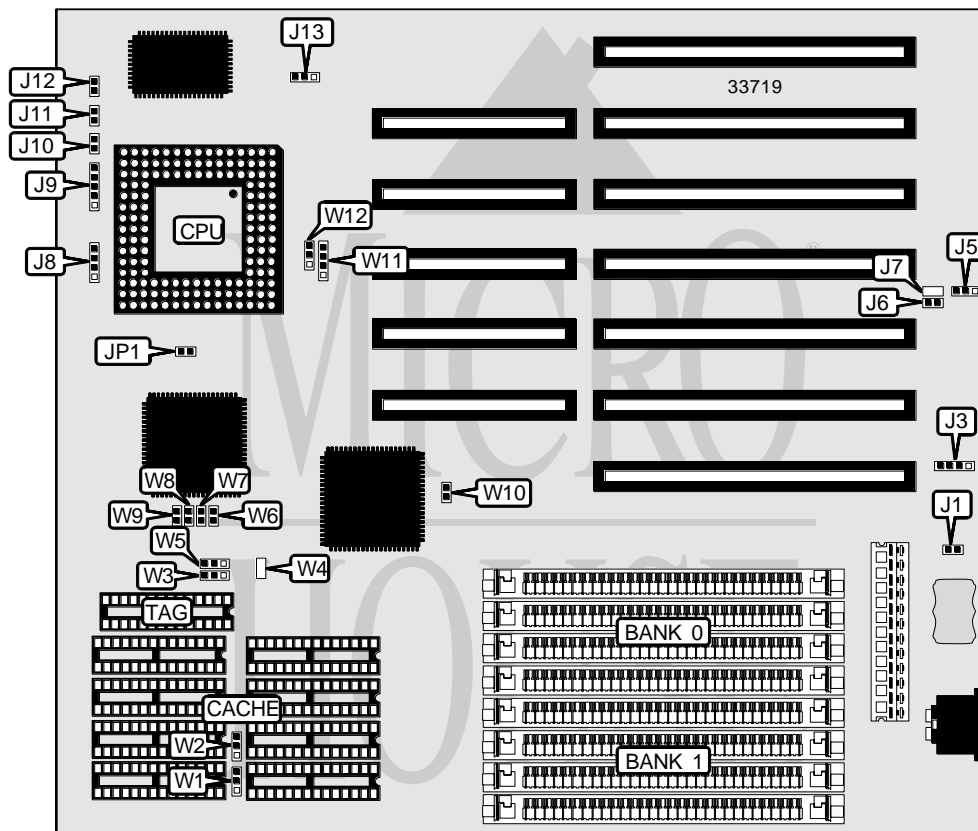


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U486 (ML-U486DX50/33HS/V1)

Processor	80486SX/80487SX/80486DX
Processor Speed	33/50MHz
Chip Set	UMC
Video Chip Set	None
Maximum Onboard Memory	32MB
Maximum Video Memory	None
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Turbo LED	J11
Speaker	J8	Reset switch	J12
Power LED & keylock	J9	Turbo switch	JP1
Turbo switch	J10		

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U486 (ML-U486DX50/33HS/V1)

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Battery type select internal	J1	Closed
Battery type select external	J1	Open
í NPU synchronous with CPU	J5	Pins 2 & 3 closed
NPU asynchronous with CPU	J5	Pins 1 & 2 closed
í Monitor type select color	J6	Closed
Monitor type select monochrome	J6	Open
í Factory configured - do not alter	J7	Unidentified
í CMOS memory normal operation	J13	Pins 1 & 2 closed
CMOS memory clear	J13	Pins 2 & 3 closed
í Factory configured - do not alter	W4	Unidentified

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION (33MHZ CPU)			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

Note: The location of banks 0 & 1 are unidentified. The speed for the cache is 15ns and 25ns for the TAG.

CACHE CONFIGURATION (50MHZ CPU)			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

Note: The location of banks 0 & 1 are unidentified. The speed for the cache is 15ns and 20ns for the TAG.

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U486 (ML-U486DX50/33HS/V1)

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CACHE JUMPER CONFIGURATION								
Size	W1	W2	W3	W5	W6	W7	W8	W9
64KB	1 & 2	Open	1 & 2	1 & 2	Open	Open	Open	Open
128KB	2 & 3	1 & 2	1 & 2	2 & 3	Closed	Open	Open	Closed
256KB	1 & 2	2 & 3	2 & 3	2 & 3	Closed	Closed	Closed	Closed

Note: Pins designated should be in the closed position.

CPU CLOCK SELECTION	
Setting	W10
1x	Open
2x	Closed

CPU TYPE SELECTION		
Type	W11	W12
80486SX	Pins 2 & 3 closed	Open
80487SX	Pins 1 & 2, 3 & 4 closed	Pins 2 & 3 closed
80486DX	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed

TURBO SELECTION		
Setting	J10	JP1
Enabled	Closed	Open
Disabled	Open	Closed