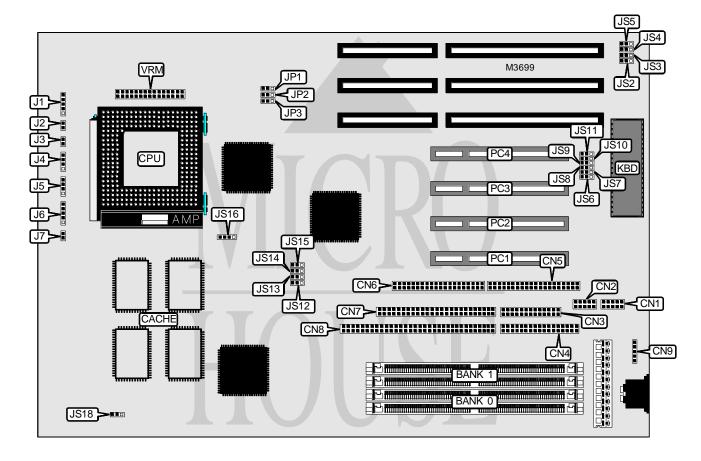
Processor	Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI
Dimensions	330mm x 221mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces
	(2), SCSI interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR
	connector, VRM connector
NPU Options	None



Continued on next page. . .

... continued from previous page

CONNECTIONS			
Purpose	Purpose	Location	
Serial port 1	CN1	IR connector	J1
Serial port 2	CN2	Green PC connector	J2
Parallel port	CN3	Reset switch	J3
Floppy drive interface	CN4	Speaker	J4
IDE interface 2	CN5	IDE interface LED	J5
IDE interface 1	CN6	Power LED & keylock	JG
Fast SCSI interface	CN7	Turbo LED	J7
Ultra SCSI interface	CN8	32-bit PCI slots	PC1 - PC4
PS/2 mouse interface	CN9	VRM connector	VRM

USER CONFIGURABLE SETTINGS				
Function Label Position				
í Monitor type select color	JP1	Pins 1 & 2 closed		
Monitor type select monochrome	JP1	Pins 2 & 3 closed		
í Password normal operation	JP2	Pins 2 & 3 closed		
Password clear	JP2	Pins 1 & 2 closed		
Flash BIOS voltage select 12v	JP3	Pins 1 & 2 closed		
Flash BIOS voltage select 5v	JP3	Pins 2 & 3 closed		
í CMOS memory normal operation	JS2	Pins 2 & 3 closed		
CMOS memory clear	JS2	Pins 1 & 2 closed		
í PS/2 mouse enabled	JS3	Pins 1 & 2 closed		
PS/2 mouse disabled	JS3	Pins 2 & 3 closed		
í Parallel port IRQ select IRQ7	JS8	Pins 1 & 2 closed		
Parallel port IRQ select IRQ5	JS8	Pins 2 & 3 closed		
í SCSI bus data width select Fast 8-bit	JS15	Pins 2 & 3 closed		
SCSI bus data width select Fast & wide 16-bit	JS15	Pins 1 & 2 closed		

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	
8MB	(2) 1M x 36	None	
16MB	(2) 2M x 36	None	
16MB	(2) 1M x 36	(2) 1M x 36	
24MB	(2) 1M x 36	(2) 2M x 36	
32MB	(2) 4M x 36	None	
32MB	(2) 2M x 36	(2) 2M x 36	
40MB	(2) 1M x 36	(2) 4M x 36	
48MB	(2) 2M x 36	(2) 4M x 36	
64MB	(2) 8M x 36	None	
64MB	(2) 4M x 36	(2) 4M x 36	
72MB	(2) 1M x 36	(2) 8M x 36	
80MB	(2) 2M x 36	(2) 8M x 36	
96MB	(2) 4M x 36	(2) 8M x 36	

Continued on next page. . .

... continued from previous page

	DRAM CONFIGURATION (CON'T)	
Size	Bank 0	Bank 1
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 1M x 36	(2) 16M x 36
144MB	(2) 2M x 36	(2) 16M x 36
160MB	(2) 4M x 36	(2) 16M x 36
192MB	(2) 8M x 36	(2) 16M x 36
256MB	(2) 16M x 36	(2) 16M x 36
256MB	(2) 32M x 36	None
264MB	(2) 1M x 36	(2) 32M x 36
272MB	(2) 2M x 36	(2) 32M x 36
288MB	(2) 4M x 36	(2) 32M x 36
320MB	(2) 8M x 36	(2) 32M x 36
384MB	(2) 16M x 36	(2) 32M x 36
512MB	(2) 32M x 36	(2) 32M x 36
lote: Board accepts EDO memory.	Board also accepts x 32 SIMMs.	

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	
256KB	(2) 32K x 32	None	(1) 16K/32K x 8	
512KB (2) 32K x 32 (2) 32K x 32 (1) 16K/32K x 8				
Note: The location of bank	s 0 & 1 are unidentified.			

CPU SPEED SELECTION						
CPU speed	Clock speed	Multiplier	JS12	JS13	JS14	JS16
75MHz	50MHz	1.5x	1&2	2 & 3	2&3	Open
90MHz	60MHz	1.5x	1&2	1 & 2	1&2	Open
100MHz	66MHz	1.5x	2&3	2 & 3	1&2	Open
120MHz	60MHz	2x	1 & 2	1 & 2	1&2	3 & 4
133MHz	66MHz	2x	2&3	2 & 3	1&2	3 & 4
150MHz	60MHz	2.5x	1&2	1 & 2	1&2	1&2,3&4
166MHz	66MHz	2.5x	2&3	2 & 3	1&2	1&2,3&4
180MHz	60MHz	3x	1 & 2	1 & 2	1&2	1 & 2
200MHz	66MHz	3x	2&3	2 & 3	1&2	1 & 2
Note: Pins designated should be in the closed position.						

CPU VOLTAGE SELECTION			
Voltage JS18			
3.3v	Pins 2 & 3 closed		
í3.45v	Pins 1 & 2 closed		

Continued on next page. . .

. . . continued from previous page

DMA CHANNEL SELECTION				
Channel JS9 JS10 JS11				
í Normal mode	Pins 2 & 3 closed	Open	Open	
1	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	
2	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	

FLOPPY DRIVE SELECTION				
Setting	JS6	JS7		
í Normal mode	Pins 2 & 3 closed	Pins 1 & 2 closed		
Enhanced mode	Pins 1 & 2 closed	Pins 2 & 3 closed		

SERIAL PORT 2 SELECTION				
Setting	JS4	JS5		
í Used as serial port 2	Pins 1 & 2 closed	Pins 1 & 2 closed		
Used as IR connector	Pins 2 & 3 closed	Pins 2 & 3 closed		