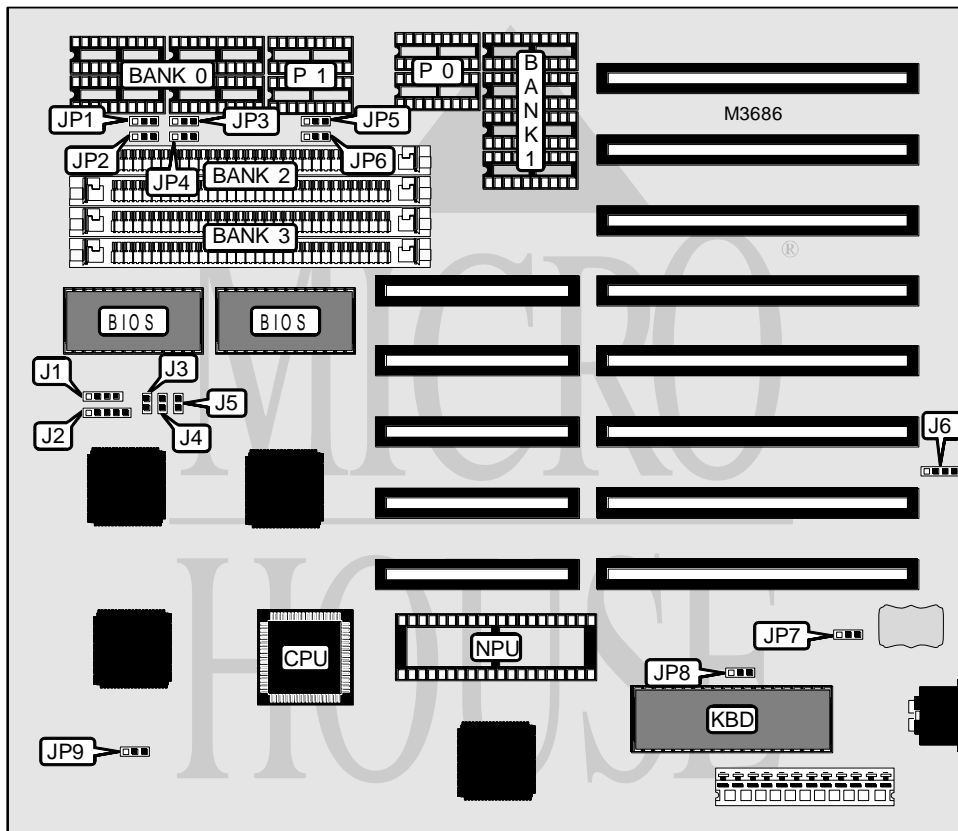


DTK COMPUTER, INC.

TECH-1632

Processor	80286
Processor Speed	16MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	5MB
Maximum Video Memory	None
Cache	None
BIOS	Unidentified
Dimensions	254mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Reset switch	J4
Power LED & keylock	J2	Turbo switch	J5
Turbo LED	J3	External battery	J6

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DTK COMPUTER, INC.
TECH-1632

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
Battery type select internal	JP7	Pins 2 & 3 closed
Battery type select external	JP7	Pins 1 & 2 closed
Monitor type select color	JP8	Pins 2 & 3 closed
Monitor type select monochrome	JP8	Pins 1 & 2 closed
Power fail detect circuit enabled	JP9	Pins 1 & 2 closed
External power good function enabled	JP9	Pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	P 0	Bank 1	P 1	Bank 2	Bank 3
512KB (A)	(4) 44256	(2) 41256	None	None	None	None
512KB (B)	None	None	None	None	(2) 256K x 9	None
640KB (A)	(4) 44256	(2) 41256	None	None	None	(2) 64K x 9
640KB (B)	None	None	None	None	(2) 256K x 9	(2) 64K x 9
1MB (A)	None	None	None	None	(2) 256K x 9	(2) 256K x 9
1MB (B)	(4) 44256	(2) 41256	(4) 44256	(2) 41256	None	None
1MB (C)	(4) 44256	(2) 41256	None	None	(2) 256K x 9	None
1MB (D)	(4) 44256	(2) 41256	None	None	None	(2) 256K x 9
1.5MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	None
2MB (A)	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	(2) 256K x 9
2MB (B)	None	None	None	None	(1) 1M x 9	None
3MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(1) 1M x 9	None
4MB	None	None	None	None	(1) 1M x 9	(1) 1M x 9
5MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(1) 1M x 9	(1) 1M x 9

DRAM CONFIGURATION						
Size	JP1	JP2	JP3	JP4	JP5	JP6
512KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
512KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
640KB (A)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
640KB (B)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1MB (A)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1MB (B)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1MB (C)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1MB (D)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1.5MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB (B)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
3MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
5MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.