## SANTA CLARA SYSTEMS, INC.

## AT TERMINAL

| Processor | 80286 |
| :--- | :--- |
| Processor Speed | Unidentified |
| Chip Set | Unidentified |
| Video Chip Set | None |
| Maximum Onboard Memory | 1 MB |
| Maximum Video Memory | None |
| Cache | None |
| BIOS | Unidentified |
| Dimensions | $355 m m \times 304 m m$ |
| I/O Options | Network connectors (2) |
| NPU Options | 80287 |



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SANTA CLARA SYSTEMS, INC.

## AT TERMINAL

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| CONNECTIONS |  |  |  |
| :---: | :---: | :---: | :---: |
| Purpose | Location | Purpose | Location |
| Network connector | CN1 | Network connector | CN2 |
| USER CONFIGURABLE SETTINGS |  |  |  |
| Function |  |  | Position |
| í Factory configured - do not alter |  | W1/3 | Unidentified |
| í Factory configured - do not alter |  | W1/4 | Unidentified |
| Internal Network Data Bus enabled |  | W12 | Closed |
| Internal Network Data Bus disabled |  | W12 | Open |
| Note: The number of jumpers that comprise W12 are unidentified. |  |  |  |


| DRAM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 |
| $256 \mathrm{~KB}(\mathrm{~A})$ | $(18) 4164$ | $(18) 4164$ |
| $256 \mathrm{~KB}(\mathrm{~B})$ | $(18) 41256$ | None |
| 640 KB | $(18) 41256$ | $(18) 4164$ |
| 1 MB | $(18) 41256$ | $(18) 41256$ |
| Note: The location of banks 0 \& 1 are unidentified. |  |  |


| DRAM SWITCH CONFIGURATION |  |  |  |
| :---: | :---: | :---: | :---: |
| Size | W1/1 | W1/2 |  |
| $256 \mathrm{~KB}(\mathrm{~A})$ | On | On |  |
| $256 \mathrm{~KB}(\mathrm{~B})$ | On | Off |  |
| 640 KB | Off | On |  |
| 1 MB | Off | Off |  |


| DMA CHANNEL SELECTION |  |  |  |
| :---: | :---: | :---: | :---: |
| Channel | W6 | W7 |  |
| 0 | Pins 1 \& 2 closed | Pins 1 \& 2 closed |  |
| 1 | Pins 3 \& 4 closed | Pins 3 \& 4 closed |  |
| 2 | Pins 5 \& 6 closed | Pins 5 \& 6 closed |  |
| 3 | Pins 7 \& 8 closed | Pins 7 \& 8 closed |  |

IRQ SELECTION (TXD/RXD)

| IRQ |  |
| :---: | :---: |
| $\mathbf{i} 2$ | W8 |
| 3 | Pins 3 \& 4 closed |
| 4 | Pins 5 \& 6 closed |
| 5 | Pins 7 \& 8 closed |

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## AT TERMINAL

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IRQ SELECTION (TIMEOUT)

| IRQ |  |
| :---: | :---: |
| 2 | Wins 1 \& 2 closed |
| $\mathbf{i} 3$ | Pins 3 \& 4 closed |
| 4 | Pins 5 \& 6 closed |
| 5 | Pins 7 \& 8 closed |


| Setting |  |  |  | WORT ADDRESS SELECTION |
| :---: | :---: | :---: | :---: | :---: |
| $2 \times X h$ | Pins 2 \& 3 closed | W10 |  |  |
| í 3XXh | Pins 1 \& 2 closed | N/A |  |  |
| í X8Fh | N/A | N/A |  |  |
| X9Fh | N/A | Pins 1 \& 2 closed |  |  |


| NETWORK ID SELECTION |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ID | U189/1 | U189/2 | U189/3 | U189/4 | U189/5 | U189/6 | U189/7 | U189/8 |
| 0011 | Off | Off | Off | Off | Off | Off | Off | Off |
| 0012 | Off | Off | Off | Off | Off | Off | Off | Off |
| 0013 | Off | Off | Off | Off | Off | Off | Off | Off |
| 0014 | Off | Off | Off | Off | Off | Off | Off | Off |
| 0015 | Off | Off | Off | Off | Off | Off | Off | Off |
| 0016 | Off | Off | Off | Off | Off | Off | Off | Off |
| 0017 | Off | Off | Off | Off | Off | Off | Off | Off |
| 0018 | Off | Off | Off | Off | Off | Off | Off | Off |


| NETWORK ID SELECTION (CON'T) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ID | U192/1 | U192/2 | U192/3 | U192/4 | U192/5 | U192/6 | U192/7 | U192/8 |
| 0011 | Off | Off | Off | On | Off | Off | Off | On |
| 0012 | Off | Off | Off | On | Off | Off | On | Off |
| 0013 | Off | Off | Off | On | Off | Off | On | On |
| 0014 | Off | Off | Off | On | Off | On | Off | Off |
| 0015 | Off | Off | Off | On | Off | On | Off | On |
| 0016 | Off | Off | Off | On | Off | On | On | Off |
| 0017 | Off | Off | Off | On | Off | On | On | On |
| 0018 | Off | Off | Off | On | On | Off | Off | Off |


| BIOS SELECTION |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Type | W2/1 | W2/2 | W2/3 | W2/4 |
| 27128 | On | On | Off | Off |
| 27256 | Off | Off | On | On |


| SERIAL PORT INTERRUPT SELECTION |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| IRQ | W4/1 | W4/2 | W4/3 | W4/4 |  |
| Serial port 1 IRQ4 | On | On | Off | Off |  |
| Serial port 2 IRQ3 | Off | Off | On | On |  |

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## AT TERMINAL

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| PARALLEL PORT INTERRUPT SELECTION |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| IRQ | W4/5 | W4/6 | W4/7 | W4/8 |  |
| Parallel port 1 IRQ7 | On | On | Off | Off |  |
| Parallel port 2 IRQ5 | Off | Off | On | On |  |

