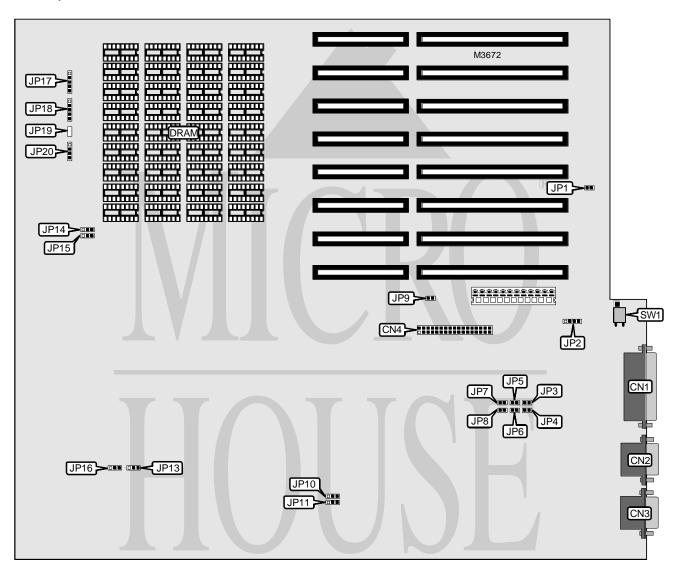
SAMSUNG ELECTRONICS, INC. S 500

80286 **Processor Processor Speed** 16/20MHz **Chip Set** Unidentified **Video Chip Set** None **Maximum Onboard Memory** 1MB **Maximum Video Memory** None Cache None **BIOS** Unidentified **Dimensions** 355mm x 304mm

I/O Options Floppy drive interface, parallel port, serial ports (2)

NPU Options 80287



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| CONNECTIONS | | | |
|------------------------|----------|---------------------|----------|
| Purpose | Location | Purpose | Location |
| Parallel port | CN1 | External battery | JP2 |
| Serial port 1 | CN2 | Keyboard connector | JP17 |
| Serial port 2 | CN3 | Power LED & keylock | JP18 |
| Floppy drive interface | CN4 | Speaker | JP20 |

| USER CONFIGURABLE SETTINGS | | |
|--|-------|-------------------|
| Function | Label | Position |
| Parallel port enabled | JP1 | Closed |
| Parallel port disabled | JP1 | Open |
| Serial port 1 enabled | JP3 | Closed |
| Serial port 1 disabled | JP3 | Open |
| Serial port 2 enabled | JP4 | Closed |
| Serial port 2 disabled | JP4 | Open |
| Floppy drive secondary address enabled | JP5 | Closed |
| Floppy drive primary address enabled | JP5 | Open |
| Floppy drive speed select dual speed | JP7 | Closed |
| Floppy drive speed select single speed | JP7 | Open |
| Precompensation value select 187ns | JP8 | Closed |
| Precompensation value select 125ns | JP8 | Open |
| DMA CLOCK = DMA CLOCK | JP13 | Pins 1 & 2 closed |
| DMA CLOCK = SYS CLOCK | JP13 | Pins 2 & 3 closed |
| BIOS type select 27256 | JP14 | Pins 1 & 2 closed |
| BIOS type select 27128 | JP14 | Pins 2 & 3 closed |
| ROM enable signal select XMER signal | JP15 | Pins 1 & 2 closed |
| ROM enable signal select LCSROM signal | JP15 | Pins 2 & 3 closed |
| í Factory configured - do not alter | JP19 | Unidentified |
| Monitor type select color | SW1 | Up |
| Monitor type select monochrome | SW1 | Down |

| | | DRAM CONFIGURATION | N | |
|---|-----------|--------------------|-----------|-----------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 256KB | (9) 41256 | None | None | None |
| 512KB | (9) 41256 | (9) 41256 | None | None |
| 640KB | (9) 41256 | (9) 41256 | (9) 4164 | None |
| 1MB | (9) 41256 | (9) 41256 | (9) 41256 | (9) 41256 |
| Note: The location of banks 0, 1, 2 & 3 are unidentified. | | | | |

| DRAM JUMPER CONFIGURATION | | |
|---------------------------|-------------------|-------------------|
| Size | JP10 | JP11 |
| 256KB | Pins 2 & 3 closed | Pins 2 & 3 closed |
| 512KB | Pins 2 & 3 closed | Pins 1 & 2 closed |
| 640KB | Pins 1 & 2 closed | Pins 2 & 3 closed |
| 1MB | Pins 1 & 2 closed | Pins 1 & 2 closed |

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| CPU SPEED SELECTION | | |
|-----------------------------|-------------------|--|
| Speed | JP16 | |
| 16MHz | Open | |
| 20MHz | Pins 1 & 2 closed | |
| 16/20MHz (through keyboard) | Pins 2 & 3 closed | |

| ON BOARD FLOPPY DRIVE SELECTION | | |
|---------------------------------|--------|--------|
| Setting | JP6 | JP9 |
| Enabled | Closed | Closed |
| Disabled | Open | Open |