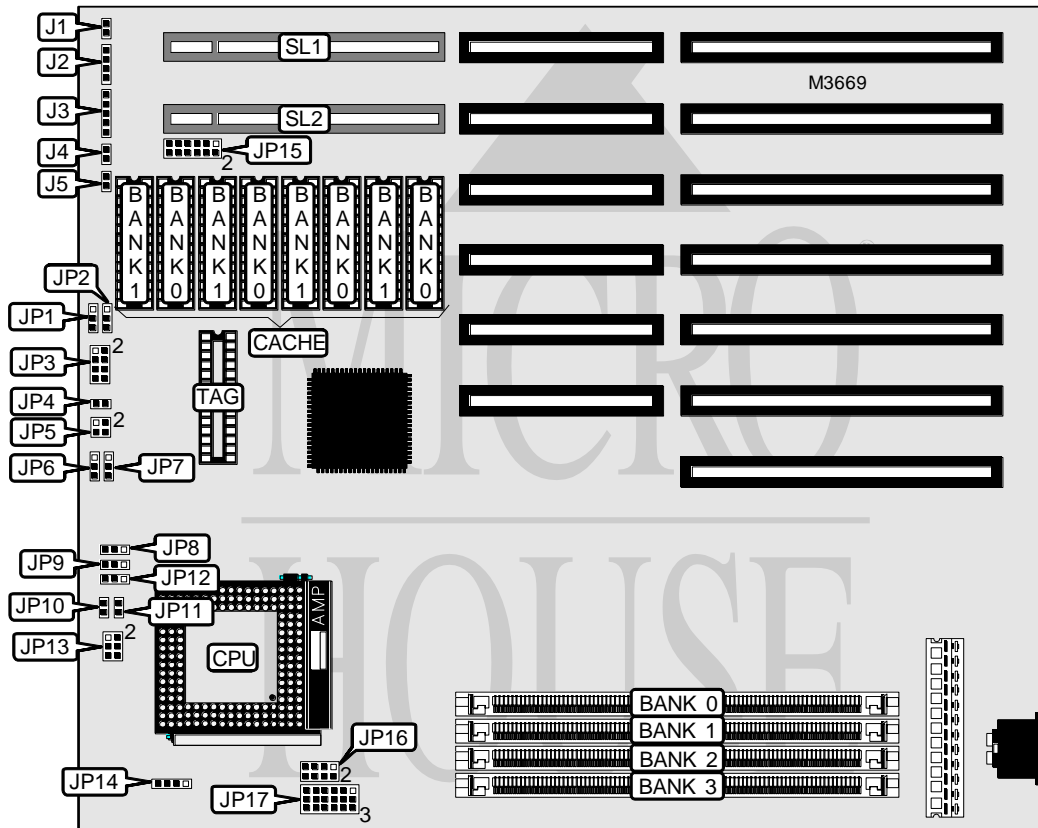


ALARIS, INC. LEOPARD VX (TORNADO LX REV. D2)

Processor	80486SX/80486SX2/80486DX2/CX486DX2/AM486DX2/80486DX2/ CX486DX4/AM486DX4/80486DX4/P24D/P24T/CXM1SC
Processor Speed	25/33/50(internal)/63(internal)66(internal)/75(internal)/80(internal) /83(internal)/100(internal)MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	128/256KB
BIOS	Alaris
Dimensions	254mm x 203mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



Continued on next page. . .

ALARIS, INC.

LEOPARD VX (TORNADO LX REV. D2)

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J1	Turbo switch	J4
Speaker	J2	Turbo LED	J5
Power LED & keylock	J3	32-bit VESA local bus slots	SL1 & SL2

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP18	Open
CMOS memory clear	JP18	Closed
í Password disabled	JP19	Open
Password enabled	JP19	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	None	None	None
8MB	(1) 2M x 36	None	None	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
12MB	(1) 2M x 36	(1) 1M x 36	None	None
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
16MB	(1) 4M x 36	None	None	None
16MB	(1) 2M x 36	(1) 1M x 36	(1) 1M x 36	None
16MB	(1) 2M x 36	(1) 2M x 36	None	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
20MB	(1) 4M x 36	(1) 1M x 36	None	None
20MB	(1) 2M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
20MB	(1) 1M x 36	(1) 4M x 36	None	None
24MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	None
24MB	(1) 4M x 36	(1) 2M x 36	None	None
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	None
24MB	(1) 2M x 36	(1) 4M x 36	None	None
28MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
32MB	(1) 8M x 36	None	None	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
36MB	(1) 8M x 36	(1) 1M x 36	None	None
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	None
40MB	(1) 8M x 36	(1) 1M x 36	(1) 1M x 36	None
40MB	(1) 8M x 36	(1) 2M x 36	None	None
40MB	(1) 2M x 36	(1) 4M x 36	(1) 4M x 36	None
40MB	(1) 2M x 36	(1) 8M x 36	None	None
44MB	(1) 8M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
48MB	(1) 8M x 36	(1) 2M x 36	(1) 2M x 36	None
48MB	(1) 8M x 36	(1) 4M x 36	None	None

Continued on next page...

ALARIS, INC.

LEOPARD VX (TORNADO LX REV. D2)

... continued from previous page

DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
52MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
56MB	(1) 8M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
56MB	(1) 2M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 4M x 36	(1) 4M x 36	None
64MB	(1) 8M x 36	(1) 8M x 36	None	None
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
72MB	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36	None
80MB	(1) 8M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	None
104MB	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36

Note: Board also accepts x 32 SIMMs.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP1	JP2	JP4
128KB	Pins 1 & 2 closed	Pins 1 & 2 closed	Open
256KB	Pins 2 & 3 closed	Pins 2 & 3 closed	Closed

CPU SPEED SELECTION	
Speed	JP5
25MHz	Pins 1 & 2 closed
33MHz	Pins 3 & 4 closed
50iMHz	Pins 1 & 2 closed
63iMHz	Pins 3 & 4 closed
66iMHz	Pins 3 & 4 closed
75iMHz	Pins 1 & 2 closed
80iMHz	Pins 1 & 2, 3 & 4 closed
83iMHz	Pins 3 & 4 closed
100iMHz	Pins 3 & 4 closed

Continued on next page...

ALARIS, INC.

LEOPARD VX (TORNADO LX REV. D2)

... continued from previous page

CPU TYPE SELECTION					
Type	JP3	JP6	JP7	JP8	JP9
80486SX	Open	2 & 3	2 & 3	1 & 2	1 & 2
80486SX2	Open	2 & 3	2 & 3	1 & 2	1 & 2
80486DX	Open	2 & 3	2 & 3	1 & 2	1 & 2
CX486DX2-V66	1 & 2, 5 & 6	2 & 3	1 & 2	1 & 2	1 & 2
CX486DX2-V80	1 & 2, 5 & 6	2 & 3	1 & 2	1 & 2	1 & 2
AM486DX2 (3v)	Open	2 & 3	1 & 2	1 & 2	2 & 3
AM486DX2 (5v)	Open	2 & 3	2 & 3	1 & 2	2 & 3
80486DX2	Open	2 & 3	2 & 3	1 & 2	1 & 2
CX486DX4	1 & 2, 5 & 6	2 & 3	2 & 3	1 & 2	1 & 2
CX486DX4 (B)	5 & 6	2 & 3	2 & 3	1 & 2	1 & 2
AM486DX4	Open	2 & 3	2 & 3	1 & 2	2 & 3
80486DX4	Open	2 & 3	2 & 3	1 & 2	1 & 2
P24D	5 & 6	2 & 3	2 & 3	1 & 2	1 & 2
P24T	5 & 6	2 & 3	1 & 2	1 & 2	1 & 2
CXM1SC	5 & 6	2 & 3	2 & 3	2 & 3	1 & 2

Note: Pins designated should be in the closed position. (B) = Cyrix processor with Intel pin-out.

CPU TYPE SELECTION (CON'T)					
Type	JP10	JP11	JP12	JP13	JP14
80486SX	Open	Open	1 & 2	Open	2 & 3
80486SX2	Open	Open	1 & 2	Open	2 & 3
80486DX	Open	Open	1 & 2	Open	1 & 2, 3 & 4
CX486DX2-V66	Open	Open	1 & 2	Open	1 & 2, 3 & 4
CX486DX2-V80	Open	Open	2 & 3	Open	1 & 2, 3 & 4
AM486DX2 (3v)	Open	Open	2 & 3	Open	1 & 2, 3 & 4
AM486DX2 (5v)	Open	Open	1 & 2	Open	1 & 2, 3 & 4
80486DX2	Open	Open	1 & 2	Open	1 & 2, 3 & 4
CX486DX4	Open	Open	1 & 2	Open	1 & 2, 3 & 4
CX486DX4 (B)	Open	Open	1 & 2	1 & 2	1 & 2, 3 & 4
AM486DX4	Open	Open	1 & 2	5 & 6	1 & 2, 3 & 4
80486DX4	Open	Open	1 & 2	1 & 2	1 & 2, 3 & 4
P24D	Open	Closed	1 & 2	Open	1 & 2, 3 & 4
P24T	Open	Closed	1 & 2	Open	1 & 2, 3 & 4
CXM1SC	Open	Open	1 & 2	Open	1 & 2, 3 & 4

Note: Pins designated should be in the closed position. (B) = Cyrix processor with Intel pin-out.

Continued on next page. . .

ALARIS, INC.
LEOPARD VX (TORNADO LX REV. D2)

... continued from previous page

CPU TYPE SELECTION (CON'T)		
Type	JP15	JP16
80486SX	1 & 2, 3 & 4, 5 & 6	7 & 8
80486SX2	1 & 2, 3 & 4, 5 & 6	7 & 8
80486DX	1 & 2, 3 & 4, 5 & 6	7 & 8
CX486DX2-V66	1 & 2, 3 & 4, 5 & 6	3 & 4
CX486DX2-V80	1 & 2, 3 & 4, 5 & 6, 9 & 10, 11 & 12	3 & 4
AM486DX2 (3v)	1 & 2, 3 & 4, 5 & 6, 9 & 10, 11 & 12	Open
AM486DX2 (5v)	1 & 2, 3 & 4, 5 & 6	7 & 8
80486DX2	1 & 2, 3 & 4, 5 & 6	7 & 8
CX486DX4	1 & 2, 3 & 4, 5 & 6	Open
CX486DX4 (B)	1 & 2, 3 & 4, 5 & 6	Open
AM486DX4	1 & 2, 3 & 4, 5 & 6	Open
80486DX4	1 & 2, 3 & 4, 5 & 6	Open
P24D	1 & 2, 3 & 4, 5 & 6	7 & 8
P24T	1 & 2, 3 & 4, 5 & 6	7 & 8
CXM1SC	1 & 2, 3 & 4, 5 & 6	Open

Note: Pins designated should be in the closed position. (B) = Cyrix processor with Intel pin-out.

CPU TYPE SELECTION (CON'T)	
Type	JP17
80486SX	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
80486SX2	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
80486DX	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
CX486DX2-V66	2 & 3, 5 & 6, 8 & 9, 14 & 15, 17 & 18
CX486DX2-V80	2 & 3, 5 & 6, 8 & 9, 14 & 15, 17 & 18
AM486DX2 (3v)	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
AM486DX2 (5v)	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
80486DX2	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
CX486DX4	2 & 3, 5 & 6, 8 & 9, 14 & 15, 17 & 18
CX486DX4 (B)	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
AM486DX4	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
80486DX4	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
P24D	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
P24T	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17
CXM1SC	1 & 2, 4 & 5, 7 & 8, 10 & 11 13 & 14, 16 & 17

Note: Pins designated should be in the closed position. (B) = Cyrix processor with Intel pin-out.