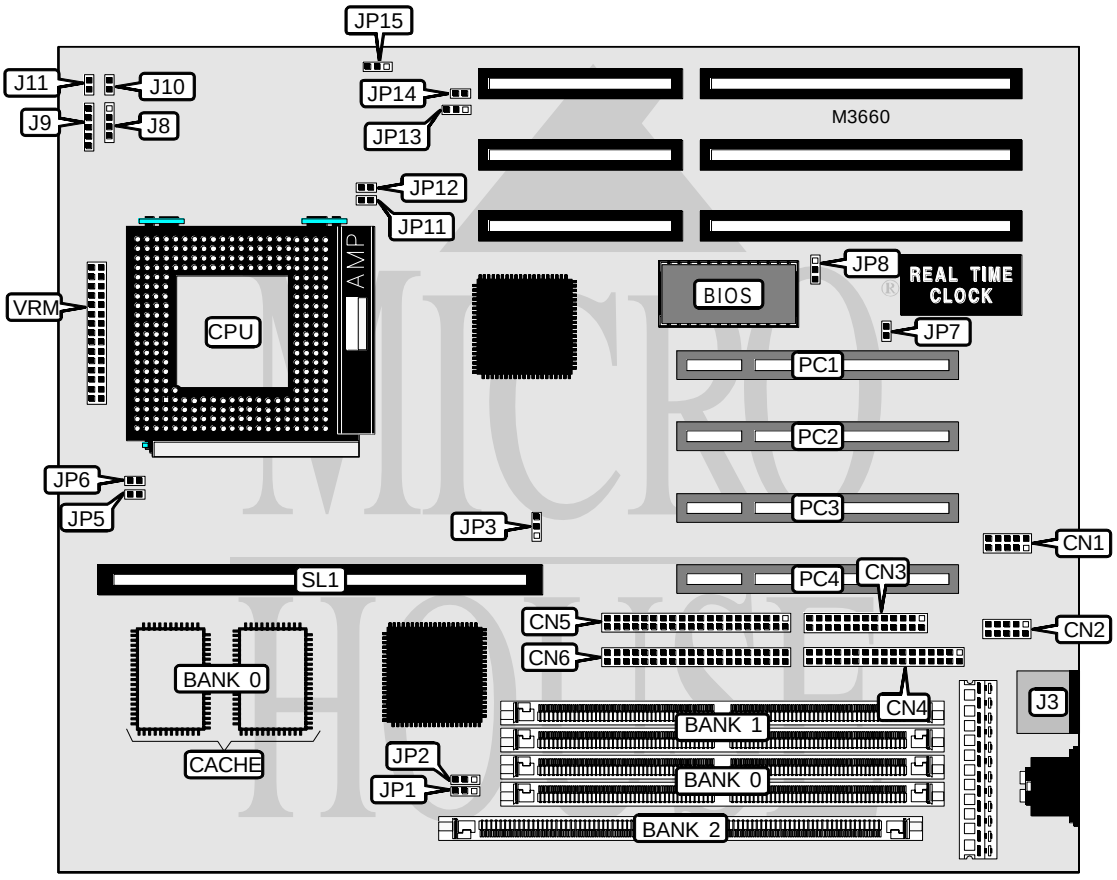
AMPTRON INTERNATIONAL, INC.
P M - 8 8 0 0

Processor	CX M1/IBM6X86/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	260mm x 220mm
I/O Options	32-bit PCI slots (4), green PC connector, floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot, IR connector, VRM connector
NPU Options	None



Continued on next page. . .

AMPTRON INTERNATIONAL, INC.
P M - 8 8 0 0

. . . continued from previous page

CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Serial port 1	CN1	Power LED & keylock	J9
Serial port 2	CN2	IDE interface LED	J10
Parallel port	CN3	Reset switch	J11
Floppy drive interface	CN4	Green PC connector	JP14
IDE interface 1	CN5	32-bit PCI slots	PC1 - PC4
IDE interface 2	CN6	Cache slot	SL1
PS/2 mouse port	J3	VRM connector	VRM
Speaker	J8		

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í CMOS memory normal operation	JP7	Open
CMOS memory clear	JP7	Closed
Flash BIOS voltage select 12v	JP8	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP8	Pins 1 & 2 closed

DRAM CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**Bank 2**
8MB	None	None	8MB
8MB	(2) 1M x 36	None	None
16MB	None	(2) 1M x 36	8MB
16MB	None	None	16MB
16MB	(2) 1M x 36	(2) 1M x 36	None
16MB	(2) 2M x 36	None	None
24MB	(2) 2M x 36	(2) 1M x 36	None
32MB	None	(2) 2M x 36	16MB
32MB	None	None	32MB
32MB	(2) 4M x 36	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None
40MB	(2) 4M x 36	(2) 1M x 36	None
48MB	(2) 4M x 36	(2) 2M x 36	None
64MB	None	(2) 4M x 36	32MB
64MB	None	None	64MB
64MB	(2) 8M x 36	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None
80MB	(2) 8M x 36	(2) 2M x 36	None
96MB	(2) 8M x 36	(2) 4M x 36	None
128MB	None	(2) 8M x 36	64MB
128MB	None	None	128MB
128MB	(2) 8M x 36	(2) 8M x 36	None
Note: Board accepts EDO memory. Bank 2 accepts only DIMM modules. The size of the DIMM module is unidentified.			

Continued on next page. . .

AMPTRON INTERNATIONAL, INC.
P M - 8 8 0 0

. . . continued from previous page

DIMM VOLTAGE CONFIGURATION		
Voltage	**JP1**	**JP2**
3.3v (SDRAM)	Pins 2 & 3 closed	Pins 2 & 3 closed
5v (EDO)	Pins 1 & 2 closed	Pins 1 & 2 closed

CACHE CONFIGURATION		
Size	**Bank 0**	**SL1**
256KB	(2) 32K x 32	Not installed
512KB	(2) 32K x 32	256KB module installed

CACHE JUMPER CONFIGURATION	
Size	**JP3**
256KB	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed

CPU SPEED SELECTION (CYRIX/IBM)							
CPU speed	**Clock speed**	**Multiplier**	**JP5**	**JP6**	**JP11**	**JP12**	**JP13**
100MHz	50MHz	2x	Closed	Closed	Closed	Open	2 & 3
120MHz	60MHz	2x	Closed	Open	Closed	Open	1 & 2
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (AMD)							
CPU speed	**Clock speed**	**Multiplier**	**JP5**	**JP6**	**JP11**	**JP12**	**JP13**
100MHz	66MHz	1.5x	Open	Closed	Open	Open	1 & 2
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	**Clock speed**	**Multiplier**	**JP5**	**JP6**	**JP11**	**JP12**	**JP13**
75MHz	50MHz	1.5x	Closed	Closed	Open	Open	2 & 3
90MHz	60MHz	1.5x	Closed	Open	Open	Open	1 & 2
100MHz	66MHz	1.5x	Open	Closed	Open	Open	1 & 2
120MHz	60MHz	2x	Closed	Open	Closed	Open	1 & 2
133MHz	66MHz	2x	Open	Closed	Closed	Open	1 & 2
150MHz	60MHz	2.5x	Closed	Open	Closed	Closed	1 & 2
166MHz	66MHz	2.5x	Open	Closed	Closed	Closed	1 & 2
180MHz	60MHz	3x	Closed	Open	Open	Closed	1 & 2
200MHz	66MHz	3x	Open	Closed	Open	Closed	1 & 2
Note: Pins designated should be in the closed position.							

CPU VOLTAGE SELECTION	
Voltage	**JP15**
3.3v	Pins 2 & 3 closed
í 3.5v	Pins 1 & 2 closed