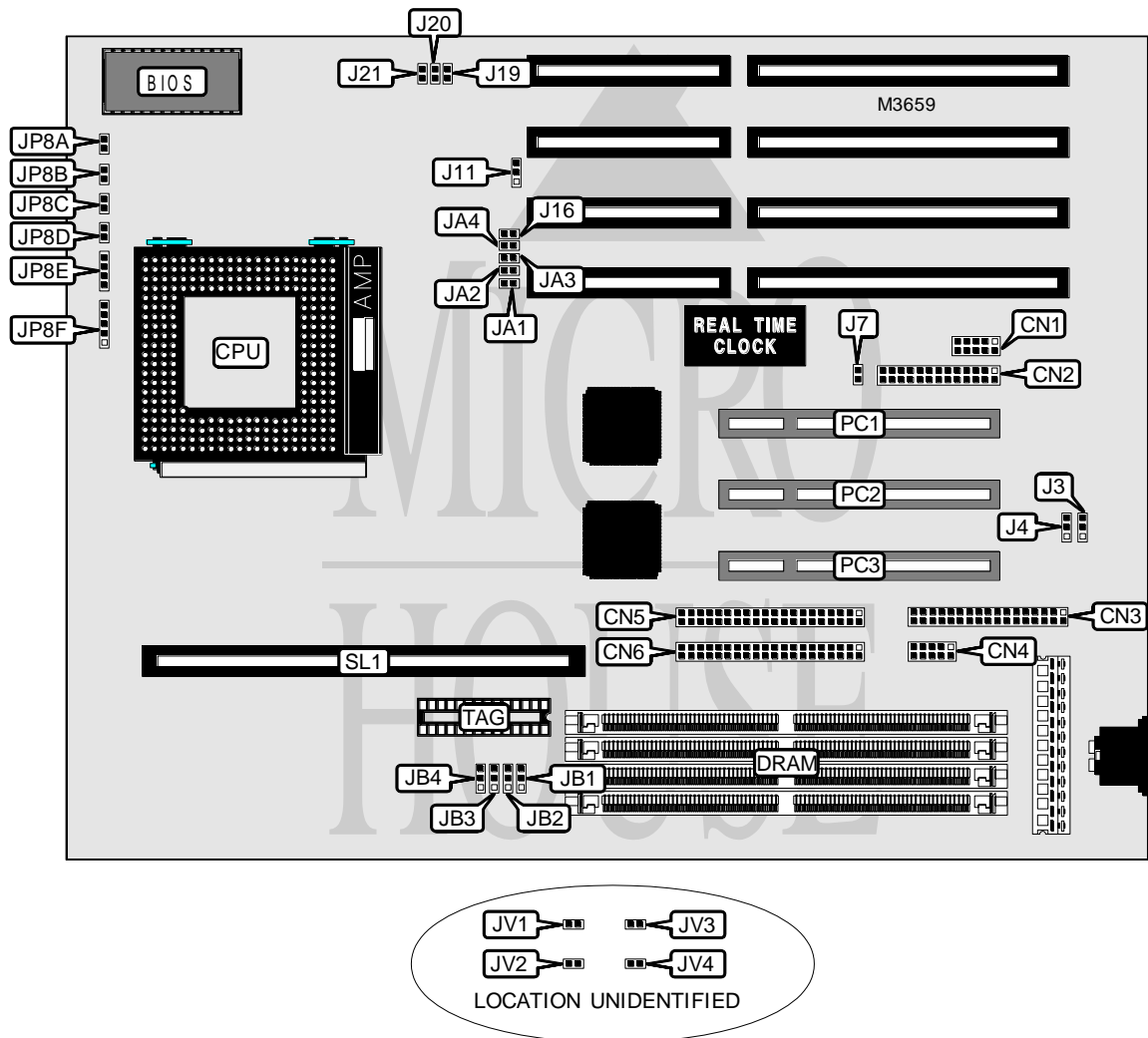


# AMPTRON INTERNATIONAL, INC.

## P M - 7 8 0 0

<b>Processor</b>	CX M1/IBM 6X86/Pentium
<b>Processor Speed</b>	75/90/100/120/133/150/166/180/200MHz
<b>Chip Set</b>	Intel
<b>Video Chip Set</b>	None
<b>Maximum Onboard Memory</b>	128MB (EDO supported)
<b>Maximum Video Memory</b>	None
<b>Cache</b>	256KB
<b>BIOS</b>	Award
<b>Dimensions</b>	254mm x 218mm
<b>I/O Options</b>	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), cache slot
<b>NPU Options</b>	None



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**AMPTRON INTERNATIONAL, INC.**  
**PM - 7800**

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Turbo LED	JP8B
Parallel port	CN2	Green PC connector	JP8C
Floppy drive interface	CN3	Reset switch	JP8D
Serial port 2	CN4	Speaker	JP8E
IDE interface 2	CN5	Power LED & keylock	JP8F
IDE interface 1	CN6	32-bit PCI slots	PC1 - PC3
IDE interface LED	JP8A	Cache slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	J3	Unidentified
í Factory configured - do not alter	J4	Unidentified
í CMOS memory normal operation	J7	Open
CMOS memory clear	J7	Closed
í Factory configured - do not alter	J11	Pins 1 & 2 closed
í Factory configured - do not alter	J16	Unidentified
í Factory configured - do not alter	JB1	Pins 2 & 3 closed
í Factory configured - do not alter	JB2	Pins 2 & 3 closed
í Factory configured - do not alter	JB3	Pins 2 & 3 closed
í Factory configured - do not alter	JB4	Pins 2 & 3 closed
í Factory configured - do not alter	JV1	Open
í Factory configured - do not alter	JV2	Open
í Factory configured - do not alter	JV3	Closed
í Factory configured - do not alter	JV4	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	None
4MB	(2) 512K x 36	None
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	None
10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 2M x 36	(2) 256K x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
36MB	(2) 512K x 36	(2) 4M x 36

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# AMPTRON INTERNATIONAL, INC.

## P M - 7 8 0 0

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 8M x 36	(2) 256K x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None

Note: Board accepts EDO memory. Banks are interchangeable. Board also accepts x 32 SIMMs. The location of banks 0 & 1 are unidentified.

CACHE CONFIGURATION		
Size	SL1	TAG
256KB	256KB module installed	Unidentified

CPU SPEED SELECTION (CYRIX/IBM)						
CPU speed	Clock speed	Multiplier	JA1	JA2	JA3	JA4
100MHz	50MHz	2x	Closed	Closed	Closed	Open
120MHz	60MHz	2x	Open	Closed	Closed	Open

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JA1	JA2	JA3	JA4
75MHz	50MHz	1.5x	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	Open	Closed	Open	Open
100MHz	66MHz	1.5x	Closed	Open	Open	Open
120MHz	60MHz	2x	Open	Closed	Closed	Open
133MHz	66MHz	2x	Closed	Open	Closed	Open
150MHz	60MHz	2.5x	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	Closed	Open	Closed	Closed
180MHz	60MHz	3x	Open	Closed	Open	Closed
200MHz	66MHz	3x	Closed	Open	Open	Closed

CPU VOLTAGE SELECTION			
Voltage	J19	J20	J21
3.35v	Closed	Open	Open
3.46v	Open	Closed	Open
3.6v	Open	Open	Closed