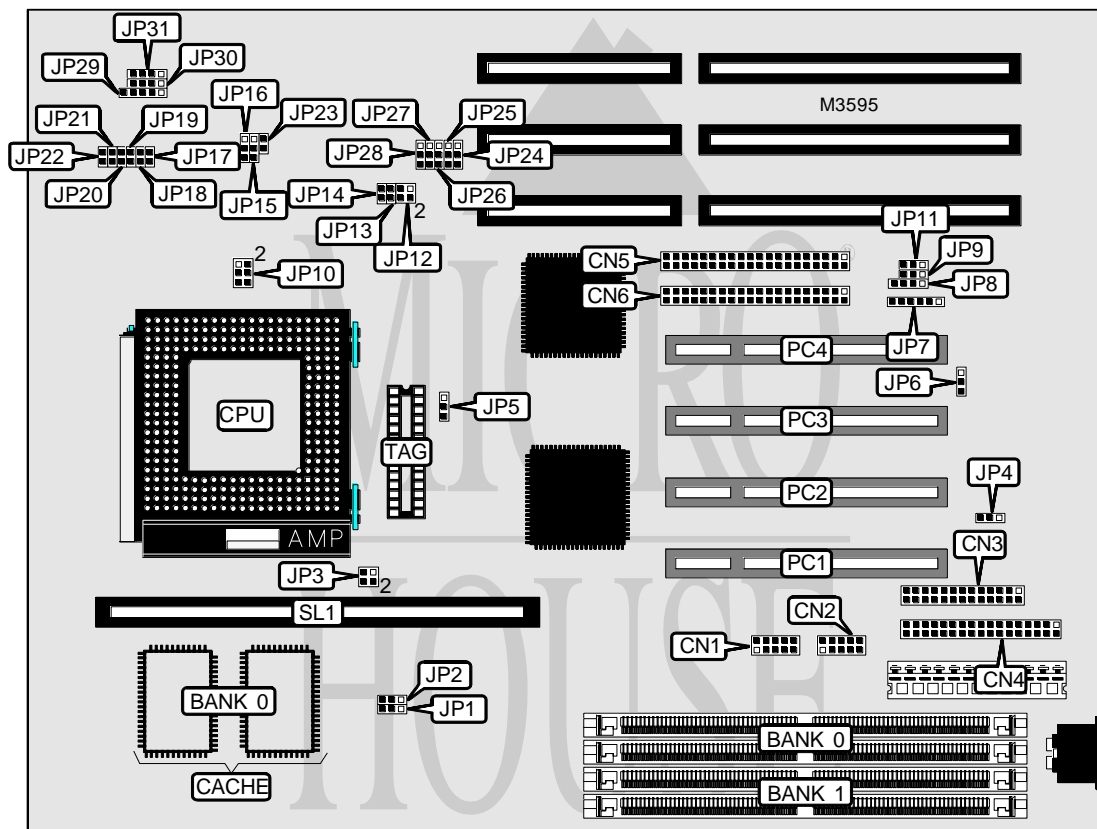


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PT-5001

Processor	CX M1/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	250mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), cache slot, IR connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Reset switch	JP19
Serial port 2	CN2	Turbo switch	JP20
Parallel port	CN3	Turbo LED	JP21
Floppy drive interface	CN4	IDE interface LED	JP22
IDE interface 2	CN5	Power LED & keylock	JP29
IDE interface 1	CN6	Speaker	JP30
PS/2 mouse interface	JP7	IR connector	JP31
External battery	JP8	32-bit PCI slots	PC1 - PC4
Green PC connector	JP18	Cache slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	JP4	Unidentified
í Factory configured - do not alter	JP6	Unidentified
í CMOS memory normal operation	JP8	Pins 2 & 3 closed
CMOS memory clear	JP8	Pins 3 & 4 closed
Battery type select external	JP8	Closed
í Factory configured - do not alter	JP9	Unidentified
í Factory configured - do not alter	JP11	Unidentified
Dallas 1287A clock normal operation	JP17	Open
Dallas 1287A clock clear	JP17	Closed
IDE interface 2 IRQ15 enabled	JP25	Pins 1 & 2 closed
IDE interface 2 IRQ15 disabled	JP25	Pins 2 & 3 closed
IDE interface 1 IRQ14 enabled	JP26	Pins 1 & 2 closed
IDE interface 1 IRQ14 disabled	JP26	Pins 2 & 3 closed
Flash BIOS write protect enabled	JP27	Pins 1 & 2 closed
Flash BIOS write protect disabled	JP27	Pins 2 & 3 closed
Flash BIOS voltage select 12v	JP28	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP28	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	None
24MB	(2) 1M x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
40MB	(2) 1M x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 4M x 36

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
72MB	(2) 1M x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 2M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 4M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs.

CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB	(2) 32K x 32	Unidentified	Not installed
512KB (A)	(2) 32K x 32	Unidentified	256KB module installed
512KB (B)	(2) 64K x 32	Unidentified	Not installed

Note: If cache module has onboard TAG, remove TAG from TAG socket. If cache module does not have onboard TAG, install TAG at TAG socket. The size of the TAG is unidentified.

CACHE JUMPER CONFIGURATION			
Size	JP1	JP2	JP5
256KB	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB (A/B)	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU SPEED SELECTION (CYRIX)				
CPU speed	JP12	JP13	JP14	JP24
120MHz	1 & 2, 3 & 4	Closed	Open	1 & 2
150MHz	1 & 2	Closed	Open	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JP12	JP13	JP14	JP24
75MHz	50MHz	1.5x	1 & 2, 3 & 4	Open	Open	1 & 2
90MHz	60MHz	1.5x	1 & 2	Open	Open	2 & 3
100MHz	66MHz	1.5x	3 & 4	Open	Open	2 & 3
120MHz	60MHz	2x	1 & 2	Closed	Open	2 & 3
133MHz	66MHz	2x	3 & 4	Closed	Open	2 & 3
150MHz	60MHz	2.5x	1 & 2	Closed	Closed	2 & 3
166MHz	66MHz	2.5x	3 & 4	Closed	Closed	2 & 3
180MHz	60MHz	3x	1 & 2	Open	Closed	2 & 3
200MHz	66MHz	3x	3 & 4	Open	Closed	2 & 3

Note: Pins designated should be in the closed position.

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CPU VOLTAGE SELECTION					
Voltage	JP3	JP10	JP15	JP16	JP23
3.3v	1 & 3, 2 & 4	1 & 2, 3 & 4, 5 & 6	1 & 2	1 & 2	Open
3.45v	1 & 3, 2 & 4	1 & 2, 3 & 4, 5 & 6	2 & 3	2 & 3	Open
Dual 2.5v/3.3v	Open	Open	Open	1 & 2	Closed
Dual 2.5v/3.45v	Open	Open	Open	2 & 3	Closed

Note: Pins designated should be in the closed position.