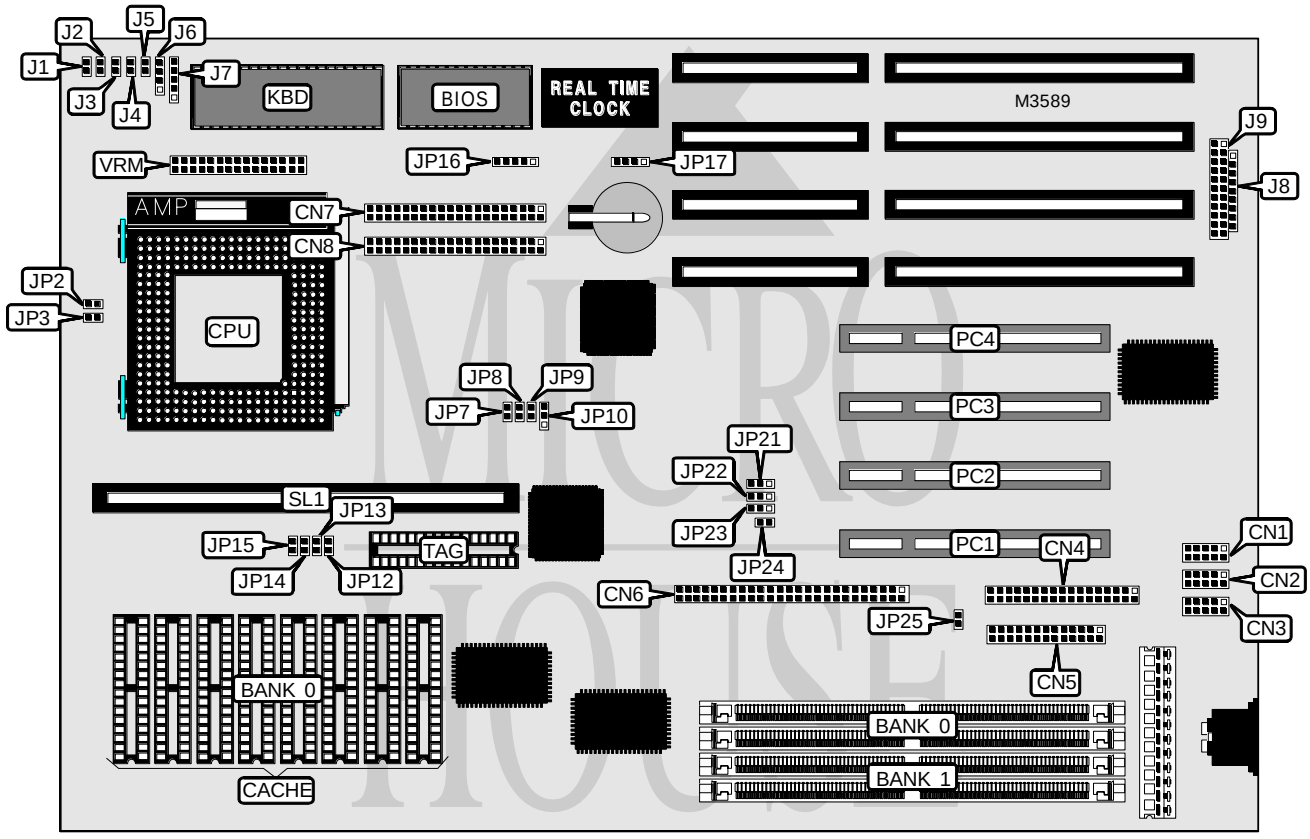
J-BOND COMPUTER SYSTEMS CORPORATION
P C I 5 0 0 C - E

Processor	Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	330mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, IDE interfaces (2), SCSI connector, parallel port, serial ports (2), cache slot, VRM connector
NPU Options	None



Continued on next page. . .

Copyright © 1995, 1996, **Micro House International** (303) 443-3388. All rights reserved. Including the right of reproduction in any form.

J-BOND COMPUTER SYSTEMS CORPORATION
P C I 5 0 0 C - E

. . . continued from previous page

CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Serial port 1	CN2	IDE interface LED	J3
Serial port 2	CN3	Turbo switch	J4
Floppy drive interface	CN4	Turbo LED	J5
Parallel port	CN5	Speaker	J6
SCSI interface	CN6	Power LED & keylock	J7
IDE interface	CN7	External battery	JP17
IDE interface	CN8	32-bit PCI slots	PC1 - PC4
Reset switch	J1	Cache slot	SL1
SCSI LED	J2	VRM connector	VRM

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	CN1	Unidentified
í Factory configured - do not alter	J8	Unidentified
í Factory configured - do not alter	J9	Unidentified
í Factory configured - do not alter	JP16	Pins 2 & 3, 5 & 6 closed
í CMOS memory normal operation	JP17	Pins 1 & 2 closed
CMOS memory clear	JP17	Pins 2 & 3 closed
Battery type select external	JP17	Closed
í Factory configured - do not alter	JP21	Unidentified
í Factory configured - do not alter	JP22	Unidentified
í Factory configured - do not alter	JP23	Unidentified
í Factory configured - do not alter	JP24	Unidentified
í SCSI terminator power source select from system board	JP25	Closed
SCSI terminator power source select from peripheral	JP25	Open

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
4MB	(2) 512K x 32	None
8MB	(2) 1M x 32	None
8MB	(2) 512K x 32	(2) 512K x 32
12MB	(2) 1M x 32	(2) 512K x 32
12MB	(2) 512K x 32	(2) 1M x 32
16MB	(2) 2M x 32	None
16MB	(2) 1M x 32	(2) 1M x 32
20MB	(2) 2M x 32	(2) 512K x 32
20MB	(2) 512K x 32	(2) 2M x 32
24MB	(2) 2M x 32	(2) 1M x 32
24MB	(2) 1M x 32	(2) 2M x 32
32MB	(2) 4M x 32	None
32MB	(2) 2M x 32	(2) 2M x 32
36MB	(2) 4M x 32	(2) 512K x 32

Continued on next page. . .

Copyright © 1995, 1996, **Micro House International** (303) 443-3388. All rights reserved. Including the right of reproduction in any form.

J-BOND COMPUTER SYSTEMS CORPORATION
P C I 5 0 0 C - E

. . . continued from previous page

DRAM CONFIGURATION (CON'T)		
Size	**Bank 0**	**Bank 1**
36MB	(2) 512K x 32	(2) 4M x 32
40MB	(2) 4M x 32	(2) 1M x 32
40MB	(2) 1M x 32	(2) 4M x 32
48MB	(2) 4M x 32	(2) 2M x 32
48MB	(2) 2M x 32	(2) 4M x 32
64MB	(2) 8M x 32	None
64MB	(2) 4M x 32	(2) 4M x 32
68MB	(2) 512K x 32	(2) 8M x 32
72MB	(2) 8M x 32	(2) 1M x 32
72MB	(2) 1M x 32	(2) 8M x 32
80MB	(2) 8M x 32	(2) 2M x 32
80MB	(2) 2M x 32	(2) 8M x 32
96MB	(2) 8M x 32	(2) 4M x 32
96MB	(2) 4M x 32	(2) 8M x 32
128MB	(2) 16M x 32	None
128MB	(2) 8M x 32	(2) 8M x 32
Note: Board accepts EDO memory.		

CACHE CONFIGURATION			
Size	**Bank 0**	**TAG**	**SL1**
256KB (A)	(8) 32K x 8	(1) 8K x 8	Not installed
256KB (B)	None	None	256KB module installed
512KB (A)	(8) 64K x 8	(1) 16K x 8	Not installed
512KB (B)	None	None	512KB module installed

CACHE JUMPER CONFIGURATION				
Size	**JP12**	**JP13**	**JP14**	**JP15**
256KB (A)	Closed	Closed	Open	Open
512KB (A)	Closed	Open	Closed	Closed

CPU SPEED SELECTION (MX8325)								
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP7**	**JP8**	**JP9**	**JP10**
75MHz	50MHz	1.5x	Open	Open	Open	Closed	Closed	1 & 2
90MHz	60MHz	1.5x	Open	Open	Closed	Open	Closed	2 & 3
100MHz	66MHz	1.5x	Open	Open	Closed	Closed	Closed	2 & 3
100MHz	50MHz	2x	Closed	Open	Open	Closed	Closed	1 & 2
120MHz	60MHz	2x	Closed	Open	Closed	Open	Closed	2 & 3
133MHz	66MHz	2x	Closed	Open	Closed	Closed	Closed	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	Closed	Open	Closed	2 & 3
166MHz	66MHz	2.5x	Closed	Closed	Closed	Closed	Closed	2 & 3
180MHz	60MHz	3x	Open	Closed	Closed	Open	Closed	2 & 3
200MHz	66MHz	3x	Open	Closed	Closed	Closed	Closed	2 & 3
Note: Pins designated should be in the closed position.								

Continued on next page. . .

Copyright © 1995, 1996, **Micro House International** (303) 443-3388. All rights reserved. Including the right of reproduction in any form.

J-BOND COMPUTER SYSTEMS CORPORATION
P C I 5 0 0 C - E

. . . continued from previous page

CPU SPEED SELECTION (IMI464)								
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP7**	**JP8**	**JP9**	**JP10**
75MHz	50MHz	1.5x	Open	Open	Open	Open	Closed	1 & 2
90MHz	60MHz	1.5x	Open	Open	Closed	Open	Closed	2 & 3
100MHz	66MHz	1.5x	Open	Open	Closed	Closed	Closed	2 & 3
100MHz	50MHz	2x	Closed	Open	Open	Open	Closed	1 & 2
120MHz	60MHz	2x	Closed	Open	Closed	Open	Closed	2 & 3
133MHz	66MHz	2x	Closed	Open	Closed	Closed	Closed	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	Closed	Open	Closed	2 & 3
166MHz	66MHz	2.5x	Closed	Closed	Closed	Closed	Closed	2 & 3
180MHz	60MHz	3x	Open	Closed	Closed	Open	Closed	2 & 3
200MHz	66MHz	3x	Open	Closed	Closed	Closed	Closed	2 & 3
Note: Pins designated should be in the closed position.								

CPU SPEED SELECTION (IMI484)								
CPU speed	**Clock speed**	**Multiplier**	**JP2**	**JP3**	**JP7**	**JP8**	**JP9**	**JP10**
75MHz	50MHz	1.5x	Open	Open	Closed	Open	Open	1 & 2
90MHz	60MHz	1.5x	Open	Open	Closed	Open	Closed	2 & 3
100MHz	66MHz	1.5x	Open	Open	Closed	Closed	Closed	2 & 3
100MHz	50MHz	2x	Closed	Open	Closed	Open	Open	1 & 2
120MHz	60MHz	2x	Closed	Open	Closed	Open	Closed	2 & 3
133MHz	66MHz	2x	Closed	Open	Closed	Closed	Closed	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	Closed	Open	Closed	2 & 3
166MHz	66MHz	2.5x	Closed	Closed	Closed	Closed	Closed	2 & 3
180MHz	60MHz	3x	Open	Closed	Closed	Open	Closed	2 & 3
200MHz	66MHz	3x	Open	Closed	Closed	Closed	Closed	2 & 3
Note: Pins designated should be in the closed position.								

Copyright © 1995, 1996, **Micro House International** (303) 443-3388. All rights reserved. Including the right of reproduction in any form.