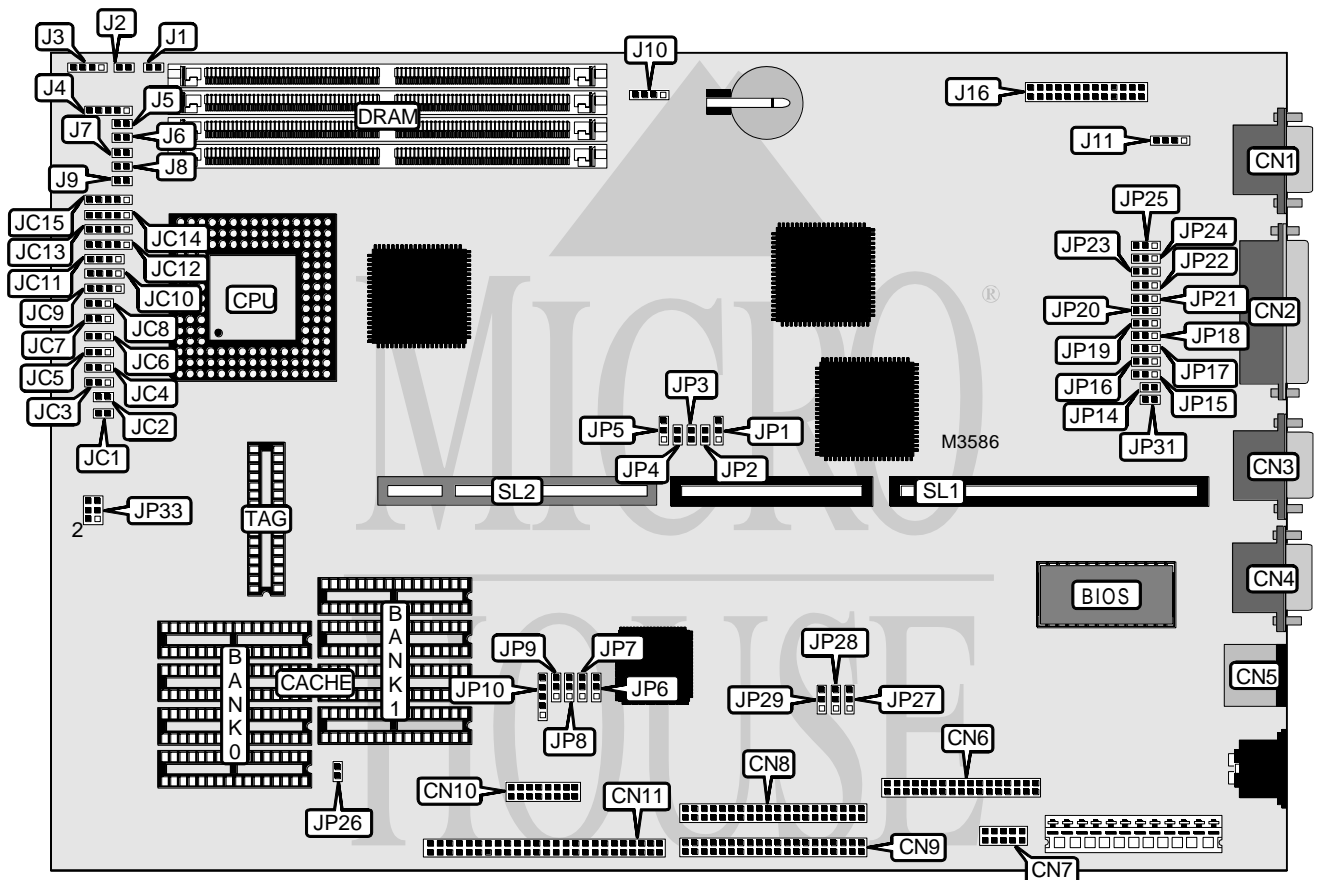


J-BOND COMPUTER SYSTEMS CORPORATION

PCI400C - D

Processor	UMCU5S/UMCU5SD/SL80486SX/SL80486SX2/SL80486DX/ SL80486DX2/CX486DX2/AM486DX2/AM486DXL2/SL80486DX2/ SL80486DX2(ODP)/CX486DX4/AM486DX4/(SL)AM486DX4(WB)/ SL80486DX4/P24T/ CX5X86
Processor Speed	25/33/40/50(internal)/66(internal)/75(internal)/100(internal)MHz
Chip Set	SIS
Video Chip Set	S3
Maximum Onboard Memory	128MB
Maximum Video Memory	Unidentified
Cache	128/256/512KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slot, Ethernet 10Base2 connector, Ethernet 10BaseT connector, floppy drive interface, game port, green PC connector, IDE interfaces (2), SCSI connector, parallel port, PS/2 mouse port, serial ports (2), VGA port, riser slot
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	CN1	Speaker	J3
Parallel port	CN2	Power LED & keylock	J4
Serial port 2	CN3	Reset switch	J5
Serial port 1	CN4	Green PC connector	J6
PS/2 mouse port	CN5	Turbo LED	J7
Floppy drive interface	CN6	Turbo switch	J8
Ethernet 10BaseT connector	CN7	Green PC LED	J9
IDE interface 2	CN8	External battery	J10
IDE interface 1	CN9	Chassis fan power	J11
Ethernet 10Base2 connector	CN10	Game interface	J16
SCSI interface	CN11	Green PC connector (for I/O)	JP5
IDE interface LED	J1	Riser slot	SL1
SCSI LED	J2	32-bit PCI slot	SL2

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	J10	Pins 2 & 3 closed
CMOS memory clear	J10	Pins 3 & 4 closed
Battery type select external	J10	Closed
í Factory configured - do not alter	JC1	Open
í Factory configured - do not alter	JC2	Open
í Green PC select STP.CLK	JP1	Pins 1 & 2 closed
Green PC select SMI OUT	JP1	Pins 2 & 3 closed
í Game port enabled	JP14	Closed
Game port disabled	JP14	Open
í IDE interface enabled	JP15	Pins 1 & 2 closed
IDE interface disabled	JP15	Pins 2 & 3 closed
í Parallel port IRQ select IRQ7	JP16	Pins 1 & 2 closed
Parallel port IRQ select IRQ5	JP16	Pins 2 & 3 closed
SCSI terminator power source select from system board	JP26	Closed
SCSI terminator power source select from peripheral	JP26	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	None	None	None
2MB	(1) 512K x 36	None	None	None
2MB	(1) 256K x 36	(1) 256K x 36	None	None
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	None
4MB	(1) 1M x 36	None	None	None
4MB	(1) 512K x 36	(1) 256K x 36	(1) 256K x 36	None
4MB	(1) 512K x 36	(1) 512K x 36	None	None
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36

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DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
5MB	(1) 1M x 36	(1) 256K x 36	None	None
5MB	(1) 256K x 36	(1) 512K x 36	(1) 512K x 36	None
5MB	(1) 256K x 36	(1) 1M x 36	None	None
6MB	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36	None
7MB	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
8MB	(1) 1M x 36	(1) 512K x 36	(1) 512K x 36	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
9MB	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	None
9MB	(1) 256K x 36	(1) 2M x 36	None	None
10MB	(1) 2M x 36	(1) 512K x 36	None	None
11MB	(1) 2M x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
12MB	(1) 2M x 36	(1) 512K x 36	(1) 512K x 36	None
13MB	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
14MB	(1) 2M x 36	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36
14MB	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None	None	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
17MB	(1) 4M x 36	(1) 256K x 36	None	None
18MB	(1) 4M x 36	(1) 256K x 36	(1) 256K x 36	None
20MB	(1) 4M x 36	(1) 1M x 36	None	None
28MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
32MB	(1) 8M x 36	None	None	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
33MB	(1) 256K x 36	(1) 8M x 36	None	None
34MB	(1) 8M x 36	(1) 256K x 36	(1) 256K x 36	None
34MB	(1) 8M x 36	(1) 512K x 36	None	None
36MB	(1) 8M x 36	(1) 512K x 36	(1) 512K x 36	None
40MB	(1) 4M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
44MB	(1) 8M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
48MB	(1) 8M x 36	(1) 2M x 36	(1) 2M x 36	None
64MB	(1) 8M x 36	(1) 8M x 36	None	None
72MB	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36	None
80MB	(1) 8M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
80MB	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36	None
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	None
97MB	(1) 256K x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
98MB	(1) 512K x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
100MB	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
104MB	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
112MB	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 16M x 36	(1) 16M x 36	None	None

Note: The location of the banks are unidentified. Any SIMM can be installed in any slot.

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8
256KB (B)	(4) 64K x 8	None	(1) 16K x 8
512KB (A)	(4) 128K x 8	None	(1) 32K x 8
512KB (B)	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	JP6	JP7	JP8	JP9	JP10
128KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2, 3 & 4
256KB (A)	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3, 4 & 5
256KB (B)	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2, 3 & 4
512KB (A)	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2, 3 & 4
512KB (B)	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3, 4 & 5

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION			
Speed	JP2	JP3	JP4
25MHz	Open	Open	Open
33MHz	Open	Closed	Closed
40MHz	Open	Closed	Open
50iMHz	Open	Open	Open
66iMHz	Open	Closed	Closed
75iMHz	Open	Open	Open
100iMHz	Open	Closed	Closed

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CPU TYPE SELECTION					
Type	JC3	JC4	JC5	JC6	JC7
UMC U5S (5v)	Open	1 & 2	2 & 3	Open	Open
UMC U5SD (5v)	Open	1 & 2	2 & 3	Open	Open
SL80486SX (5v)	Open	1 & 2	2 & 3	Open	Open
SL80486SX2 (5v)	Open	1 & 2	2 & 3	Open	Open
SL80486DX (5v)	Open	1 & 2	2 & 3	Open	Open
CX486DX2 (5v)	Open	1 & 2	2 & 3	2 & 3	Open
AM486DX2	Open	Open	2 & 3	Open	Open
AM486DXL2 (3.3v)	Open	1 & 2	2 & 3	Open	2 & 3
SL80486DX2 (5v)	Open	1 & 2	2 & 3	Open	Open
SL80486DX2(ODP)	Open	1 & 2	2 & 3	Open	Open
CX486DX4 (3.3v)	Open	1 & 2	2 & 3	2 & 3	Open
AM486DX4 (3v)	Open	Open	2 & 3	Open	Open
(SL)AM486DX4(WB)	Open	1 & 2	2 & 3	1 & 2	Open
SL80486DX4	Open	1 & 2	2 & 3	Open	Open
SL80486DX4(3v)	Open	1 & 2	2 & 3	Open	Open
P24T	1 & 2	1 & 2	2 & 3	Open	Open
CX5X86 (3v)	Open	1 & 2	2 & 3	1 & 2	Open

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION (CON'T)				
Type	JC8	JC9	JC10	JC11
UMC U5S (5v)	Open	Open	3 & 4	2 & 3
UMC U5SD (5v)	Open	Open	3 & 4	3 & 4
SL80486SX (5v)	Open	3 & 4	Open	2 & 3
SL80486SX2 (5v)	Open	3 & 4	Open	2 & 3
SL80486DX (5v)	Open	3 & 4	Open	1 & 2, 3 & 4
CX486DX2 (5v)	Open	2 & 3	2 & 3	1 & 2, 3 & 4
AM486DX2	Open	Open	Open	1 & 2, 3 & 4
AM486DXL2 (3.3v)	Open	Open	3 & 4	1 & 2, 3 & 4
SL80486DX2 (5v)	Open	3 & 4	Open	1 & 2, 3 & 4
SL80486DX2(ODP)	Open	3 & 4	Open	1 & 2, 3 & 4
CX486DX4 (3.3v)	Open	2 & 3	2 & 3	1 & 2, 3 & 4
AM486DX4 (3v)	Open	Open	Open	1 & 2, 3 & 4
(SL)AM486DX4(WB)	Open	1 & 2, 3 & 4	Open	1 & 2, 3 & 4
SL80486DX4	Open	3 & 4	Open	1 & 2, 3 & 4
SL80486DX4(3v)	Open	3 & 4	Open	1 & 2, 3 & 4
P24T	Open	3 & 4	Open	1 & 2, 3 & 4
CX5X86 (3v)	Open	1 & 2, 3 & 4	Open	1 & 2, 3 & 4

Note: Pins designated should be in the closed position.

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CPU TYPE SELECTION (CON'T)				
Type	JC12	JC13	JC14	JC15
UMC U5S (5v)	1 & 2	Open	Open	Open
UMC U5SD (5v)	1 & 2	Open	Open	Open
SL80486SX (5v)	3 & 4	Open	2 & 3	4 & 5
SL80486SX2 (5v)	3 & 4	Open	2 & 3	4 & 5
SL80486DX (5v)	3 & 4	Open	2 & 3	4 & 5
CX486DX2 (5v)	3 & 4	Open	1 & 2	2 & 3
AM486DX2	3 & 4	Open	Open	Open
AM486DXL2 (3.3v)	1 & 2, 3 & 4	Open	4 & 5	Open
SL80486DX2 (5v)	3 & 4	Open	2 & 3	4 & 5
SL80486DX2(ODP)	3 & 4	Open	2 & 3	4 & 5
CX486DX4 (3.3v)	3 & 4	Open	1 & 2	2 & 3
AM486DX4 (3v)	3 & 4	Open	Open	Open
(SL)AM486DX4(WB)	3 & 4	Open	2 & 3	4 & 5
SL80486DX4	2 & 3	4 & 5	2 & 3	1 & 2
SL80486DX4(3v)	3 & 4	Open	2 & 3	4 & 5
P24T	2 & 3	Open	2 & 3	1 & 2
CX5X86 (3v)	3 & 4	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION	
Voltage	JP33
3.3v	Open
5v	Pins 1 & 2, 3 & 4, 5 & 6 closed

DMA CHANNEL SELECTION		
Channel	JP20	JP21
1	Pins 1 & 2 closed	Pins 1 & 2 closed
í 3	Pins 2 & 3 closed	Pins 2 & 3 closed

PARALLEL PORT ADDRESS SELECTION				
Setting	JP17	JP18	JP19	JP31
í EPP mode	Open	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
LPT1 (378h)	Any setting	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
ECP/EPP	Open	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
EXT, 2 FDD	Open	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

SERIAL PORT 1 ADDRESS SELECTION		
Setting	JP22	JP25
COM1 (3F8h)	Pins 1 & 2 closed	Pins 2 & 3 closed
COM3 (3E8h)	Pins 2 & 3 closed	Pins 1 & 2 closed
COM4 (2E8h)	Pins 1 & 2 closed	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed

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SERIAL PORT 2 ADDRESS SELECTION		
Setting	JP23	JP24
COM2 (2F8h)	Pins 1 & 2 closed	Pins 2 & 3 closed
COM3 (3E8h)	Pins 1 & 2 closed	Pins 1 & 2 closed
COM4 (2E8h)	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed

SCSI/LAN INTERRUPT SELECTION			
Setting	JP27	JP28	JP29
On board LAN INT C	2 & 3	1 & 2	Any setting
On board LAN INT B	2 & 3	2 & 3	Any setting
On board SCSI INT C	2 & 3	1 & 2	Any setting
On board SCSI INT B	2 & 3	2 & 3	Any setting
Both LAN/SCSI used (LAN INT C, SCSI INT B)	2 & 3	2 & 3	2 & 3
Both LAN/SCSI used (LAN INT B, SCSI INT C)	2 & 3	1 & 2	1 & 2
No LAN/SCSI	1 & 2	Any setting	Any setting

Note: Pins designated should be in the closed position.