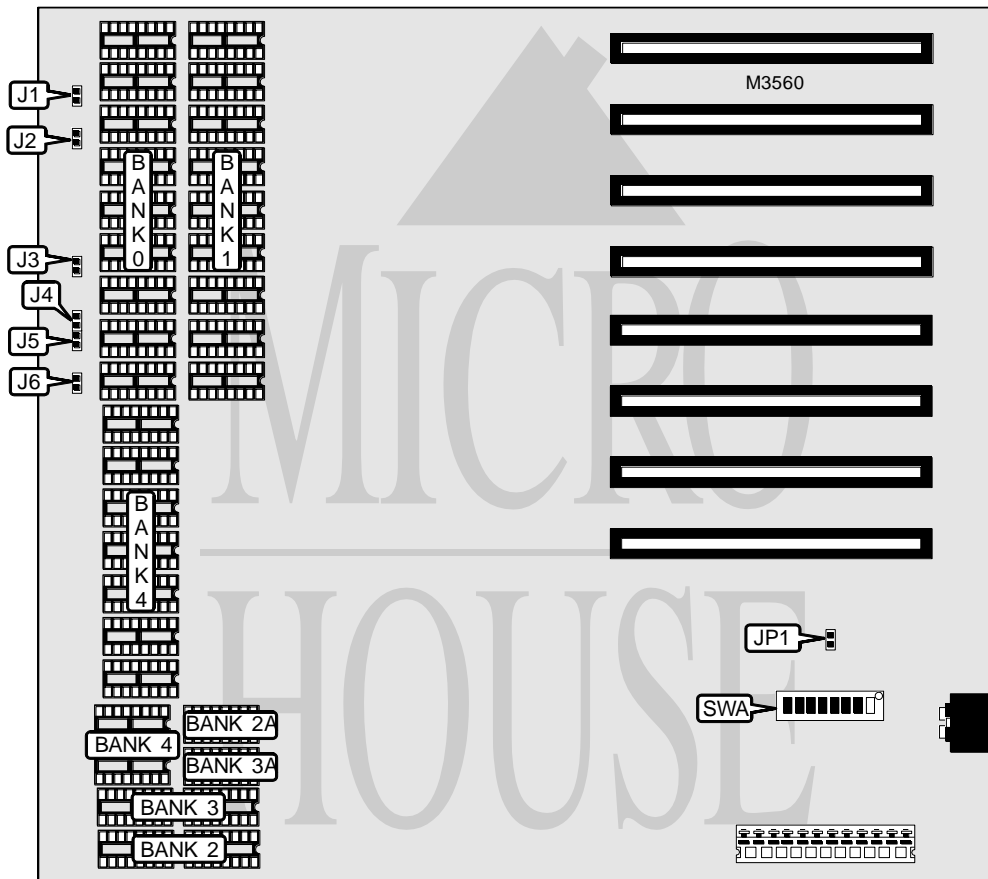


UNIDENTIFIED

B 1 5 8

Processor	V 20 (exact location unidentified)
Processor Speed	4MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	1MB
Maximum Video Memory	None
Cache	None
BIOS	NEL
Dimensions	254mm x 218mm
I/O Options	None
NPU Options	8087 (exact location unidentified)



CONNECTIONS			
Purpose	Location	Purpose	Location
Keylock	J1	Turbo LED	J4
Speaker	J2	Turbo switch	J5
Power LED	J3	Reset switch	J6

Continued on next page. . .

UNIDENTIFIED

B 1 5 8

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
DRAM parity check disabled	SWA/1	On
DRAM parity check enabled	SWA/1	Off
í Factory configured - do not alter	SWA/2	On

DRAM CONFIGURATION							
Size	Bank 0	Bank 1	Bank 2	Bank 2A	Bank 3	Bank 3A	Bank 4
512KB	(9) 41256	(9) 41256	None	None	None	None	None
640KB	(9) 41256	(9) 41256	(2) 4464	(1) 4164	(2) 4464	(1) 4164	None
1MB	None	None	None	None	None	None	(9) 411000

DRAM JUMPER/SWITCH CONFIGURATION			
Size	JP1	SWA/3	SWA/4
None	Closed	On	On
512KB	Closed	Off	On
640KB	Closed	On	Off
1MB	Open	Off	Off

MONITOR SELECTION		
Setting	SWA/5	SWA/6
None	On	On
Monochrome	Off	Off
Color 40 x 25	Off	On
Color 80 x 25	On	Off

FLOPPY DRIVE SELECTION		
Drives installed	SWA/7	SWA/8
1	On	On
2	Off	On
3	On	Off
4	Off	Off